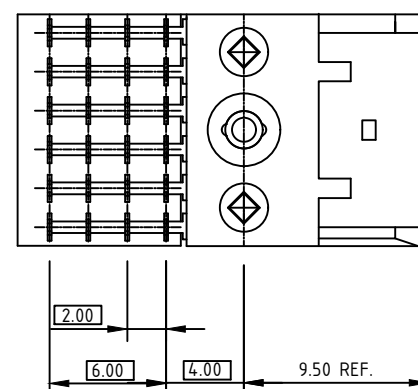
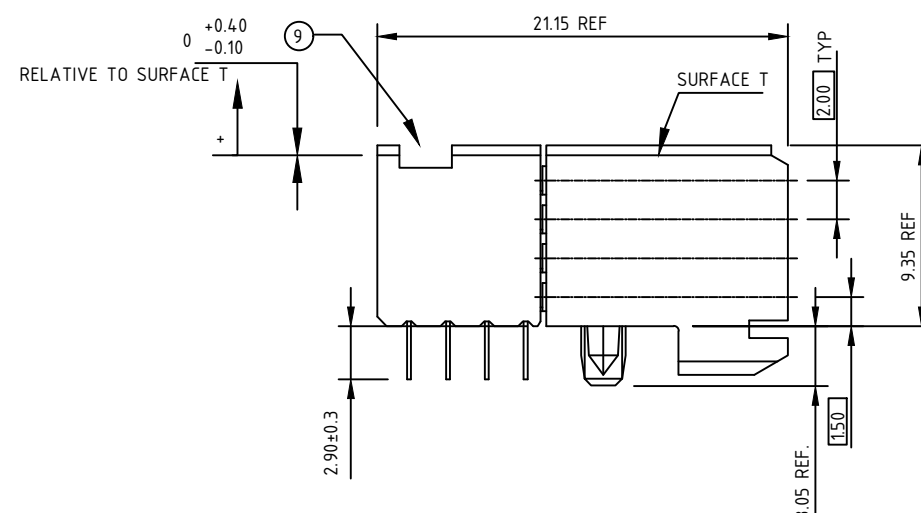


PRODUCT NUMBER
85928-XY YLF

- X DENOTES PLATING CODE



NOTES :

1. EXTENSIONS CAN BE ADDED TO THE PART NUMBER LOGIC
IF LUBRICATED CONTACTS ARE REQUIRED 
e.g. 85928-XYYLLF L DENOTES LUBRICATED
2. PLATING AT TERMINAL AREA : Sn MATT FINISH PLATING (LF)
3. PLATING ON CONTACT AREA MEETS THE PERFORMANCE LEVELS SHOWN IN TABLE ON THIS SHEET .
PRECIOUS METAL PLATING MAY BE EITHER GOLD OR PALLADIUM NICKEL
4. SPECIAL PLATING CODE IS GOLD OVER NICKEL PLATING FOR INCREASED RESISTANCE TO FRETTING CORROSION
UNDER HIGH VIBRATION APPLICATION.
5. PLAING CODE - 2L : GXT PLUS - ADDITIONAL HIGH PERFORMANCE LUBE OVER GXT PLATING.
6. CONNECTOR MATERIALS:
HOUSING : THERMOPLASTIC UL 94-V0
CONTACT : COPPER ALLOY
7. PRODUCT SPECIFICATION : GS-12-002
8. APPLICATION SPECIFICATIONS : BUS-20-061, GS-20-001 & TA-941
9. TOP SURFACE OF PRESS BLOCK MAY EXTEND UP TO 0.4MM HIGHER THAN HOUSING. THIS MAY AFFECT THE
TAIL LENGTH BEFORE APPLICATION TO A BOARD.
10. THESE PRODUCTS WHERE THE PART NUMBER ENDS IN "LF" MEET THE EUROPEAN UNION DIRECTIVES AND
OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-47-0004.
11. THIS PRODUCTS WILL WITHSTAND EXPOSURE TO 240°C FOR 60 SECONDS IN A CONVECTION,
INFAR-RED OR VAPOR PHASE REFLOW OVEN.
12. FOR RECOMMENDED PTH DIMENSIONS REFER DRAWING NO : 58351.
13.  SYMBOL WILL BE NEXT TO ANY DIMENSION, VIEW OR NOTE WHICH HAS BEEN MODIFIED WITH CURRENT DRAWING REVISION. 

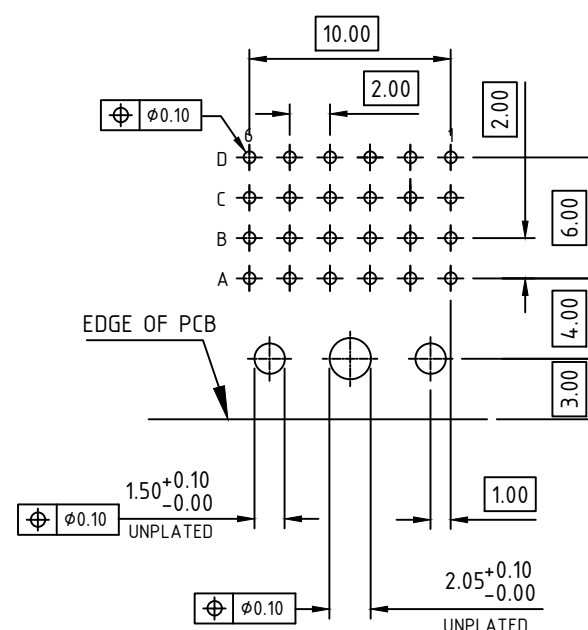
PLATING CODE	PERFORMANCE LEVEL
85928-1YYLF	IEC PERFORMANCE LEVEL 1 (250 Mating cycles) /
85928-2LYLF (NOTE-5)	TELCORDIA CO) (200 MATING CYCLE)
85928-3YYLF (NOTE-4)	TELCORDIA UE (HIGH PERFORMANCE FOR AEROSPACE APPLICATION)
85928-4YYLF (NOTE-4)	TELCORDIA UE (200 MATING CYCLES)

PART NUMBER	CONFIGURATION						
	ROW	6	5	4	3	2	1
85928—X01 SEE NOTE 11 LEAD FREE OPTION	D	1	0	1	0	1	0
	C	0	1	0	1	0	1
	B	1	0	1	0	1	0
	A	0	1	0	1	0	1

85928-X02 SEE NOTE 11 LEAD FREE OPTION	D	0	1	0	1	0	1
	C	1	0	1	0	1	0
	B	0	1	0	1	0	1
	A	1	0	1	0	1	0

85928-X03 SEE NOTE 11 LEAD FREE OPTION	D	1	1	0	0	1	1
	C	1	1	0	0	1	1
	B	1	1	0	0	1	1
	A	1	1	0	0	1	1

LOAD PATTERN	
1	LOADED
0	NOT LOADED



PCB LAYOUT - COMPONENT SIDE.

[illegible]