

# IGBT - Power, Co-PAK N-Channel, Field Stop IV, MQ (Medium Speed), TO247-4L 650 V, 1.45 V, 50 A FGH4L50T65MQDC50

Using the novel field stop 4th generation IGBT technology and generation 1.5 SiC Schottky Diode technology in TO-247 4-lead package, FGH4L50T65MQDC50 offers the optimum performance with both low conduction and switching losses for high-efficiency operations in various applications, especially totem pole bridgeless PFC and Inverter.

#### **Features**

- Positive Temperature Coefficient for Easy Parallel Operation
- High Current Capability
- 100% of the Parts are Tested for I<sub>LM</sub> (Note 2)
- Smooth and Optimized Switching
- Low Saturation Voltage:  $V_{CE(Sat)} = 1.45 \text{ V (Typ.)}$  @  $I_C = 50 \text{ A}$
- No Reverse Recovery / No Forward Recovery
- Tight Parameter Distribution
- RoHS Compliant

### **Applications**

- Charging Station (EVSE)
- Solar Inverter

• UPS, ESS

• PFC, Converters

### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

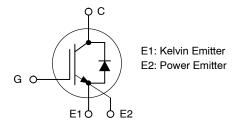
Parameter			Value	Unit
Collector-to-Emitter Voltage		V <sub>CES</sub>	650	V
Gate-to-Emitter Voltage		$V_{GES}$	±20	
Transient Gate–to–Emitter Voltage (t <sub>p</sub> < 0.5 μs, D < 0.001)			±30	
Collector Current	T <sub>C</sub> = 25°C (Note 1)	Ic	100	Α
	T <sub>C</sub> = 100°C		50	
Power Dissipation	Power Dissipation $T_C = 25^{\circ}C$		246	W
	T <sub>C</sub> = 100°C		123	
Pulsed Collector Current	T <sub>C</sub> = 25°C (Note 2)	$I_{LM}$	200	Α
T <sub>C</sub> = 25°C (Note 3)		I <sub>CM</sub>	200	
Diode Forward Current $T_{C} = 25^{\circ}C$ (Note 1)		ΙF	60	Α
T <sub>C =</sub> 100°C			50	
Pulsed Diode Maximum T <sub>C =</sub> 25°C Forward Current		I <sub>FM</sub>	200	Α
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	–55 to +175	°C
Maximum Lead Temp. for Soldering Purposes (1/8" from case for 5 s)		TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Value limit by bond wire
- 2.  $V_{CC}$  = 400  $\acute{V}$ ,  $V_{GE}$  = 15  $\acute{V}$ ,  $I_{C}$  = 200  $\acute{A}$ , Inductive Load, 100% tested
- 3. Repetitive rating: pulse width limited by max. junction temperature

BV <sub>CES</sub>	V <sub>CE(sat)</sub>	lc
650 V	1.45 V	50 A

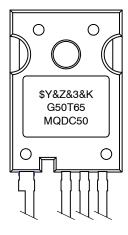
#### **PIN CONNECTIONS**





TO-247-4LD CASE 340CJ

#### **MARKING DIAGRAM**



\$Y = onsemi Logo &Z = Assembly Plant Code &3 = 3-Digit Date Code &K = 2-Digit Lot Traceability Code G50T65MQDC50 = Specific Device Code

#### ORDERING INFORMATION

Device	Package	Shipping
FGH4L50T65MQDC50	TO-247	30 Units / Tube
	-4LD	

#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance Junction-to-Case, for IGBT	$R_{ heta JC}$	0.61	°C/W
Thermal Resistance Junction-to-Case, for Diode	$R_{ heta JCD}$	0.70	
Thermal Resistance junction-to-Ambient	$R_{ hetaJA}$	40	

## **ELECTRICAL CHARACTERISTICS** (T<sub>.I</sub> = 25°C unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•		•	•	
Collector-emitter Breakdown Voltage, Gate-emitter Short-circuited	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	BV <sub>CES</sub>	650	-	_	V
Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	ΔBV <sub>CES</sub> ΔΤ <sub>J</sub>	-	0.5	-	V/°C
Collector-emitter Cut-off Current, Gate-emitter Short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 650 V	I <sub>CES</sub>	-	-	250	μА
Gate Leakage Current, Collector-emitter Short-circuited	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	-	±400	nA
ON CHARACTERISTICS		•		•	•	
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 50 \text{ mA}$	V <sub>GE(th)</sub>	3.0	4.5	6.0	V
Collector-emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>CE(sat)</sub>	_	1.45	1.8	V
	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 175°C	1	_	1.65	-	1
DYNAMIC CHARACTERISTICS		•		•	•	
Input Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 1 MHz	C <sub>ies</sub>	_	3340	-	pF
Output Capacitance		C <sub>oes</sub>	_	630	-	-
Reverse Transfer Capacitance		C <sub>res</sub>	_	10	-	
Gate Charge Total	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	Qg	_	102	-	nC
Gate-to-emitter Charge		Q <sub>ge</sub>	_	19	-	
Gate-to-collector Charge		Q <sub>gc</sub>	-	25	-	1
SWITCHING CHARACTERISTICS, INDUC	TIVE LOAD					-
Turn-on Delay Time	$T_J = 25^{\circ}C, V_{CC} = 400 V,$	t <sub>d(on)</sub>	_	27	-	ns
Rise Time	$I_C$ = 25 A, $R_G$ = 15 $\Omega$ , $V_{GE}$ = 15 V, Inductive Load	t <sub>r</sub>	_	10	-	
Turn-off Delay Time		t <sub>d(off)</sub>	_	181	-	
Fall Time		t <sub>f</sub>	_	21	-	
Turn-on Switching Loss		E <sub>on</sub>	_	0.24	-	mJ
Turn-off Switching Loss		E <sub>off</sub>	_	0.31	-	
Total Switching Loss		E <sub>ts</sub>	_	0.55	-	
Turn-on Delay Time	T <sub>J</sub> = 25°C, V <sub>CC</sub> = 400 V,	t <sub>d(on)</sub>	_	29	-	ns
Rise Time	$I_C$ = 50 A, $R_G$ = 15 $\Omega$ , $V_{GE}$ = 15 V, Inductive Load	t <sub>r</sub>	-	21	-	1
Turn-off Delay Time		t <sub>d(off)</sub>	-	173	-	1
Fall Time		t <sub>f</sub>	-	18	-	1
Turn-on Switching Loss		E <sub>on</sub>	-	0.54	-	mJ
Turn-off Switching Loss		E <sub>off</sub>	-	0.59	-	1
Total Switching Loss		E <sub>ts</sub>	_	1.13	-	1

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS,	INDUCTIVE LOAD		•			•
Turn-on Delay Time	T <sub>J</sub> = 175°C, V <sub>CC</sub> = 400 V,	t <sub>d(on)</sub>	-	24	_	ns
Rise Time	$I_C$ = 25 A, $R_G$ = 15 Ω, $V_{GE}$ = 15 V, Inductive Load	t <sub>r</sub>	-	11	_	1
Turn-off Delay Time		t <sub>d(off)</sub>	-	197	_	1
Fall Time		t <sub>f</sub>	-	24	_	1
Turn-on Switching Loss		E <sub>on</sub>	-	0.31	_	mJ
Turn-off Switching Loss		E <sub>off</sub>	-	0.51	-	1
Total Switching Loss		E <sub>ts</sub>	-	0.82	-	1
Turn-on Delay Time	$T_J = 175^{\circ}C, V_{CC} = 400 V,$	t <sub>d(on)</sub>	-	26	-	ns
Rise Time	$I_C$ = 50 A, $R_G$ = 15 Ω, $V_{GE}$ = 15 V, Inductive Load	t <sub>r</sub>	-	27	-	1
Turn-off Delay Time		t <sub>d(off)</sub>	-	186	-	1
Fall Time		t <sub>f</sub>	-	26	-	1
Turn-on Switching Loss		E <sub>on</sub>	-	0.74	-	mJ
Turn-off Switching Loss		E <sub>off</sub>	-	0.97	_	1
Total Switching Loss		E <sub>ts</sub>	-	1.71	-	1
DIODE CHARACTERISTICS	•	•				
Diode Forward Voltage	I <sub>F</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	1.46	1.7	V
	I <sub>F</sub> = 50 A, T <sub>J</sub> = 175°C	1	_	1.83	_	1
Total Capacitance	V <sub>R</sub> = 400 V, f = 1 MHz, T <sub>J</sub> = 25°C	С	_	210	-	pF
	V <sub>R</sub> = 600 V, f = 1 MHz, T <sub>J</sub> = 25°C	1	-	202	-	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

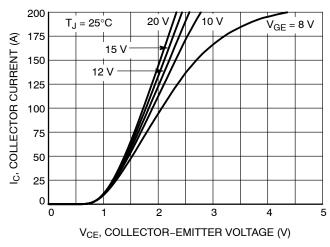
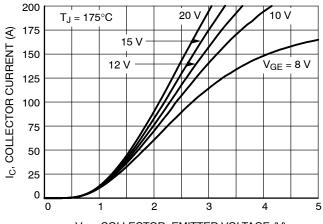


Figure 1. Typical Output Characteristics



V<sub>CE</sub>, COLLECTOR-EMITTER VOLTAGE (V) Figure 2. Typical Output Characteristics



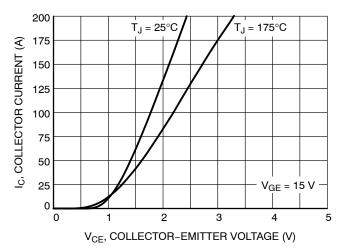


Figure 3. Typical Output Characteristics

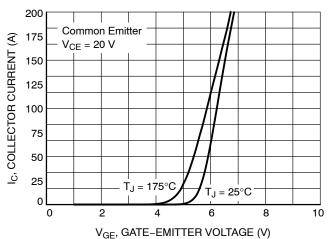


Figure 4. Transfer Characteristics

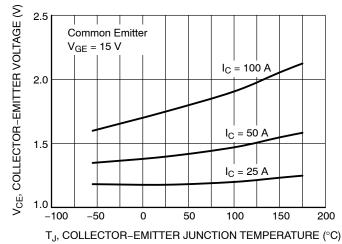


Figure 5. Saturation Voltage vs. Junction **Temperature at Variant Current Level** 

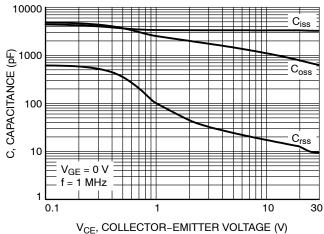


Figure 6. Capacitance Variation

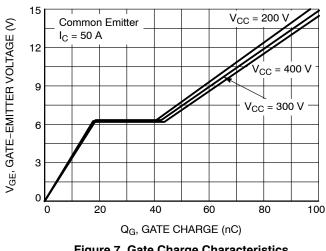


Figure 7. Gate Charge Characteristics

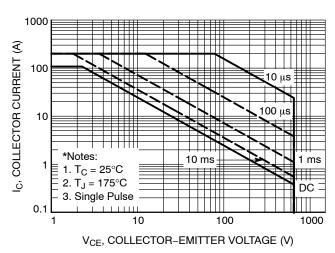


Figure 8. SOA Characteristics

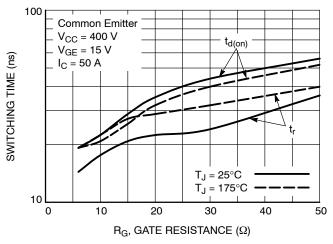


Figure 9. Turn-On Characteristics vs. Gate Resistance

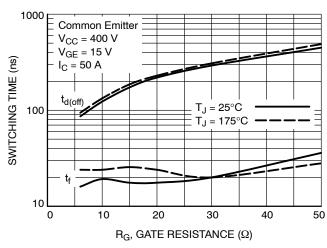


Figure 10. Turn-Off Characteristics vs. Gate Resistance

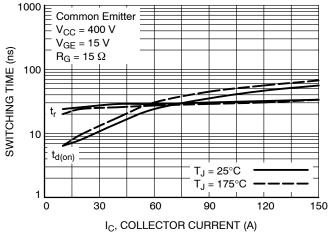


Figure 11. Turn-on Characteristics vs. **Collector Current** 

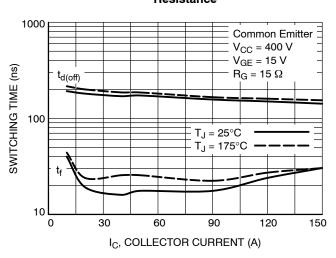


Figure 12. Turn-off Characteristics vs. **Collector Current** 

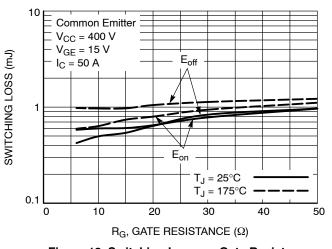


Figure 13. Switching Loss vs. Gate Resistance

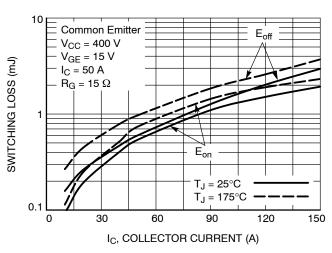


Figure 14. Switching Loss vs. Collector Current

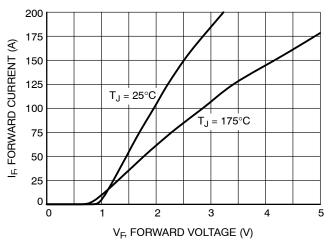


Figure 15. Forward Diode Characteristics

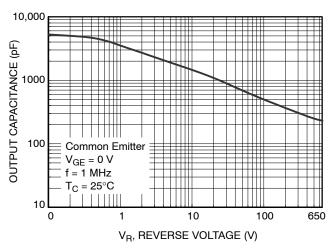


Figure 16. (Diode) Output Capacitance (Coes) vs. Reverse Voltage

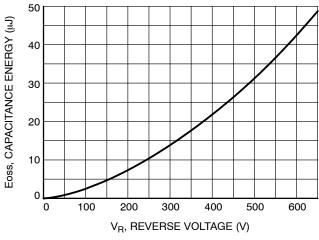


Figure 17. Output Capacitance Stored Energy

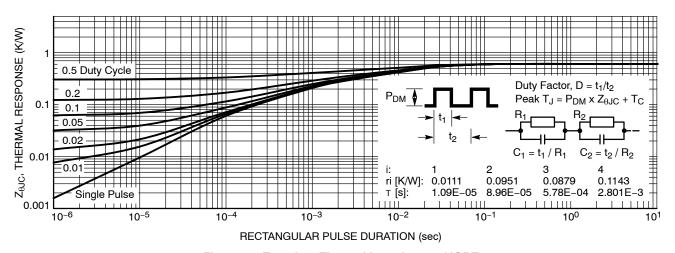


Figure 18. Transient Thermal Impedance of IGBT

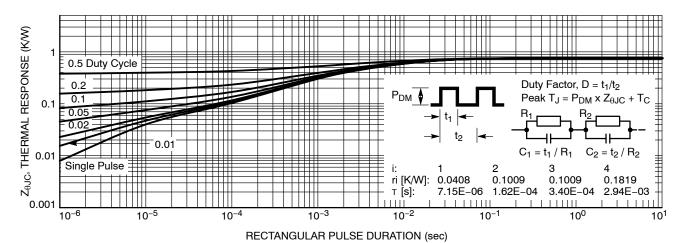
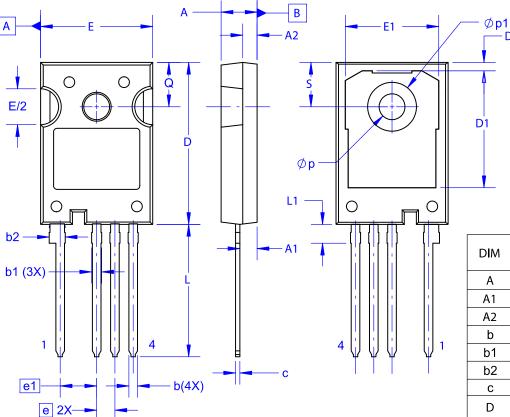


Figure 19. Transient Thermal Impedance of Diode

#### TO-247-4LD CASE 340CJ **ISSUE A**

**DATE 16 SEP 2019** 

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#### NOTES:

0.254 M

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
  B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
  FLASH, AND TIE BAR EXTRUSIONS.
  C. ALL DIMENSIONS ARE IN MILLIMETERS.
  D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MIN NOM		MAX
Α	4.80	5.00	5.20
A1	2.10	2.40	2.70
A2	1.80	2.00	2.20
b	1.07	1.20	1.33
b1	1.20	1.40	1.60
b2	2.02	2.22	2.42
С	0.50	0.60	0.70
D	22.34	22.54	22.74
D1	16.00	16.25	16.50
D2	0.97	1.17	1.37
е	2.54 BSC		
e1		5.08 BSC	
E	15.40	15.60	15.80
E1	12.80	13.00	13.20
E/2	4.80	5.00	5.20
L	18.22	18.42	18.62
L1	2.42	2.62	2.82
р	3.40	3.60	3.80
p1	6.60	6.80	7.00
Q	5.97	6.17	6.37
S	5.97	6.17	6.37

**MILLIMETERS** 

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