**ON Semiconductor** 

Is Now

# Onsemi

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# **NTD6N40**

Preferred Device

## **Power MOSFET** 6 Amps, 400 Volts **N-Channel DPAK**

Designed for high voltage, high speed switching applications in power supplies, converters, power motor controls and bridge circuits.

#### Features

- Higher Current Rating
- Lower R<sub>DS(on)</sub>
- Lower Capacitances
- Lower Total Gate Charge
- Tighter V<sub>SD</sub> Specifications
- Avalanche Energy Specified
- Industry Standard DPAK Surface Mount Package

#### **Typical Applications**

- Switch Mode Power Supplies
- PWM Motor Controls
- Converters
- Bridge Circuits

#### **MAXIMUM RATINGS** (T<sub>C</sub> = 25°C unless otherwise noted)

| Symbol   | Value  | Unit   |
|--|--|--|
| V <sub>DSS</sub>   | 400  | Vdc  |
| V <sub>DGR</sub>   | 400  | Vdc  |
| V <sub>GS</sub><br>V <sub>GSM</sub>                      | ±20<br>±40   | Vdc  |
| I <sub>D</sub><br>I <sub>D</sub><br>I <sub>DM</sub>      | 6.0<br>4.2<br>21   | Adc  |
| PD   | 96<br>0.77<br>1.75   | Watts<br>W/°C<br>W/°C  |
| T <sub>J</sub> , T <sub>stg</sub>                        | – 55 to<br>150   | °C   |
| E <sub>AS</sub>  | 180  | mJ   |
| R <sub>θJC</sub><br>R <sub>θJA</sub><br>R <sub>θJA</sub> | 1.30<br>100<br>71.4  | °C/W   |
| ΤL   | 260  | °C   |
|  | VDSS<br>VDGR<br>VGS<br>VGSM<br>ID<br>ID<br>ID<br>PD<br>TJ, Tstg<br>EAS<br>ReJC<br>ReJA<br>ReJA<br>ReJA | VDSS     400       VDGR     400       VGS     ±20       VGSM     ±40       ID     6.0       ID     4.2       IDM     21       PD     96       0.77     1.75       TJ, Tstg     -55 to       T50     EAS       ReJC     1.30       ReJA     100       ReJA     71.4 |

 When surface mounted to an FR4 board using the minimum recommended pad size.



### **ON Semiconductor**

http://onsemi.com

**6 AMPERES** 

# **400 VOLTS R**<sub>DS(on)</sub> = 1.1 Ω

N-Channel



MARKING

= Year = Work Week

WW

т



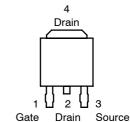
= MOSFET

CASE 369A

DPAK

**STYLE 2** 





#### **ORDERING INFORMATION**

| Device    | Package | Shipping         |
|-----------|---------|------------------|
| NTD6N40   | DPAK    | 75 Units/Rail    |
| NTD6N40-1 | DPAK    | 75 Units/Rail    |
| NTD6N40T4 | DPAK    | 2500 Tape & Reel |

Preferred devices are recommended choices for future use and best overall value.

## **NTD6N40**

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Characteristic   | Symbol                                     | Min      | Тур      | Max        | Unit         |
|--|--|----------|----------|------------|--------------|
| OFF CHARACTERISTICS  |  |          |          |            |              |
| Drain-to-Source Breakdown Voltage<br>(V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 0.25 mAdc)<br>Temperature Coefficient (Positive)   | V <sub>(BR)DSS</sub>                       | 400<br>- | _<br>500 |            | Vdc<br>mV/°C |
| Zero Gate Voltage Collector Current<br>( $V_{DS} = 400 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}$ )<br>( $V_{DS} = 400 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C}$ ) | I <sub>DSS</sub>                           |          |          | 10<br>100  | μAdc         |
| Gate-Body Leakage Current ( $V_{GS}$ = ±20 Vdc, $V_{DS}$ = 0)  | I <sub>GSS(f)</sub><br>I <sub>GSS(r)</sub> |          |          | 100<br>100 | nAdc         |

#### **ON CHARACTERISTICS** (Note 1)

| Gate Threshold Voltage<br>$I_D = 0.25 \text{ mA}, V_{DS} = V_{GS}$<br>Temperature Coefficient (Negative)  | V <sub>GS(th)</sub> | 2.0 | 2.7<br>6.0 | 4.0        | Vdc<br>mV/°C |
|---|---------------------|-----|------------|------------|--------------|
| Static Drain-to-Source On-Resistance (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 3 Adc)   | R <sub>DS(on)</sub> | -   | 900        | 1100       | mOhm         |
| $\label{eq:constraint} \begin{array}{l} \mbox{Drain-to-Source On-Voltage} \\ (V_{GS} = 10 \mbox{ Vdc}, \mbox{ I}_{D} = 6 \mbox{ Adc}) \\ (V_{GS} = 10 \mbox{ Vdc}, \mbox{ I}_{D} = 3 \mbox{ Adc}, \mbox{ T}_{J} = 125^{\circ}\mbox{C}) \end{array}$ | V <sub>DS(on)</sub> |     | - 6        | 7.9<br>6.9 | Vdc          |
| Forward Transconductance (V <sub>DS</sub> = 15 Vdc, I <sub>D</sub> = 3 Adc)   | 9 <sub>FS</sub>     | 2.0 | 4.4        | -          | mhos         |
| DYNAMIC CHARACTERISTICS   |                     |     |            |            |              |

#### DYNAMIC CHARACTERISTICS

| Input Capacitance        |   | C <sub>iss</sub> | -0       | 515 | 720 | pF |
|--------------------------|---|------------------|----------|-----|-----|----|
| Output Capacitance       | (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0 Vdc,<br>f = 1.0 MHz) | C <sub>oss</sub> |          | 185 | 260 |    |
| Transfer Capacitance     |   | C <sub>rss</sub> |          | 15  | 30  |    |
| SWITCHING CHARACTERISTIC | S (Note 2)  | 5.5              | <u> </u> |     |     |    |

#### SWITCHING CHARACTERISTICS (Note 2)

| Turn-On Delay Time  | 5   | t <sub>d(on)</sub>  | - | 7.0 | 10 | ns |
|---------------------|---|---------------------|---|-----|----|----|
| Rise Time           | $(V_{DD}=200 \; Vdc, \; I_{D}=6 \; Adc, \\ V_{GS}=10 \; Vdc, \\ R_{G}=9.1 \; \Omega)$ | tr                  | - | 11  | 20 |    |
| Turn-Off Delay Time | $R_{\rm G} = 9.1 \ \Omega$  | t <sub>d(off)</sub> | - | 19  | 40 |    |
| Fall Time           | N. 10-  | t <sub>f</sub>      | - | 10  | 20 |    |
| Gate Charge         |   | QT                  | - | 9.5 | 19 | nC |
|                     | (V <sub>DS</sub> = 320 Vdc, I <sub>D</sub> = 6 Adc,<br>V <sub>GS</sub> = 10 Vdc)      | Q <sub>1</sub>      | - | 2.0 | -  |    |
|                     | V <sub>GS</sub> = 10 Vdc)   | Q <sub>2</sub>      | - | 3.0 | -  |    |
|                     |   | Q <sub>3</sub>      | - | 6.0 | -  |    |

## SOURCE-DRAIN DIODE CHARACTERISTICS

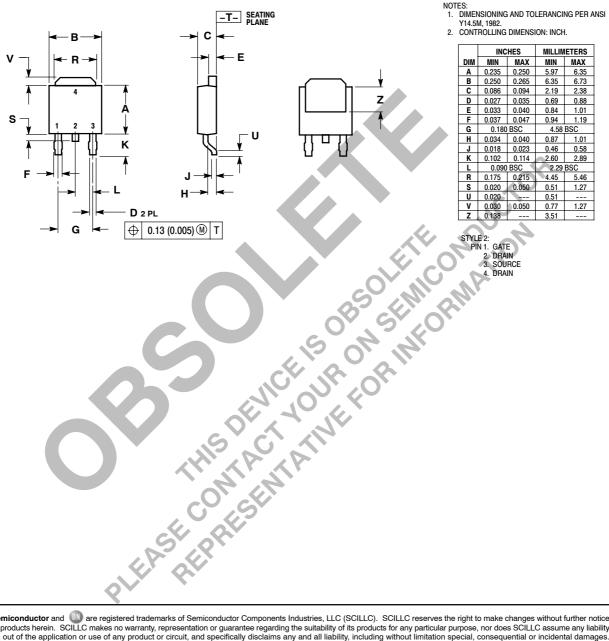
| Forward On-Voltage (Note 1)       |   | V <sub>SD</sub> |   |     |     | Vdc |
|-----------------------------------|---|-----------------|---|-----|-----|-----|
|                                   | $(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$<br>$(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$ |                 | - | 0.9 | 1.0 |     |
|                                   | $(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$  |                 | - | 0.8 | -   |     |
| Reverse Recovery Time             |   | t <sub>rr</sub> | - | 270 | -   | ns  |
| <b>Q</b> *                        |   | t <sub>a</sub>  | - | 110 | -   |     |
|                                   | (I <sub>S</sub> = 6 Adc, V <sub>GS</sub> = 0 Vdc,<br>di <sub>S</sub> /dt = 100 A/μs)  | t <sub>b</sub>  | - | 160 | -   |     |
| Reverse Recovery Stored<br>Charge |   | Q <sub>RR</sub> | - | 1.6 | _   | μC  |

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
Switching characteristics are independent of operating junction temperature.

#### NTD6N40

#### PACKAGE DIMENSIONS





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