

# **MOSFET** - N-Channel, POWERTRENCH®

100 V, 67 A, 8.5 m $\Omega$ 

# FDPF045N10A

#### Description

This N-Channel MOSFET is Produced using **onsemi**'s advanced PowerTrench Process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

#### **Features**

- $R_{DS(on)} = 3.7 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 67 \text{ A}$
- Fast Switching Speed
- Low Gate Charge, Qg = 57 nC (Typ.)
- High Performance Trench Technology for Extremely Low R<sub>DS(on)</sub>
- High Power and Current Handling Capability
- This Device is Pb-Free Halide, Free and RoHS Compliant

#### **Applications**

- Synchronous Rectification for ATX / Sever / Telecom PSU
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter

#### **ABSOLUTE MAXIMUM RATINGS**

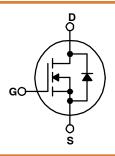
( $T_C = 25^{\circ}C$  unless otherwise noted.)

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain to Source Voltage 100		V
V <sub>GSS</sub>	Gate to Source Voltage	±20	V
Ι <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C) - Continuous (T <sub>C</sub> = 100°C)	67 47	Α
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	Current – Pulsed (Note 1) 268	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	637	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	6.0	V/ns
P <sub>D</sub>	Power Dissipation - (T <sub>C</sub> = 25°C) - Derate Above 25°C	43 0.29	W W/°C
T <sub>J</sub> ,T <sub>STG</sub>	T <sub>J</sub> ,T <sub>STG</sub> Operating and Storage Temperature –55 to +175 Range		°C
TL	T <sub>L</sub> Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



TO-220F CASE 221AT



#### MARKING DIAGRAM

FDPF04 5N10A AYWWZZ

FDPF045N10A = Specific Device Code
A = Assembly Location
YWW = Date Code (Year and Week)
ZZ = Assembly Lot Code

#### **ORDERING INFORMATION**

Device	Package	Shipping
FDPF045N10A	TO-220-3 FullPack	1000 Units / Tube

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Charac	cteristics		•	•	•	
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0 V	100	_	_	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C	-	0.06	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V	_	_	1	μΑ
		V <sub>DS</sub> = 80 V, T <sub>C</sub> = 150°C	-	_	500	
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	_	±100	nA
On Charac	cteristics	•				
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2.0	_	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 67 A	-	3.7	4.5	mΩ
9FS	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 67 A	-	127	_	S
Dynamic 0	Characteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	3961	5270	pF
Coss	Output Capacitance		_	925	1230	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		_	34	-	pF
C <sub>oss</sub> (er)	Engry Related Output Capacitance	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V	_	1521	_	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 50 V, I <sub>D</sub> = 100 A	-	57	74	nC
Q <sub>gs</sub>	Gate to Source Gate Charge		-	17	_	nC
Q <sub>gs2</sub>	Gate Charge Threshold to Plateau		_	8	_	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		_	13	_	nC
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	_	1.9	-	Ω
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 100 A,	_	23	56	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 4.7 \Omega \text{ (Note 4)}$	-	26	62	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		_	50	110	ns
t <sub>f</sub>	Turn-Off Fall Time		_	15	40	ns
Drain-Sou	rce Diode Characteristics and Maximum F	Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	67	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		_	_	268	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 67 A	_	_	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, V <sub>DD</sub> = 50 V, I <sub>SD</sub> = 100 A,	_	75	_	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_{F}/dt = 100 A/\mu s$	_	120	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 1. Repetitive Rating: Pulse-width limited by maximum junction temperature.
- 2. L = 3 mH,  $I_{AS}$  = 20.6 A,  $R_G$  = 25  $\Omega$  starting  $T_J$  = 25°C. 3.  $I_{SD} \le 100$  A, di/dt  $\le 200$  A/ $\mu$ s,  $V_{DD} \le BV_{DSS}$ , starting  $T_J$  = 25°C. 4. Essentially independent of operating temperature.

#### **TYPICAL CHARACTERISTICS**

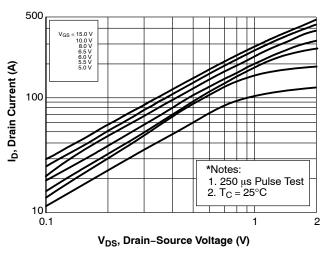


Figure 1. On-Region Characteristics

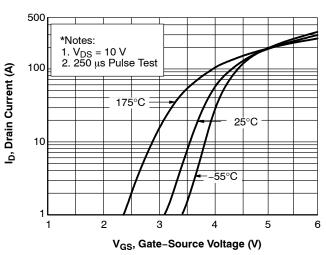


Figure 2. Transfer Characteristics

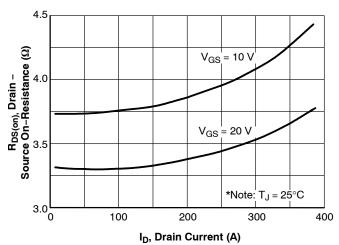


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

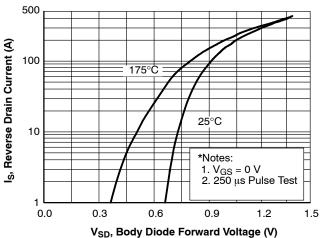


Figure 4. Body Diode Forward Voltage Variation vs Source Current and Temperature

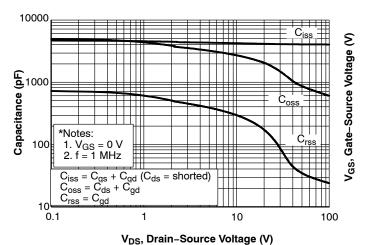


Figure 5. Capacitance Characteristics

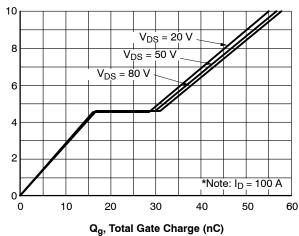


Figure 6. Gate Charge Characteristics

### TYPICAL CHARACTERISTICS (CONTINUED)

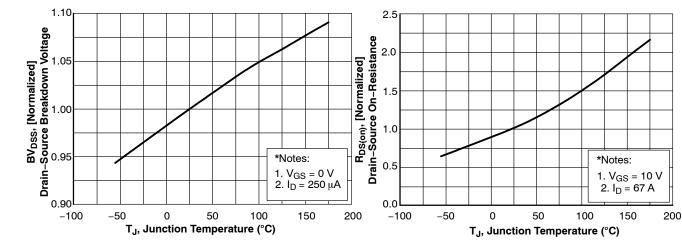


Figure 7. Breakdown Voltage Variation vs Temperature

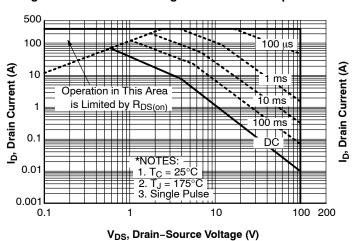


Figure 9. Maximum Safe Operating Area

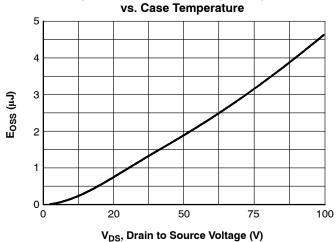


Figure 11. Eoss vs. Drain to Source Voltage

Figure 8. On-Resistance Variation vs Temperature

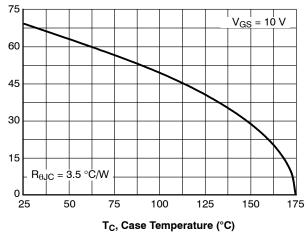


Figure 10. Maximum Drain Current

# TYPICAL CHARACTERISTICS (CONTINUED)

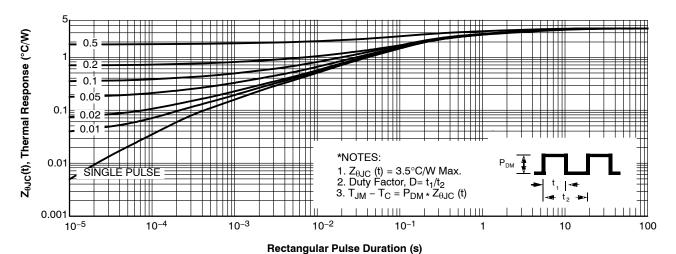


Figure 12. Transient Thermal Response Curve

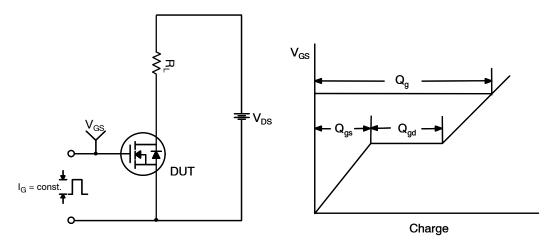


Figure 13. Gate Charge Test Circuit & Waveform

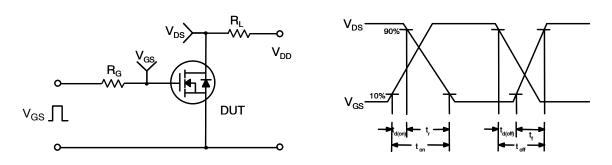


Figure 14. Resistive Switching Test Circuit & Waveforms

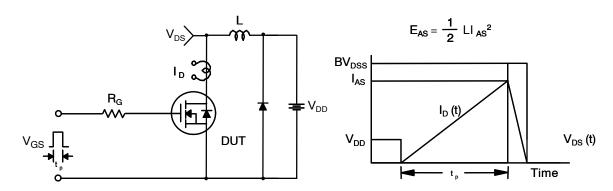
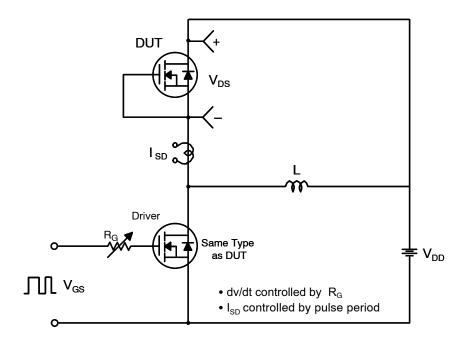


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms



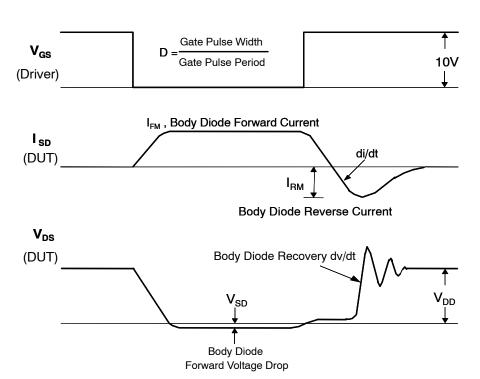
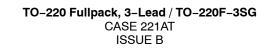
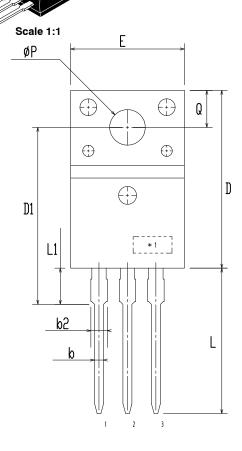
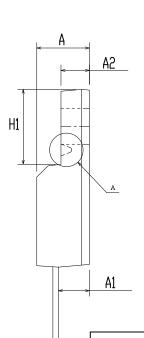


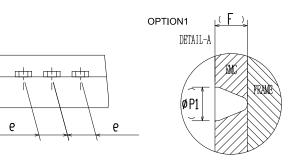
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms



**DATE 19 JAN 2021** 







DIM	MILLIMITER2			
ויונע	MIN	NDM	MAX	
Α	4.50	4.70	4.90	
A1	2.56	2.76	2.96	
A2	2.34	2.54	2.74	
b	0.70	0.80	0.90	
b2	~	~	1.47	
С	0.45	0.50	0.60	
D	15.67	15.87	16.07	
D1	15.60	15.80	16.00	
E	9.96	10.16	10.36	
е	2.34	2.54	2.74	
F	~	0.84	~	
H1	6.48	6.68	6.88	
L	12.78	12.98	13.18	
L1	3.03	3.23	3.43	
ØΡ	2.98	3.18	3.38	
Ø P1	~	1.00	~	
Q	3.20	3.30	3.40	

MILL IMITEDS

#### NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

C. OPTION 1 - WITH SUPPORT PIN HOLE

**OPTION 2 - NO SUPPORT PIN HOLE** 

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