



SBR1045SP5

10A SBR SUPER BARRIER RECTIFIER PowerDI5

Product Summary (@ T_A = +25°C)

V _{RRM} (V)	I ₀ (A)	V _{F(MAX)} (V)	I _{R(MAX)} (mA)	
45	10	0.55	0.45	

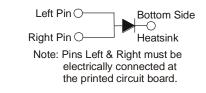
Features and Benefits

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for +200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology (SBR®)
- Low Forward Voltage Drop
- **Excellent High Temperature Stability**
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR1045SP5Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Package: PowerDI[®]5
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.093 grams (Approximate)



Applications SMPS

- **DC-DC** converters
- Freewheeling diodes



Top View

Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Fait Nulliber	Package	Qty.	Carrier	
SBR1045SP5-13	PowerDI5	5000	Tape & Reel	
SBR1045SP5-13D (Note 5)	PowerDI5	5000	Tape & Reel	
SBR1045SP5Q-13	PowerDI5	5000	Tape & Reel	

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. Notes:

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

5. PowerDI5 available in 5k quantity on 13in. reel & 12mm tape, part number suffix "13D".

Marking Information



S1045S = Product Type Marking Code DII = Manufacturer's Code Marking YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 23 for 2023) WW = Week Code (01 to 53) K = Factory Designator



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vrm	45	V
RMS Reverse Voltage	VR(RMS)	32	V
Average Rectified Output Current	lo	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	IFSM	180	A
Repetitive Peak Avalanche Power (1µs, +25°C)	PARM	10,000	W

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Typical Thermal Resistance Junction to Lead		Rejl	3	°C/W	
Typical Thermal Resistance Junction to Case (Note 6)		Rejc	6		
Typical Thermal Resistance Junction to Ambient (Note 6)		Reja	102	C/W	
Typical Thermal Resistance Junction to Ambient (Note 7)		Reja	60	1	
Operating Temperature Range	V _R ≤ 80% V _{RRM}		-65 to +150	°C	
	V _R ≤ 50% V _{RRM}	TJ	≤ +180		
	DC Forward Mode (Note 8)		≤ + 200		
Storage Temperature Range		Tstg	-65 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

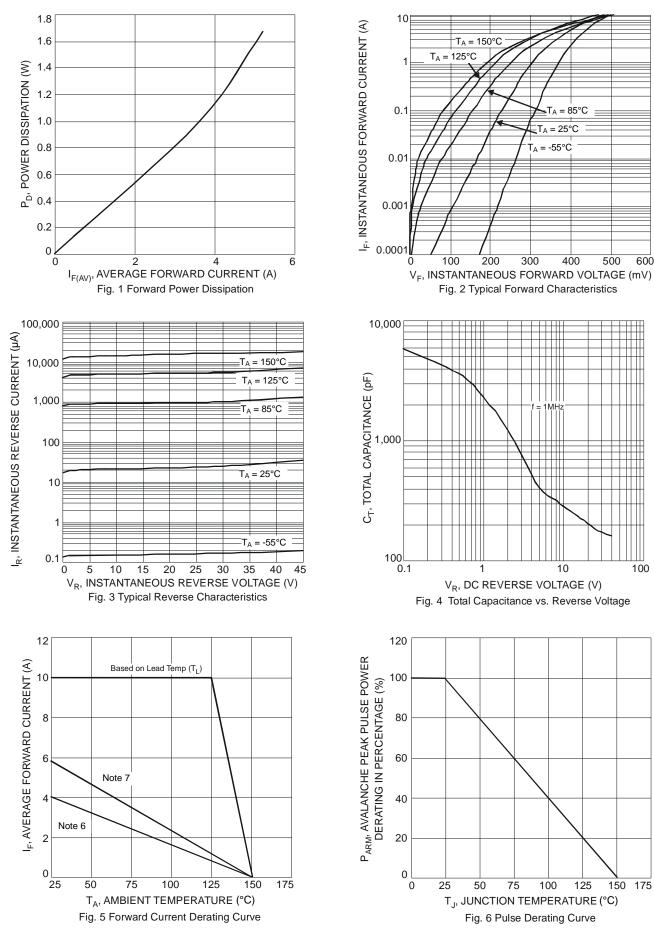
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	V(BR)R	45	—	—	V	I _R = 0.5mA
Forward Voltage Drop	VF		0.49 0.47	0.51 0.55 0.53	V	IF = 8A, TJ = +25°C IF = 10A, TJ = +25°C IF = 10A, TJ = +125°C
Leakage Current (Note 9)	I _R		0.03 17	0.45 18 100	mA	$V_{R} = 45V, T_{J} = +25^{\circ}C$ $V_{R} = 45V, T_{J} = +100^{\circ}C$ $V_{R} = 45V, T_{J} = +150^{\circ}C$
Typical Junction Capacitance	CJ	_	500	_	pF	$f = 1MHz$, $I_R = 4V$

Notes: 6. FR-4 PCB, 2oz. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

Polymide PCB, 202. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
Max junction temperature guaranteed for 2 hours.
Short duration pulse test used to minimize self-heating effect.

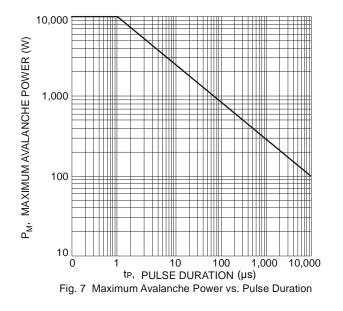






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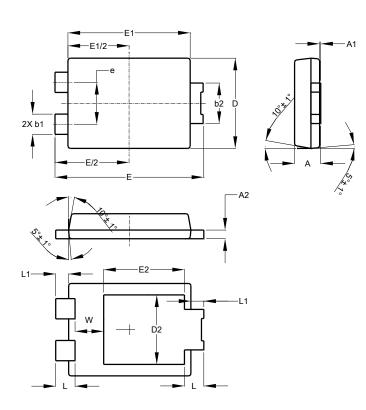






Package Outline Dimensions

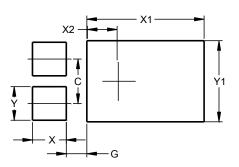
Please see http://www.diodes.com/package-outlines.html for the latest version.



PowerDI5 Dim Min Max Тур Α 1.05 1.15 1.10 A1 0.00 0.05 ---A2 0.33 0.43 0.381 b1 0.80 0.99 0.89 b2 1.70 1.88 1.78 D 3.90 4.05 3.966 D2 3.054 Ε 6.40 6.60 6.51 е 1.84 5.30 5.45 E1 5.37 3.549 E2 L 0.75 0.95 0.85 L1 0.50 0.65 0.57 W 1.10 1.41 1.255 All Dimensions in mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.400
X1	4.860
X2	1.310
Y	1.390
Y1	3.360

PowerDI5

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