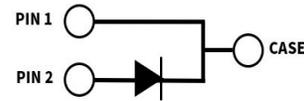


C4D40120H

4th Generation 1200 V, 40 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



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Part Number	Package	Marking
C4D40120H	TO-247-2	C4D40120H

Features

- Low Forward Voltage (V_f) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Key Parameters ($T_c = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Conditions	Notes
Repetitive Peak Reverse Voltage	V_{RRM}	1200	V		
DC Blocking Voltage	V_{DC}	1200			
Continuous Forward Current	I_F	128	A	$T_c = 25^\circ\text{C}$	Fig. 3
		62		$T_c = 135^\circ\text{C}$	
		41		$T_c = 155^\circ\text{C}$	
Repetitive Peak Forward Surge Current	I_{FRM}	161		$T_c = 25^\circ\text{C}, t_p = 10\text{ ms, Half Sine Wave}$	
		91		$T_c = 110^\circ\text{C}, t_p = 10\text{ ms, Half Sine Wave}$	
Non-Repetitive Forward Surge Current	I_{FSM}	247		$T_c = 25^\circ\text{C}, t_p = 10\text{ ms, Half Sine Wave}$	Fig. 8
		245		$T_c = 110^\circ\text{C}, t_p = 10\text{ ms, Half Sine Wave}$	
i^2t Value	$\int i^2t$	305		$T_c = 25^\circ\text{C}, t_p = 10\text{ ms}$	
		300	$T_c = 110^\circ\text{C}, t_p = 10\text{ ms}$		
Power Dissipation	P_{tot}	667	W	$T_c = 25^\circ\text{C}$	Fig. 4
		289		$T_c = 110^\circ\text{C}$	

Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V_F	1.5	1.8	V	$I_F = 40A, T_j = 25\text{ }^\circ\text{C}$	Fig. 1
		2.1	3		$I_F = 40A, T_j = 175\text{ }^\circ\text{C}$	
Reverse Current	I_R	45	300	μA	$V_R = 1200V, T_j = 25\text{ }^\circ\text{C}$	Fig. 2
		75	500		$V_R = 1200V, T_j = 175\text{ }^\circ\text{C}$	
Total Capacitive Charge	Q_C	194.9		nC	$V_R = 800V, T_j = 25\text{ }^\circ\text{C}$	Fig. 5
Total Capacitance	C	2809		pF	$V_R = 0V, T_j = 25\text{ }^\circ\text{C}, f = 1\text{ MHz}$	Fig. 6
		174			$V_R = 400V, T_j = 25\text{ }^\circ\text{C}, f = 1\text{ MHz}$	
		127			$V_R = 800V, T_j = 25\text{ }^\circ\text{C}, f = 1\text{ MHz}$	
Capacitance Stored Energy	E_C	56.6		μJ	$V_R = 800V$	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	$R_{\theta, JC (TYP)}$	0.225	$^\circ\text{C} / \text{W}$	
Junction Temperature	T_j	-55 to +175	$^\circ\text{C}$	
Case & Storage Temperature	T_c	-55 to +150		
TO-247 Mounting Torque	-	1	Nm	M3 Screw
		8.8	lbf-in	6-32 Screw

Typical Performance

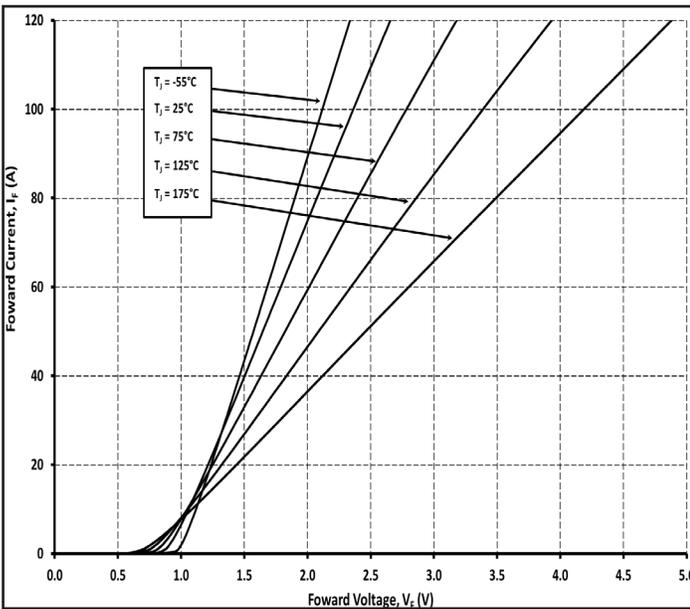


Figure 1. Forward Characteristics

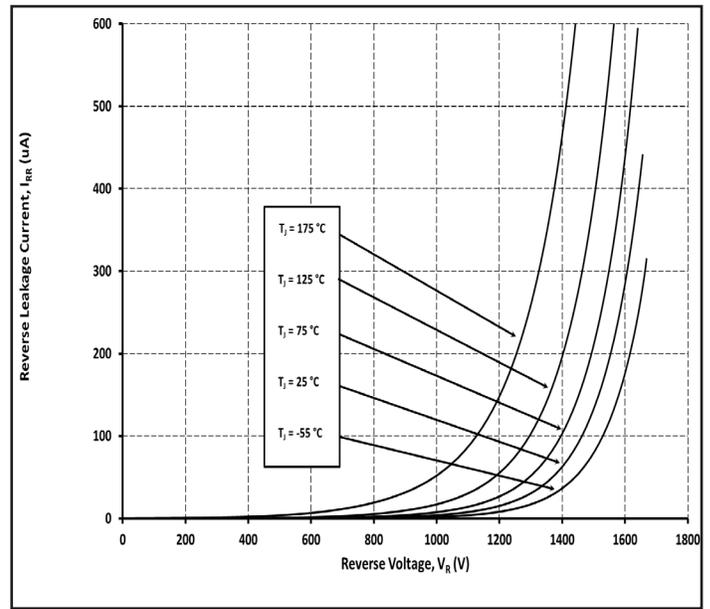


Figure 2. Reverse Characteristics

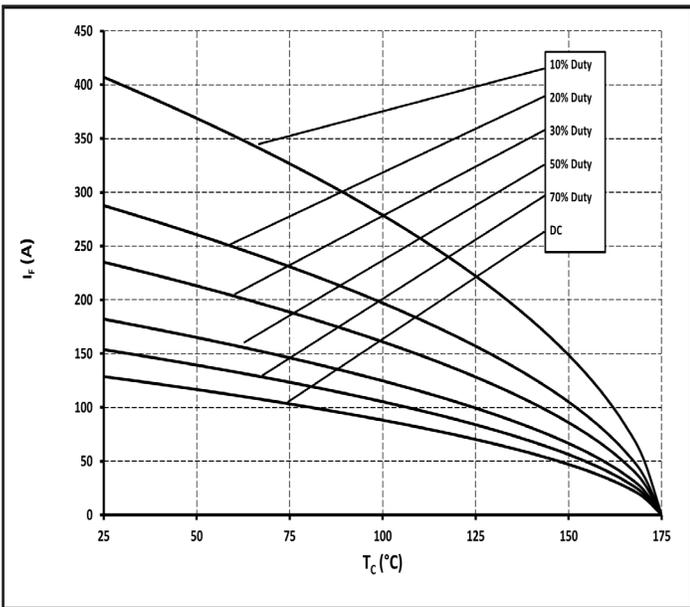


Figure 3. Current Derating

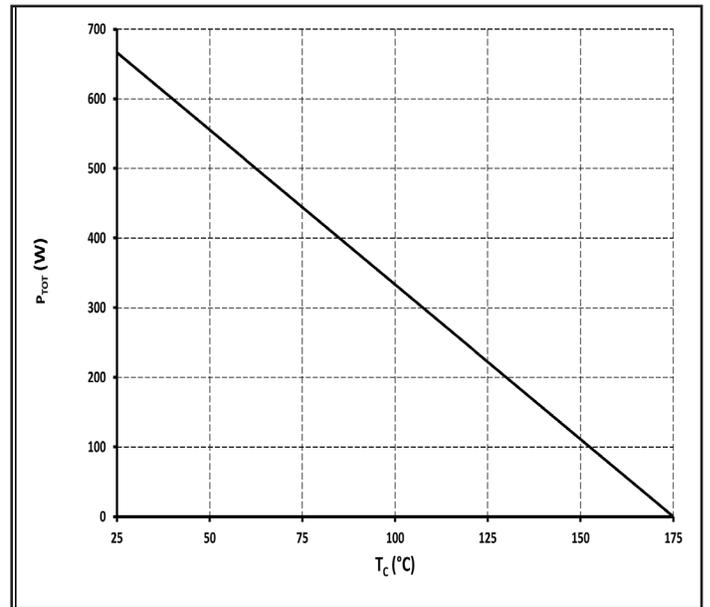


Figure 4. Power Derating

Typical Performance

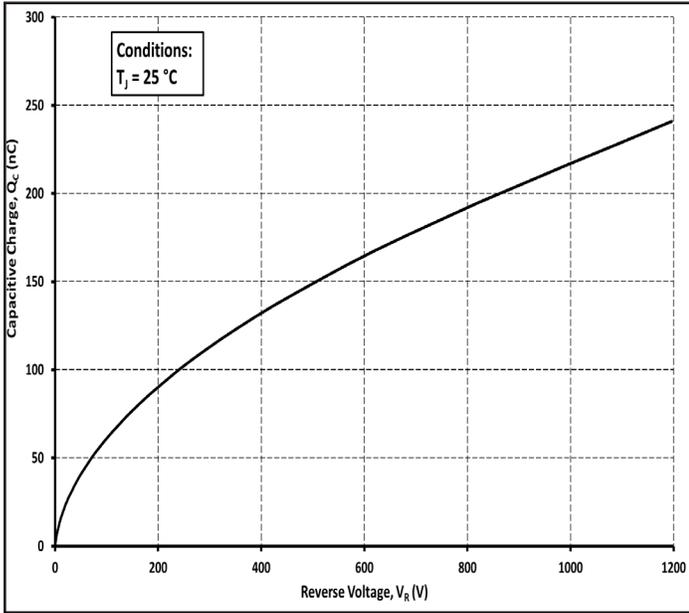


Figure 5.

Total Capacitance Charge vs. Reverse Voltage

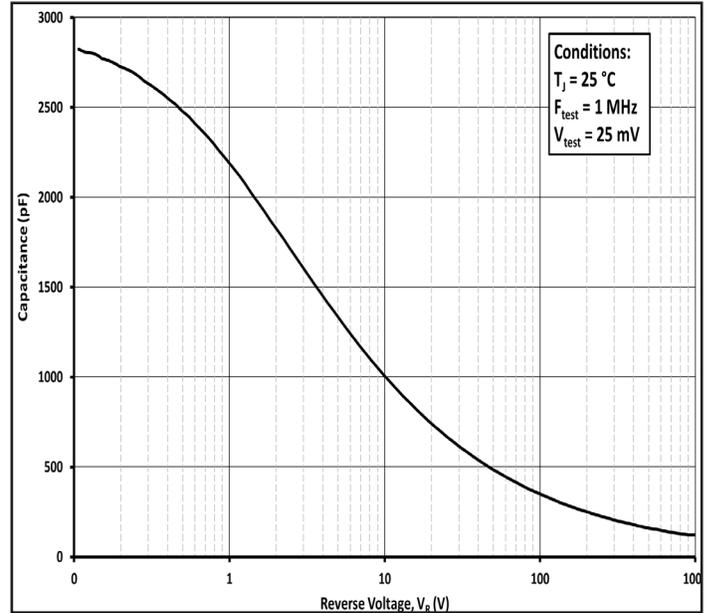


Figure 6.

Capacitance vs. Reverse Voltage

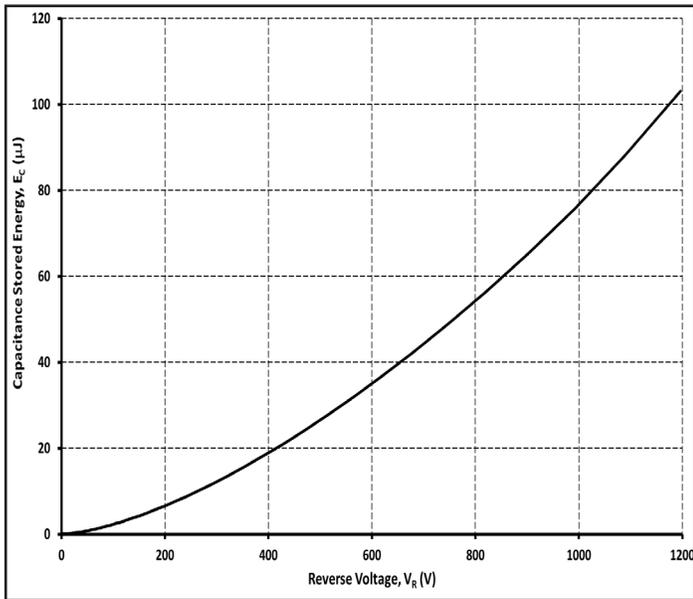


Figure 7.

Typical Capacitance Stored Energy

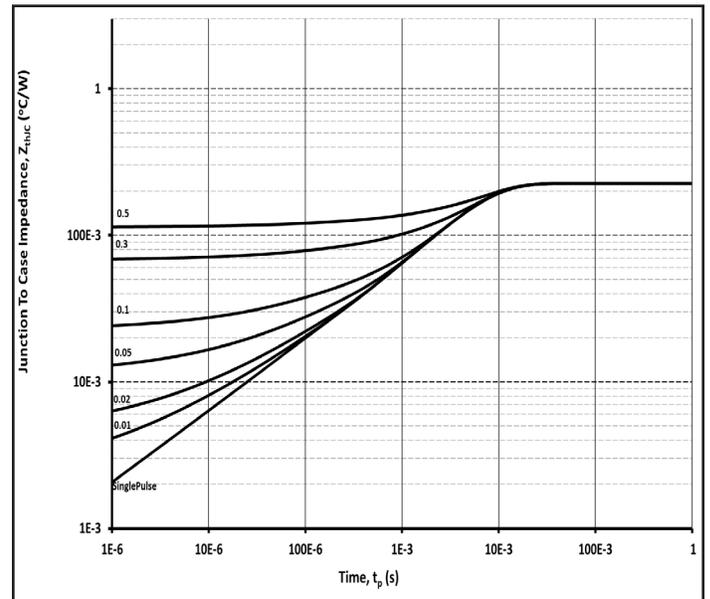
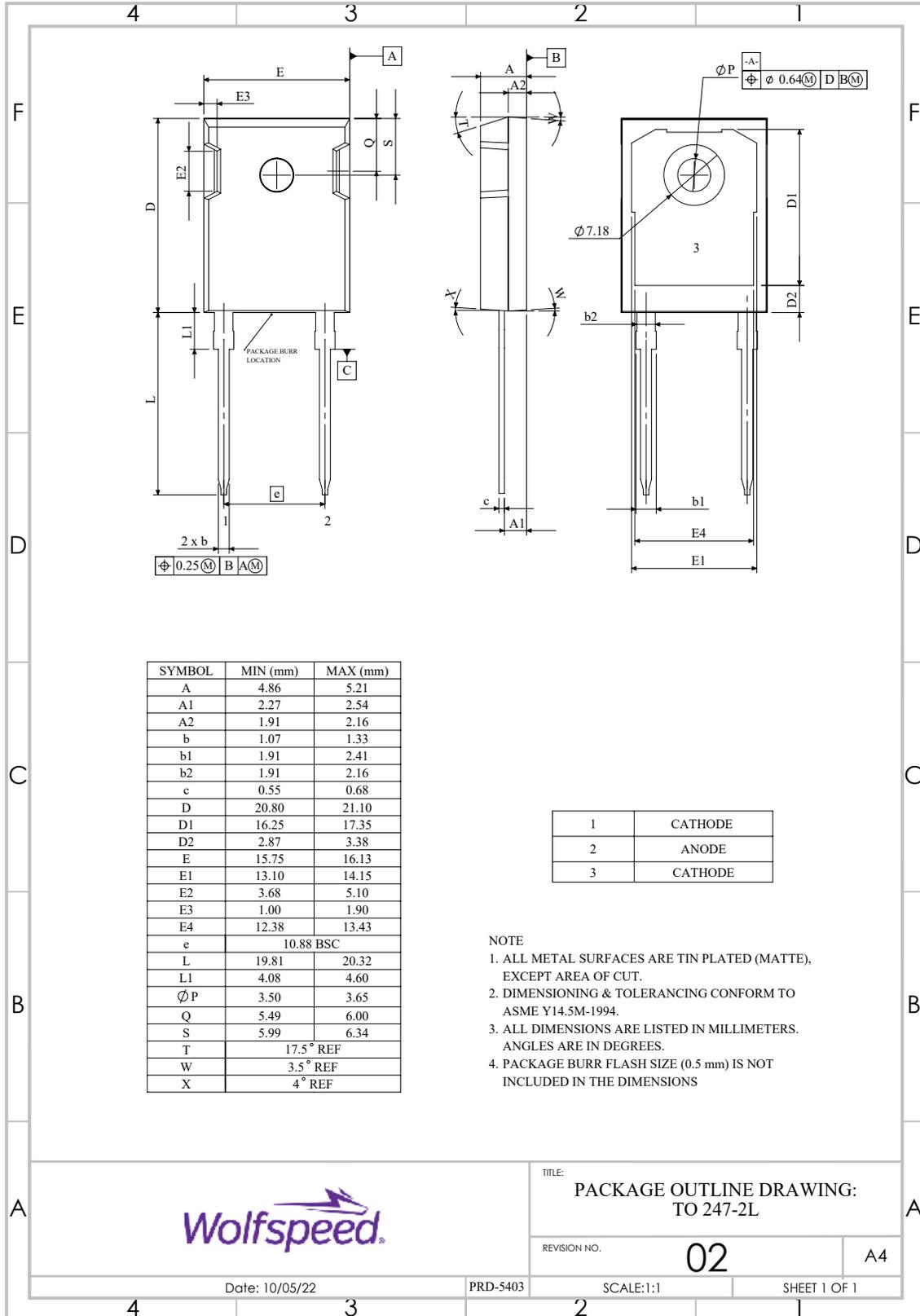


Figure 8. Transient Thermal Impedance

Package Dimensions & Pin-Out

Package: TO-247-2 (All dimensions are in mm)



TITLE: PACKAGE OUTLINE DRAWING: TO 247-2L

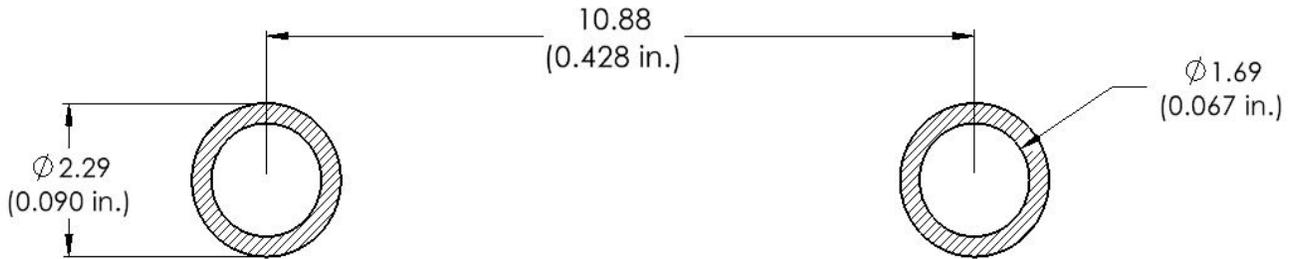
REVISION NO. 02 A4

Date: 10/05/22 PRD-5403 SCALE:1:1 SHEET 1 OF 1



Recommended Solder Pad Layout

(All dimensions are in mm)



Product Ordering Information

Order Number	Packing Type
C4D40120H	Tube

REACH, RoHS, and Halogen-Free compliance documentation available for this product.



Revision History

Document Version	Date of Release	Description of changes
0	January - 2019	Initial Release
1	January - 2023	Updated POD and Landing Pad
2	August - 2023	Total Capacitance test condition 800V value corrected Qc and Ec corrected for test condition Renamed Table on Page 1
3	September- 2023	Corrected I_F value at $T_{C=135C}$

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