C3D10065E

650 V, 10 A Silicon Carbide Schottky Diode

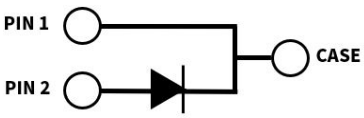
TO-252-2

Halogen-Free

RoHS compliant

Features

- 650-Volt Schottky rectifier
- Zero reverse recovery current
- Zero forward recovery voltage
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V_F



Package Types: TO-252-2
Marking: C3D10065

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Applications

- Switch mode power supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free wheeling diodes in inverter stages
- AC/DC converters

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

Maximum Ratings (T_C = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note
Repetitive Peak Reverse Voltage	V_{RRM}	650	V		
Surge Peak Reverse Voltage	V_{RSM}	650			
DC Blocking Voltage	V_{DC}	650			
Continuous Forward Current	I_F	32	A	T_C = 25 °C	Fig. 3
		15		T_C = 135 °C	
		10		T_C = 153 °C	
Repetitive Peak Forward Surge Current	I_{FRM}	43.5		T_C = 25 °C, t_P = 10 ms, Half Sine Wave	
		28		T_C = 110 °C, t_P = 10 ms, Half Sine Wave	
Non-Repetitive Peak Forward Surge Current	I_{FSM}	90		T_C = 25 °C, t_P = 10 ms, Half Sine Wave	Fig. 8
		71		T_C = 110 °C, t_P = 10 ms, Half Sine Wave	
Non-Repetitive Peak Forward Surge Current	$I_{F, Max}$	860		T_C = 25 °C, t_P = 10 µs, Pulse	Fig. 8
		680		T_C = 110 °C, t_P = 10 µs, Pulse	
Power Dissipation	P_{tot}	150	W	T_C = 25 °C	Fig. 4
		65		T_C = 110 °C	
Diode dV/dt Ruggedness	dV/dt	200	V/ns	V_R = 0-650 V	
i^2t Value	$\int i^2dt$	40.5	A^2s	T_C = 25 °C, t_P = 10 ms	
		25		T_C = 110 °C, t_P = 10 ms	
Operating Junction and Storage Temperature	T_J , T_{stg}	-55 to +175	°C		

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Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Note
Forward Voltage	V_F	1.5	1.8	V	I_F = 10 A, T_J = 25 °C	Fig. 1
		2.0	2.4		I_F = 10 A, T_J = 175 °C	
Reverse Current	I_R	12	60	µA	V_R = 650 V, T_J = 25 °C	Fig. 2
		24	220		V_R = 650 V, T_J = 175 °C	
Total Capacitive Charge	Q_C	24		nC	V_R = 400 V, I_F = 10 A di/dt = 500 A/µS T_J = 25 °C	Fig. 5
Total Capacitance	C	460.5		pF	V_R = 0 V, T_J = 25 °C, f = 1 MHz	Fig. 6
		44			V_R = 200 V, T_J = 25 °C, f = 1 MHz	
		40			V_R = 400 V, T_J = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E_C	3.6		µJ	V_R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Typ.	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.0	°C/W	Fig. 9

Typical Performance

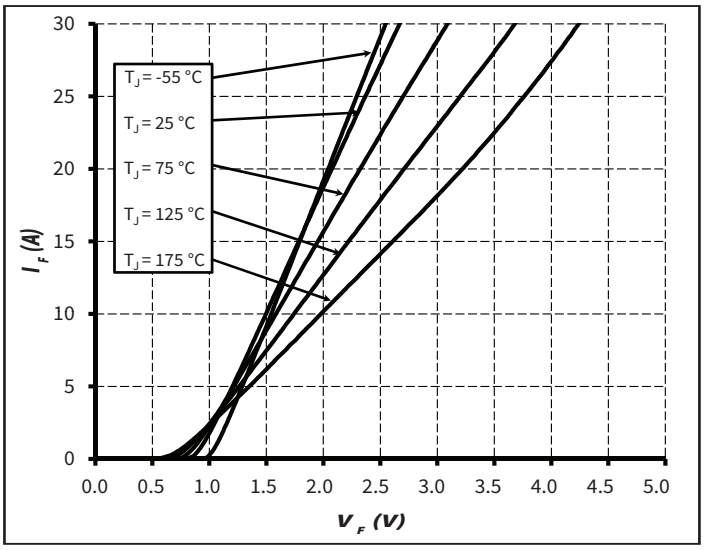


Figure 1. Forward Characteristics

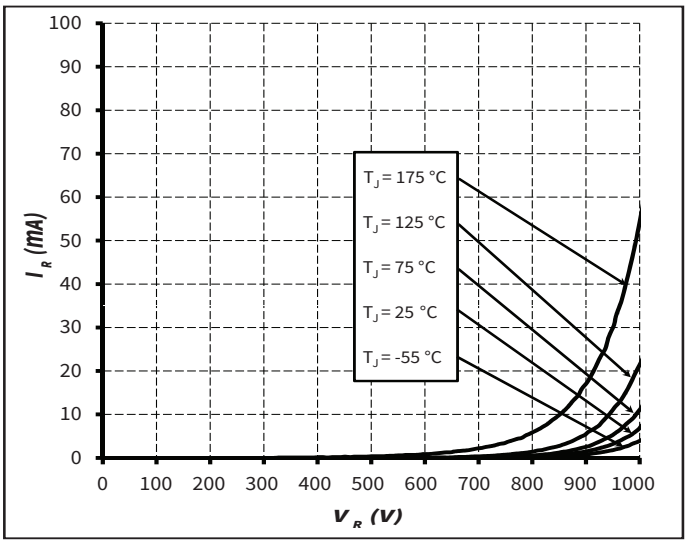


Figure 2. Reverse Characteristics

Typical Performance

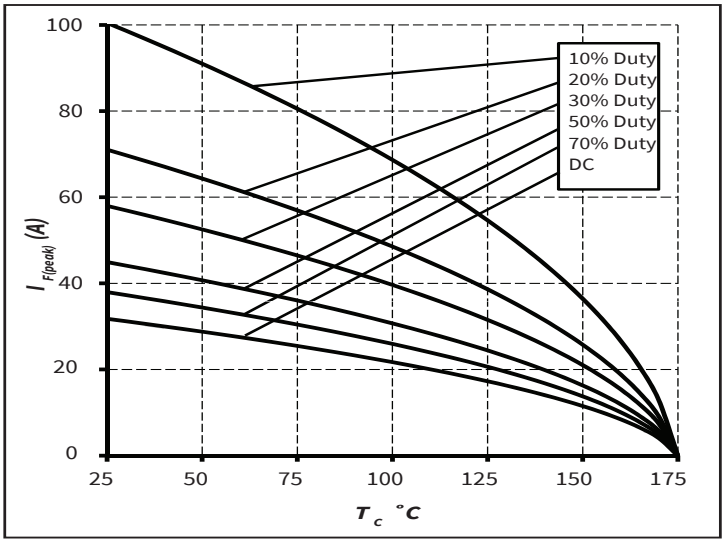


Figure 3. Current Derating

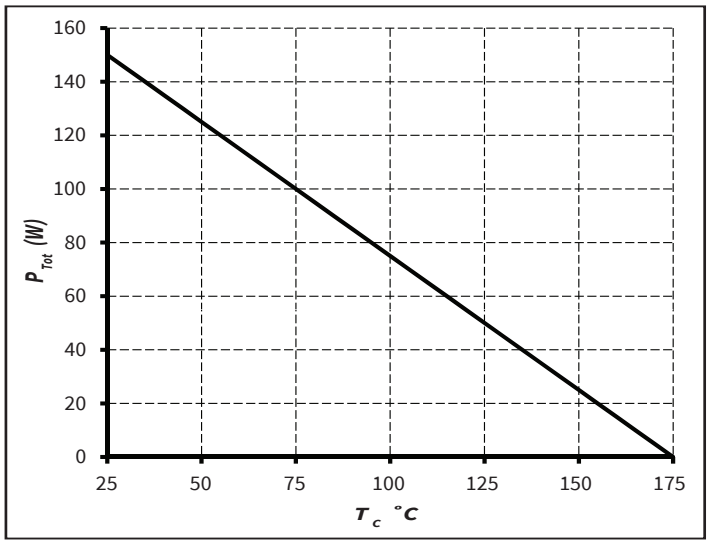


Figure 4. Power Derating

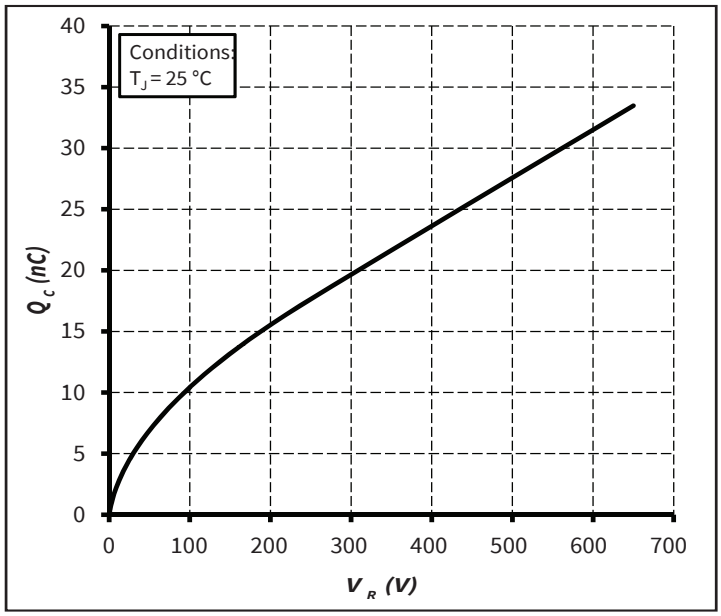


Figure 5. Total Capacitance Charge vs. Reverse Voltage

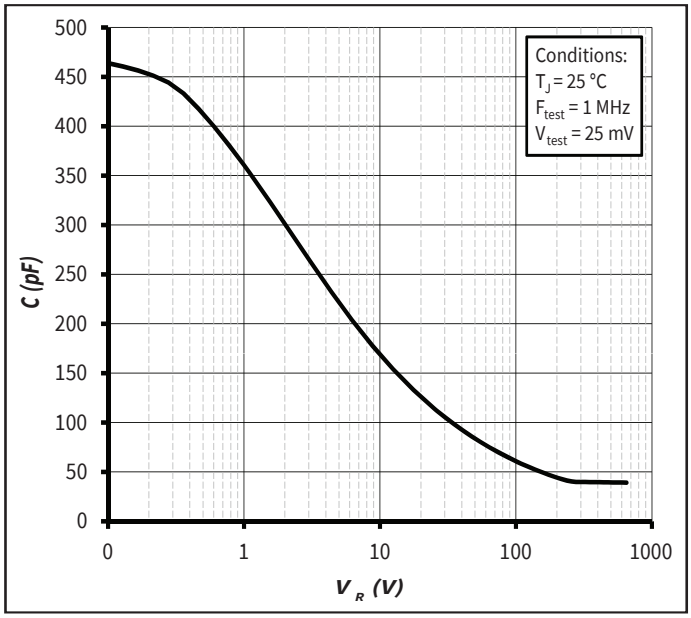


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance

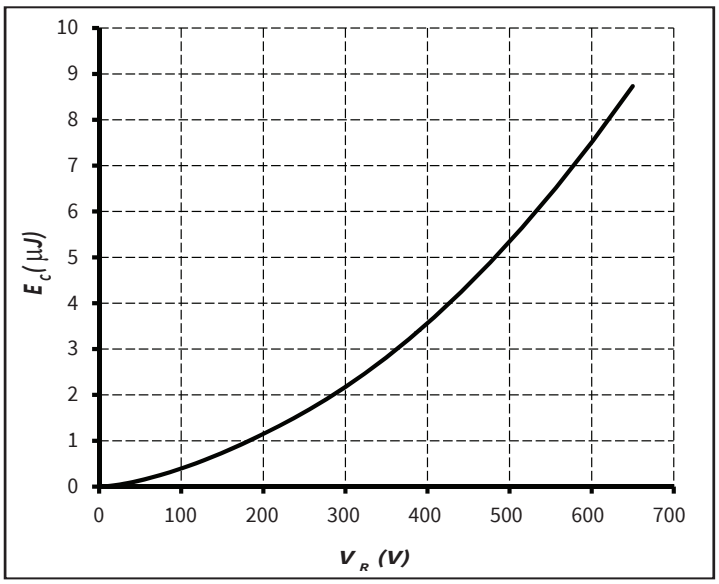


Figure 7. Capacitance Stored Energy

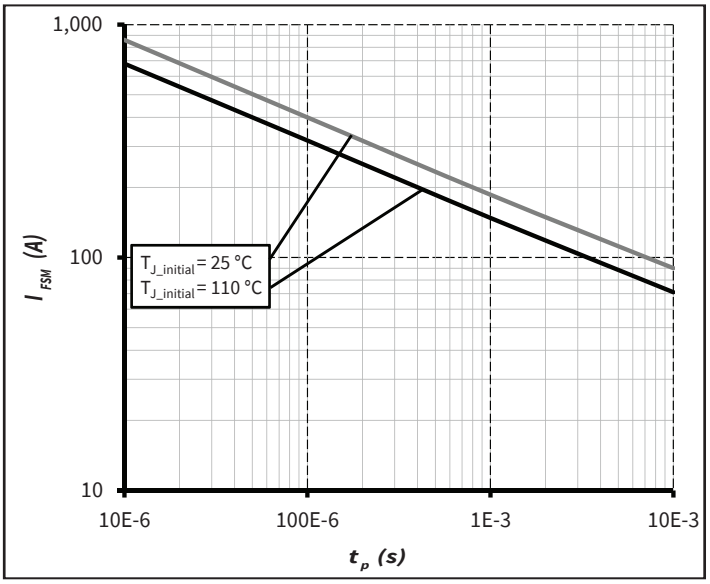


Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

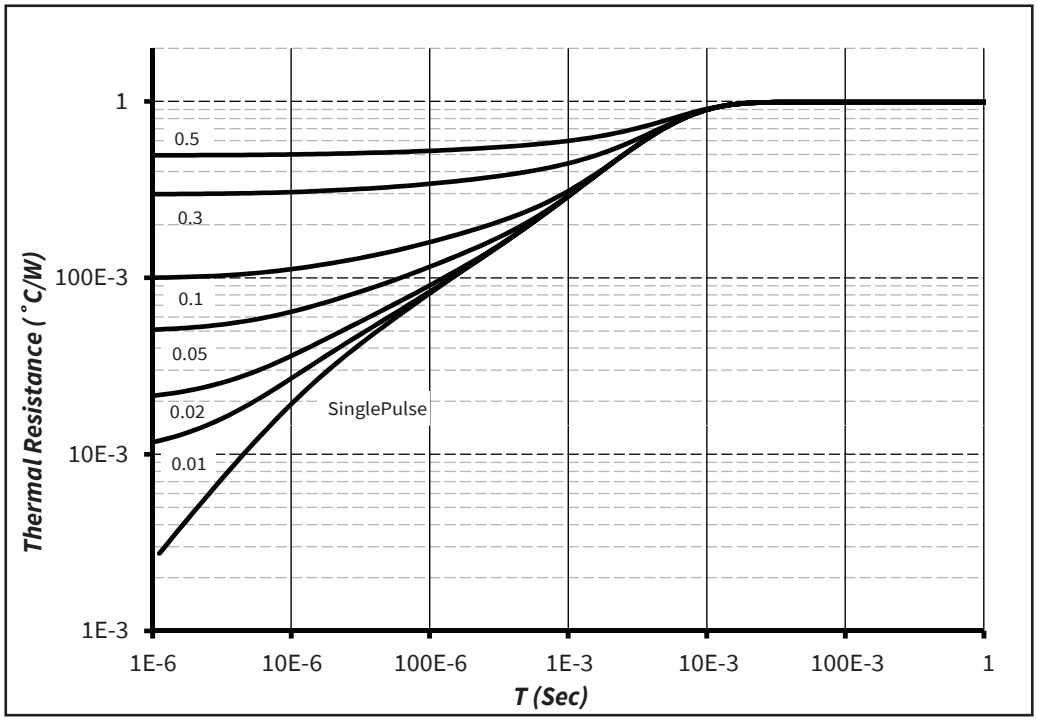
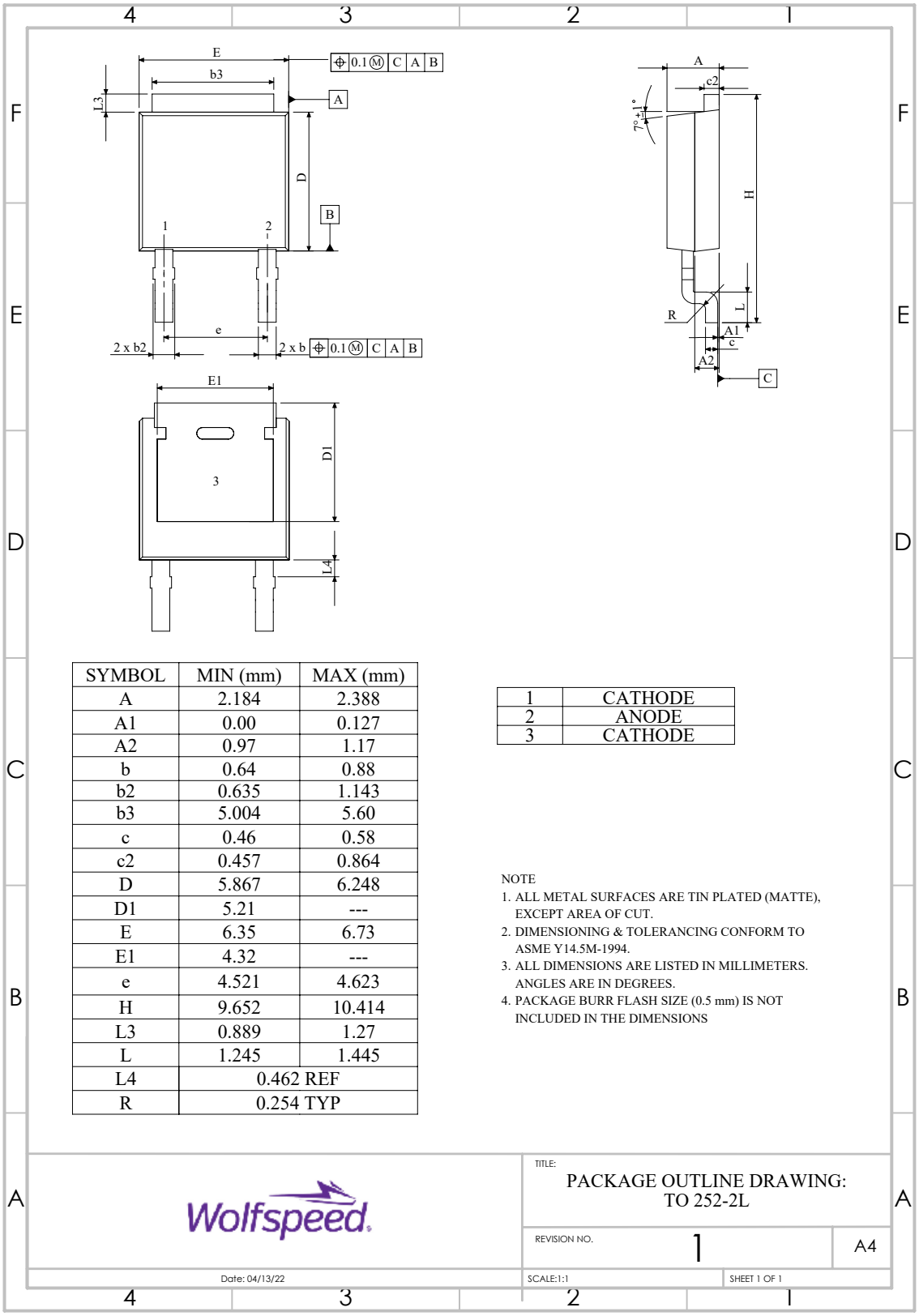


Figure 9. Transient Thermal Impedance

Package Dimensions

Package: TO-252-2



SYMBOL	MIN (mm)	MAX (mm)
A	2.184	2.388
A1	0.00	0.127
A2	0.97	1.17
b	0.64	0.88
b2	0.635	1.143
b3	5.004	5.60
c	0.46	0.58
c2	0.457	0.864
D	5.867	6.248
D1	5.21	---
E	6.35	6.73
E1	4.32	---
e	4.521	4.623
H	9.652	10.414
L3	0.889	1.27
L	1.245	1.445
L4	0.462 REF	
R	0.254 TYP	

1	CATHODE
2	ANODE
3	CATHODE

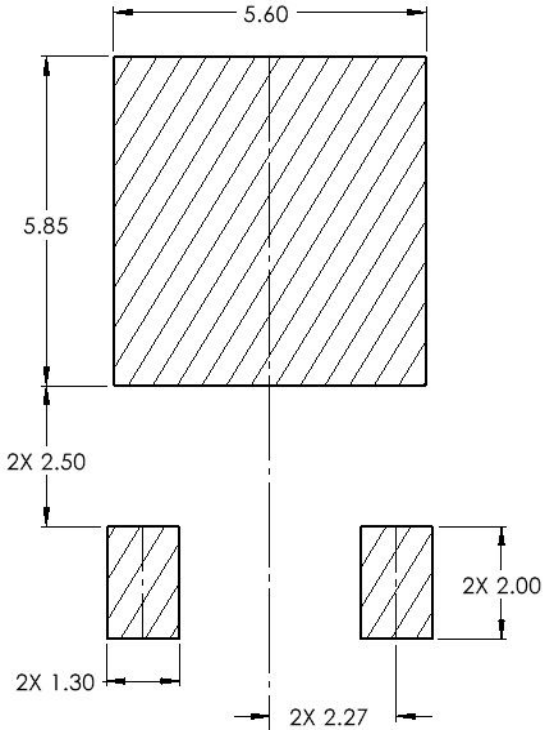
NOTE
1. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.
2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

TITLE: PACKAGE OUTLINE DRAWING: TO 252-2L

REVISION NO. 1 | A4

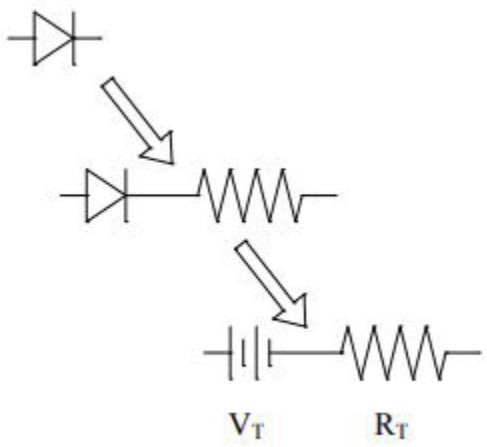
Date: 04/13/22 | SCALE:1:1 | SHEET 1 OF 1

Recommended Solder Pad Layout



Part Number	Package	Marking
C3D10065E	TO-252-2	C3D10065

Diode Model



$$Vf_T = V_T + If * R_T$$

$$V_T = 0.94 + (T_J * -1.3*10^{-3})$$
$$R_T = 0.044 + (T_J * 4.4*10^{-4})$$

Note: T_J = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Revision History

Document Version	Date of Release	Description of Changes
5	January-2018	N/A
6	August-2023	Update Package Drawing, Update Landing Pad Updated Branding, Removed AEC-Q101 Banner
7	October-2023	Corrected solder pad layout and diode model

Notes & Disclaimer

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REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact your Wolfspeed representative to ensure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

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