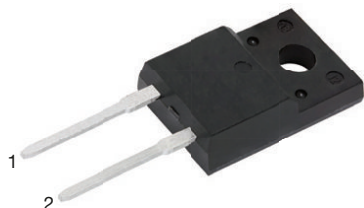
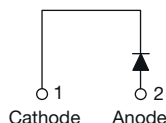


Hyperfast Rectifier, 8 A FRED Pt®



TO-220 FullPAK 2L



VS-8ETH06FP-N3



RoHS
COMPLIANT
HALOGEN
FREE

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ($V_{INS} = 2500 V_{RMS}$)
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: TO-FullPAK 2L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	8 A
V_R	600 V
V_F at I_F	1.3 V
t_{rr} typ.	18 ns
T_J max.	175 °C
Package	TO-220 FullPAK 2L
Circuit configuration	Single

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 108\text{ °C}$	8	A
Non-repetitive peak surge current	I_{FSM}		100	
Repetitive peak forward current	I_{FM}		16	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu A$	600	-	-	V
Forward voltage	V_F	$I_F = 8\text{ A}$	-	2.0	2.4	
		$I_F = 8\text{ A}, T_J = 150\text{ °C}$	-	1.3	1.8	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	0.03	50	μA
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	55	500	
Junction capacitance	C_T	$V_R = 600\text{ V}$	-	17	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	18	22	ns
		$I_F = 8\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	20	25	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	25	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	40	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.4	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	4.8	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	25	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	120	-	
Reverse recovery time	t_{rr}	$I_F = 8\text{ A}$	-	33	-	ns
Peak recovery current	I_{RRM}	$dI_F/dt = 600\text{ A}/\mu\text{s}$	-	12	-	A
Reverse recovery charge	Q_{rr}	$V_R = 390\text{ V}$	-	220	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction-to-case	R_{thJC}		-	3.4	4.3	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-to-ambient per leg	R_{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case-to-heatsink	R_{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	8ETH06FP			

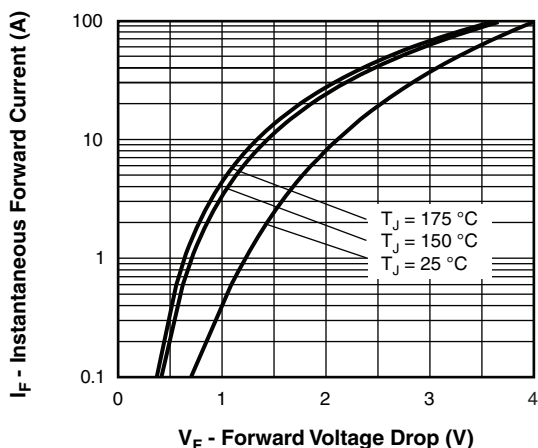


Fig. 1 - Typical Forward Voltage Drop Characteristics

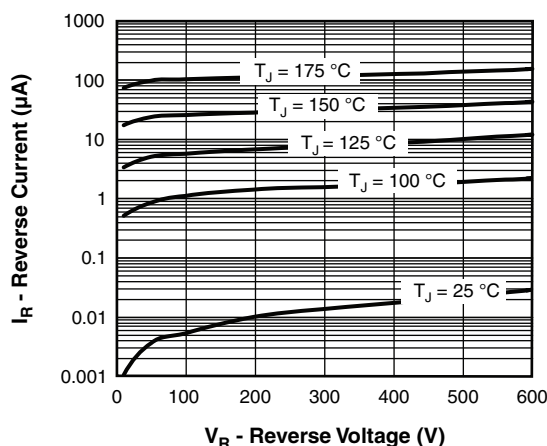


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

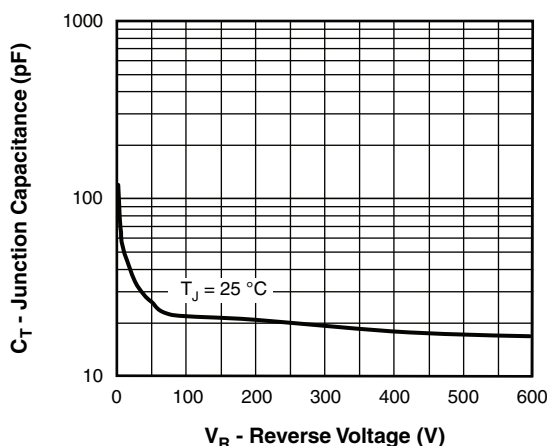


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

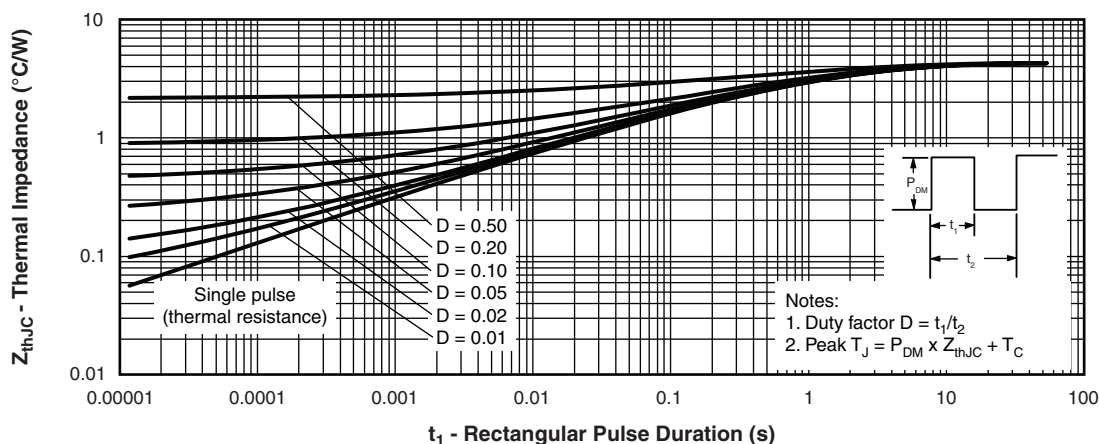
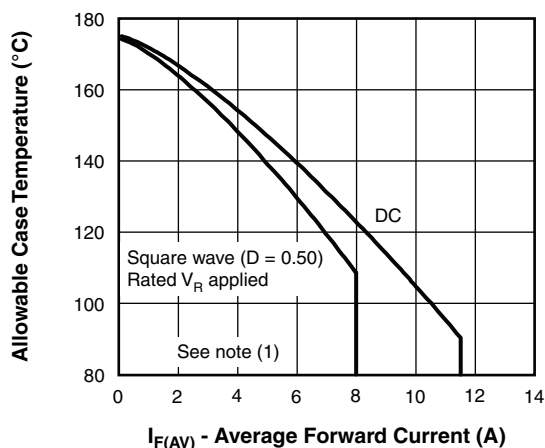

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

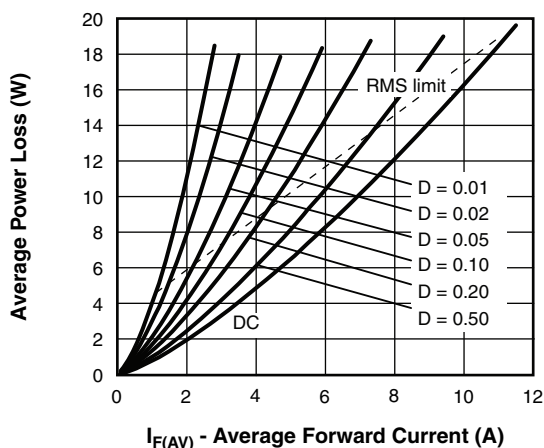


Fig. 6 - Forward Power Loss Characteristics

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

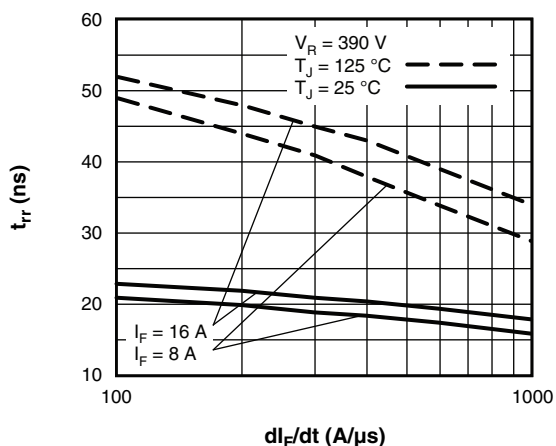
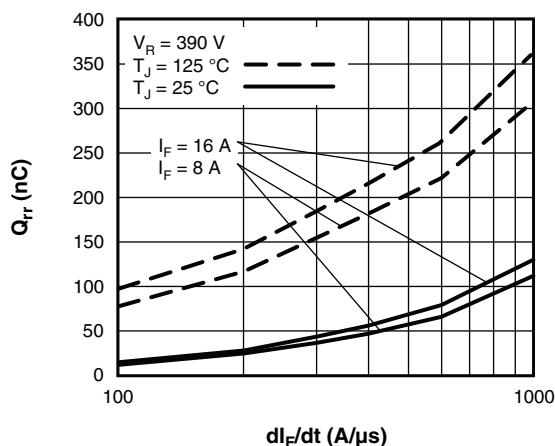
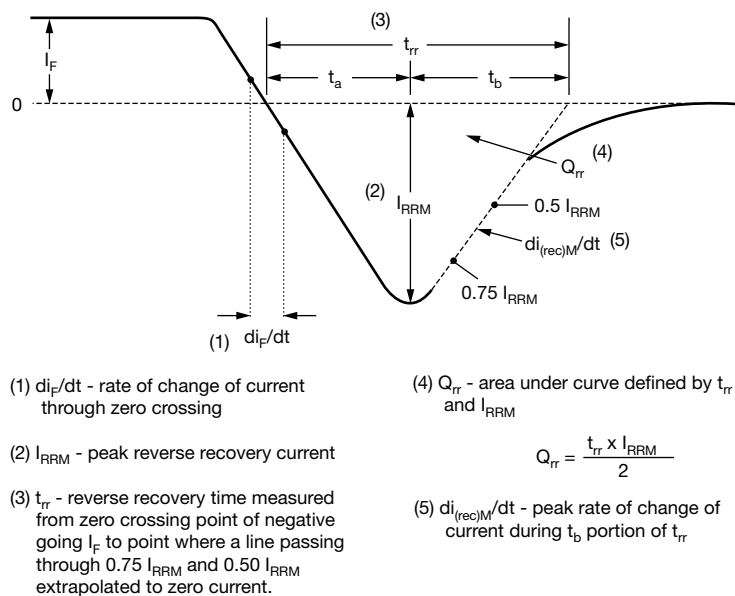

Fig. 7 - Typical Reverse Recovery Time vs. di_F/dt

Fig. 8 - Typical Stored Charge vs. di_F/dt


Fig. 9 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	VS-	8	E	T	H	06	FP	-N3
	1	2	3	4	5	6	7	8
1	- Vishay Semiconductors product							
2	- Current rating (8 = 8 A)							
3	- E = single							
4	- T = TO-220, D ² PAK (TO-263AB)							
5	- H = hyperfast recovery							
6	- Voltage rating (06 = 600 V)							
7	- FP = TO-220 FullPAK 2L							
8	- Environmental digit: -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free							

ORDERING INFORMATION (Example)

PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-8ETH06FP-N3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?96157
Part marking information	www.vishay.com/doc?95392



2L TO-220 FullPAK

DIMENSIONS in millimeters





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