

# Switching Diode

## BAS116TT1G

### Features

- Low Leakage Current Applications
- Medium Speed Switching Times
- Available in 8 mm Tape and Reel
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

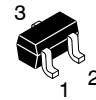
Rating	Symbol	Max	Unit
Continuous Reverse Voltage	$V_R$	75	V
Peak Forward Current	$I_F$	200	mA
Peak Forward Surge Current	$I_{FM}(\text{surge})$	500	mA

### THERMAL CHARACTERISTICS

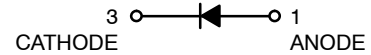
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^\circ\text{C}$ Derated above $25^\circ\text{C}$	$P_D$	225	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	$^\circ\text{C/W}$
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^\circ\text{C}$ Derated above $25^\circ\text{C}$	$P_D$	360	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	345	$^\circ\text{C/W}$
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

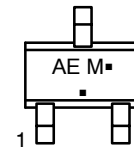
1. FR-4 @ Minimum Pad
2. FR-4 @  $1.0 \times 1.0$  Inch Pad



CASE 463  
SOT-416  
STYLE 2



### MARKING DIAGRAM



AE = Specific Device Code  
M = Date Code  
■ = Pb-Free Package

### ORDERING INFORMATION

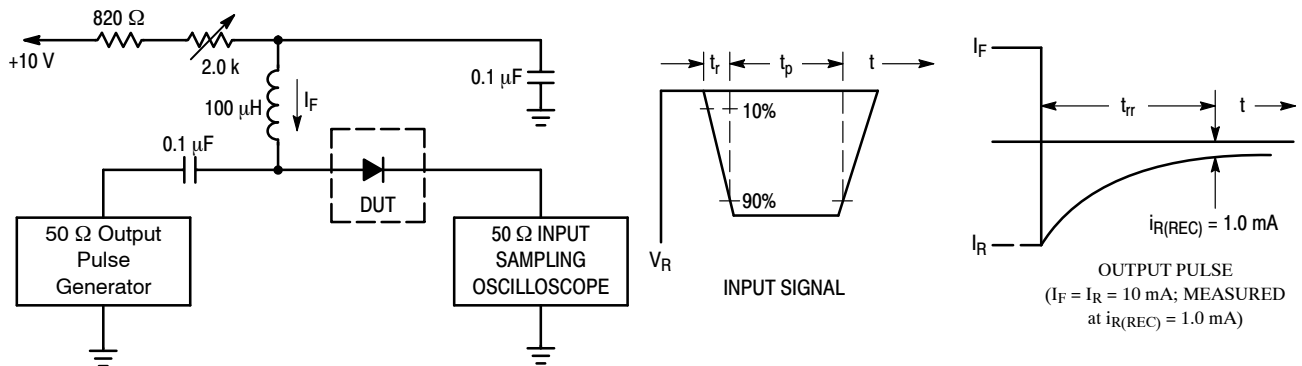
Device	Package	Shipping <sup>†</sup>
BAS116TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BAS116TT1G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Reverse Breakdown Voltage (I <sub>BR</sub> = 100 µAdc)	V <sub>(BR)</sub>	75	–	Vdc
Reverse Voltage Leakage Current (V <sub>R</sub> = 75 Vdc) (V <sub>R</sub> = 75 Vdc, T <sub>J</sub> = 150°C)	I <sub>R</sub>	–	5.0 80	nAdc
Forward Voltage (I <sub>F</sub> = 1.0 mAdc) (I <sub>F</sub> = 10 mAdc) (I <sub>F</sub> = 50 mAdc) (I <sub>F</sub> = 150 mAdc)	V <sub>F</sub>	–	900 1000 1100 1250	mV
Diode Capacitance (V <sub>R</sub> = 0 V, f = 1.0 MHz)	C <sub>D</sub>	–	2.0	pF
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mAdc) (Figure 1)	t <sub>rr</sub>	–	3.0	µs



1. A 2.0 kΩ variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.
2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA.
3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

## TYPICAL CHARACTERISTICS

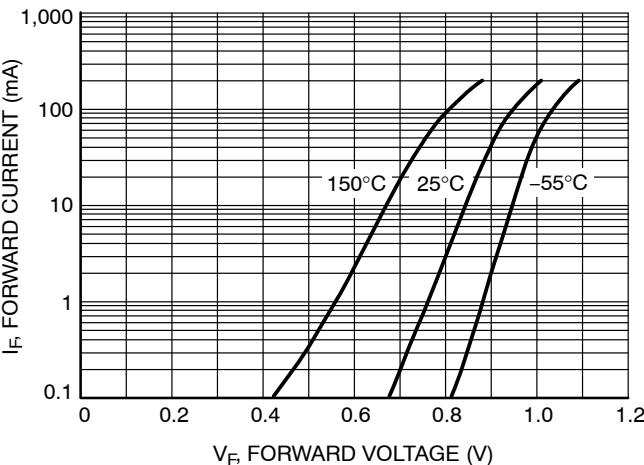


Figure 2. Forward Voltage

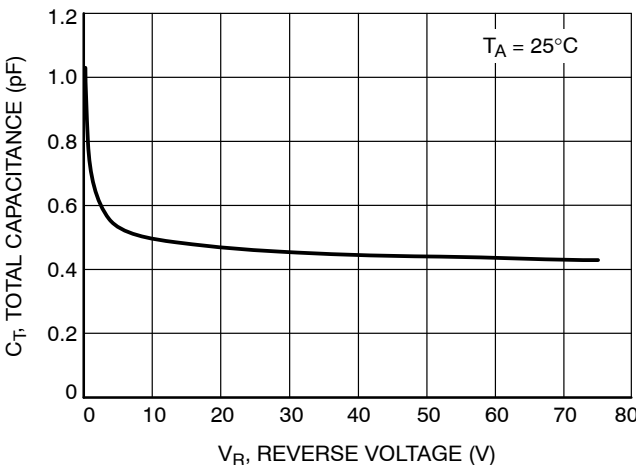


Figure 3. Capacitance

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

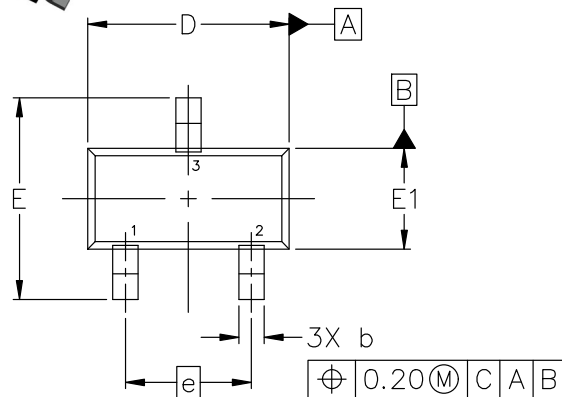


**SC75-3 1.60x0.80x0.80, 1.00P**  
CASE 463  
ISSUE H

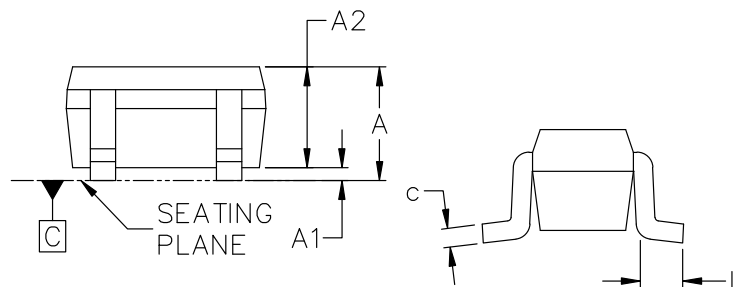
DATE 01 FEB 2024

## NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.



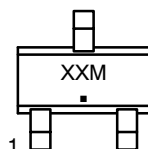
TOP VIEW



SIDE VIEW

END VIEW

## GENERIC MARKING DIAGRAM\*



XX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:  
PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

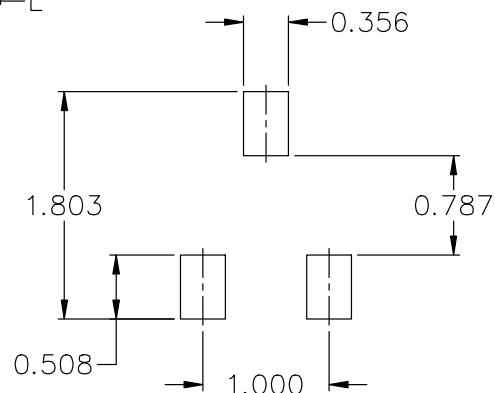
STYLE 2:  
PIN 1. ANODE  
2. N/C  
3. CATHODE

STYLE 3:  
PIN 1. ANODE  
2. ANODE  
3. CATHODE

STYLE 4:  
PIN 1. CATHODE  
2. CATHODE  
3. ANODE

STYLE 5:  
PIN 1. GATE  
2. SOURCE  
3. DRAIN

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.80	0.90
A1	0.00	0.05	0.10
A2	0.80 REF.		
b	0.15	0.20	0.30
c	0.10	0.15	0.25
D	1.55	1.60	1.65
E	1.50	1.60	1.70
E1	0.70	0.80	0.90
e	1.00 BSC		
L	0.10	0.15	0.20



## RECOMMENDED MOUNTING FOOTPRINT\*

- \* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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