



VOIDLESS-HERMETICALLY SEALED SURFACE MOUNT FAST RECOVERY GLASS RECTIFIERS

Qualified per MIL-PRF-19500/411

Qualified Levels: JAN, JANTX, JANTXV and JANS

DESCRIPTION

This "fast recovery" rectifier diode series is military qualified and is ideal for high-reliability applications where a failure cannot be tolerated. These industry-recognized 3.0 amp rated rectifiers for working peak reverse voltages from 50 to 600 volts are hermetically sealed with voidless-glass construction using an internal "Category 1" metallurgical bond. These devices are also available in axial-leaded packages for thru-hole mounting. Microsemi also offers numerous other rectifier products to meet higher and lower current ratings with various recovery time speed requirements including fast and ultrafast device types in both through-hole and surface mount packages.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- Surface mount equivalent of JEDEC registered 1N5415 thru 1N5420 series.
- Voidless hermetically sealed glass package.
- Quadruple-layer passivation.
- Internal "Category 1" metallurgical bonds.
- Working peak reverse voltage 50 to 600 volts.
- JAN, JANTX, JANTXV and JANS qualifications available per MIL-PRF-19500/411.
- RoHS compliant versions available (commercial grade only).

APPLICATIONS / BENEFITS

- Fast recovery 3 amp 50 to 600 volt rectifiers.
- Military and other high-reliability applications.
- General rectifier applications including bridges, half-bridges, catch diodes, etc.
- High forward surge current capability.
- Extremely robust construction.
- · Low thermal resistance.
- Controlled avalanche with peak reverse power capability.
- Inherently radiation hard as described in Microsemi "MicroNote 050".

MAXIMUM RATINGS

Parameters/Test Conditions		Symbol	Value	Unit
Junction and Storage Temperature		T _J and T _{STG}	-65 to +175	°C
Thermal Resistance Junction-to-End Cap		R _{OJEC}	6.5	°C/W
Forward Surge Current @ 8.3 ms half-sine		I _{FSM}	80	Α
Average Rectified Forward Current (3) °C	@ $T_A = +55$ °C @ $T_A = +100$	I _O (1, 2) I _O (2)	3 2	A
Working Peak Reverse Voltage	1N5415US 1N5416US 1N5417US 1N5418US 1N5419US 1N5420US	V _{RWM}	50 100 200 400 500 600	V
Maximum Reverse Recovery Time (5)	1N5415US 1N5416US 1N5417US 1N5418US 1N5419US 1N5420US	t _{rr}	150 150 150 150 250 400	ns
Solder Temperature @ 10 s		T _{SP}	260	°C

See notes on next page.



"B" SQ-MELF (D-5B) Package

Also available in:

"B" Package (axial-leaded)

1N5415 – 1N5420

MSC - Lawrence

6 Lake Street, Lawrence, MA 01841 Tel: 1-800-446-1158 or (978) 620-2600 Fax: (978) 689-0803

MSC - Ireland

Gort Road Business Park, Ennis, Co. Clare, Ireland Tel: +353 (0) 65 6840044 Fax: +353 (0) 65 6822298

Website:

www.microsemi.com



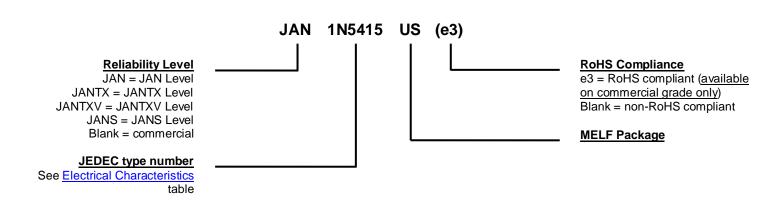
MAXIMUM RATINGS

- **Notes:** 1. Derate linearly at 22 mA/ $^{\circ}$ C for 55 $^{\circ}$ C \leq T_A \leq 100 $^{\circ}$ C.
 - 2. Above $T_A = 100$ °C, derate linearly at 26.7 mA/°C to zero at $T_A = 175$ °C.
 - These ambient ratings are for PC boards where thermal resistance from mounting point to ambient is sufficiently controlled where T_{J(max)} does not exceed 175 °C.

MECHANICAL and PACKAGING

- CASE: Hermetically sealed voidless hard glass with tungsten slugs.
- TERMINALS: End caps are copper with tin/lead (Sn/Pb) finish. Note: Previous inventory had solid silver with tin/lead (Sn/Pb) finish. RoHS compliant matte-tin is available for commercial grade only.
- · MARKING: Cathode band only.
- POLARITY: Cathode indicated by band.
- TAPE & REEL option: Standard per EIA-481-B. Contact factory for quantities.
- WEIGHT: 539 milligrams.
- See <u>Package Dimensions</u> and recommended <u>Pad Layout</u> on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS		
Symbol	Definition	
V_{BR}	Minimum Breakdown Voltage: The minimum voltage the device will exhibit at a specified current.	
V_{RWM}	Working Peak Reverse Voltage: The maximum peak voltage that can be applied over the operating temperature range excluding all transient voltages (ref JESD282-B).	
Io	Average Rectified Output Current: The Output Current averaged over a full cycle with a 50 Hz or 60 Hz sine-wave input and a 180 degree conduction angle.	
V_{F}	Maximum Forward Voltage: The maximum forward voltage the device will exhibit at a specified current.	
I_R	Maximum Reverse Current: The maximum reverse (leakage) current that will flow at the specified voltage and temperature.	
t _{rr}	Reverse Recovery Time: The time interval between the instant the current passes through zero when changing from the forward direction to the reverse direction and a specified decay point after a peak reverse current occurs.	



ELECTRICAL CHARACTERISTICS

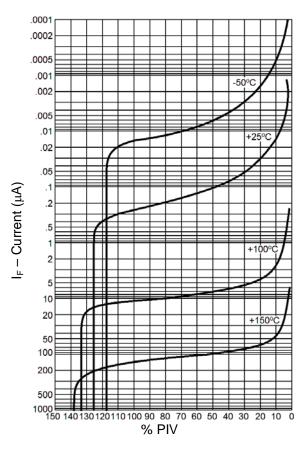
TYPE	MINIMUM BREAKDOWN VOLTAGE V _{BR} @ 50 μA	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		ERSE RENT	CAPACITANCE C V _R @ 4 V	
	Volts	MIN. Volts	MAX. Volts	25 °C μΑ	100 °C μΑ	pF
1N5415US	55	0.6	1.5	1.0	20	550
1N5416US	110	0.6	1.5	1.0	20	430
1N5417US	220	0.6	1.5	1.0	20	250
1N5418US	440	0.6	1.5	1.0	20	165
1N5419US	550	0.6	1.5	1.0	20	140
1N5420US	660	0.6	1.5	1.0	20	120

NOTE 1: $I_F = 0.5 \text{ A}$, $I_{RM} = 1 \text{ A}$, $I_{R(REC)} = 0.250 \text{ A}$.



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FIGURE 1
Typical Reverse Current vs. PIV

FIGURE 2
Maximum Thermal Impedance

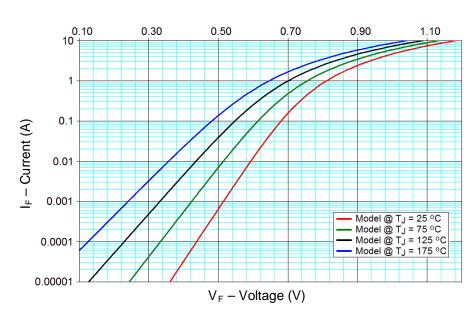
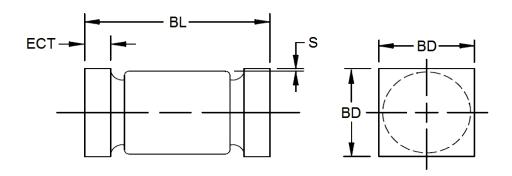


FIGURE 3
Typical Forward Current vs. Forward Voltage



PACKAGE DIMENSIONS

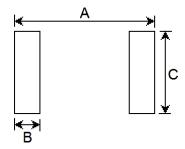


	INCH		MILLIMETERS	
	MIN	MAX	MIN	MAX
BL	0.200	0.225	5.08	5.72
BD	0.137	0.148	3.48	3.76
ECT	0.019	0.028	0.48	0.71
S	0.003		0.08	

NOTES:

- 1. Dimensions are in inches.
- 2. Millimeter equivalents are given for general information only.
- 3. Dimensions are pre-solder dip.
- 4. Minimum clearance of glass body to mounting surface on all orientations.
- 5. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.
- 6. This package outline has also previously been identified as "D-5B".

PAD LAYOUT



	INCH	MILLIMETERS
Α	0.288	7.32
В	0.070	1.78
С	0.155	3.94

Note: If mounting requires adhesive separate from the solder, an additional 0.080 inch diameter contact may be placed in the center between the pads as an optional spot for cement.