General Purpose NPN Amplifier Transistor

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	60	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current – Continuous	I _C	500	mAdc
Collector Current – Peak	I _{C(P)}	1.0	Adc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P _D	200	mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	−55 ~ + 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

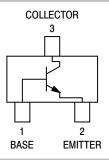


ON Semiconductor®

http://onsemi.com



SC-59 CASE 318D STYLE 1



MARKING DIAGRAM



WR = Specific Device Code

M = Date Code

■ = Pb–Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MSD-602RT1G	SC-59 (Pb-Free)	3,000 / Tape & Reel
SMSD-602RT1G	SC-59 (Pb-Free)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS $(T_A = 25^{\circ}C)$

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage (I _C = 10 mA, I _B = 0)	V _(BR) CEO	50	_	V
Collector–Base Breakdown Voltage ($I_C = 10 \mu A, I_E = 0$)	V _(BR) CBO	60	_	V
Emitter–Base Breakdown Voltage ($I_E = 10 \mu A, I_C = 0$)	V _{(BR)EBO}	7.0	_	V
Collector–Base Cutoff Current (V _{CB} = 20 V, I _E = 0)	I _{CBO}	-	0.1	μΑ
DC Current Gain (Note 1) (V _{CE} = 10 V, I _C = 150 mA) (V _{CE} = 10 V, I _C = 500 mA)	h _{FE1} h _{FE2}	120 40	240 -	-
Collector–Emitter Saturation Voltage (I _C = 300 mA, I _B = 30 mA)	V _{CE(sat)}	-	0.6	V
Base–Emitter On Voltage (I _C = 300 mA, V _{CE} = 5 V)	V _{BE(on)}	-	1.0	V
Base–Emitter Saturation Voltage (I _C = 300 mA, I _B = 30 mA)	V _{BE(sat)}	_	1.0	V
Output Capacitance (V _{CB} = 10 V, I _E = 0, f = 1.0 MHz)	C _{ob}	_	15	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width $\leq 300 \, \mu s$, D.C. $\leq 2\%$.

TYPICAL CHARACTERISTICS

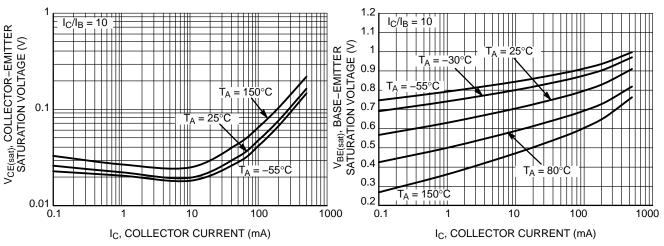


Figure 1. Collector-Emitter Saturation Voltage vs. Collector Current

Figure 2. Base-Emitter Saturation Voltage vs. **Collector Current**

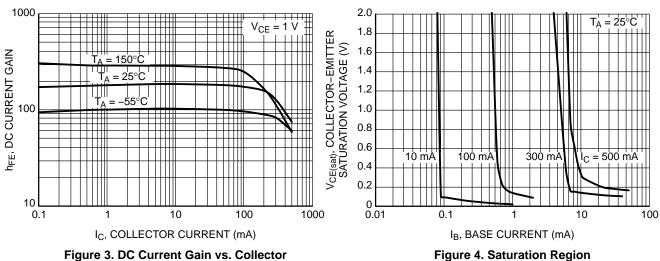


Figure 3. DC Current Gain vs. Collector Current

1.0 100 V_{BE(on)}, BASE-EMITTER VOLTAGE (V) -30^o€ C_{ibo} 0.9 0.8 CAPACITANCE (pF) 0.7 C_{obo} 0.6 10 = 80°C 0.5 0.3 $V_{CE} = 5 V$ 0.2 1000 0.1 100 I_C, COLLECTOR CURRENT (mA) V_R, REVERSE VOLTAGE (V)

Figure 5. Base-Emitter Turn-On Voltage vs. **Collector Current**

Figure 6. Capacitance

TYPICAL CHARACTERISTICS

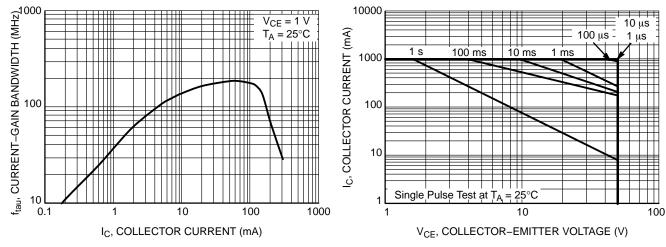


Figure 7. Current Gain Bandwidth Product vs.
Collector Current

Figure 8. Safe Operating Area

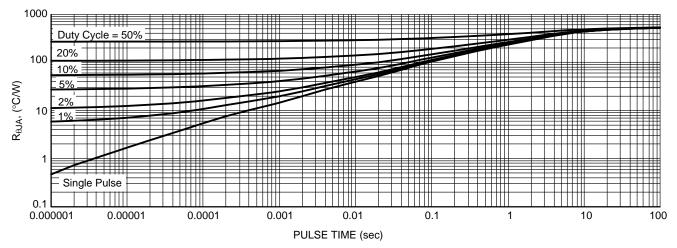


Figure 9. Thermal Response





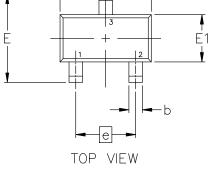
SC-59-3 2.90x1.50x1.15, 1.90P CASE 318D **ISSUE J**

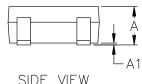
DATE 15 FEB 2024

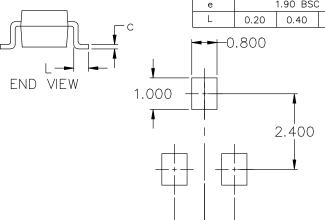
NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME
- ALL DIMENSION ARE IN MILLIMETERS.

	MILLIMETERS			
DIM	MIN.	NOM.	MAX.	
Α	1.00	1.15	1.30	
A1	0.01	0.06	0.10	
b	0.35	0.43	0.50	
С	0.09	0.14	0.18	
D	2.70	2.90	3.10	
E	2.50	2.80	3.00	
E1	1.30	1.50	1.70	
е	1.90 BSC			
L	0.20	0.40	0.60	





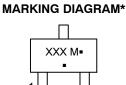


RECOMMENDED MOUNTING FOOTPRINT*

0.950 - - - 0.950

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.





GENERIC

XXX = Specific Device Code

М = Date Code

= Pb-Free Package*

(*Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE
2. EMITTER	2. N.C.	ANODE
3 COLLECTOR	3 CATHODE	3 CATHODE

STYLE 4: STYLE 5: STYLE 6: PIN 1. CATHODE PIN 1. CATHODE PIN 1 ANODE 2. N.C. 3. ANODE 2. CATHODE 2. CATHODE 3. ANODE/CATHODE 3. ANODE

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DESCRIPTION:	SC-59-3 2.90x1.50x1.15, 1.90P		PAGE 1 OF 1	

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