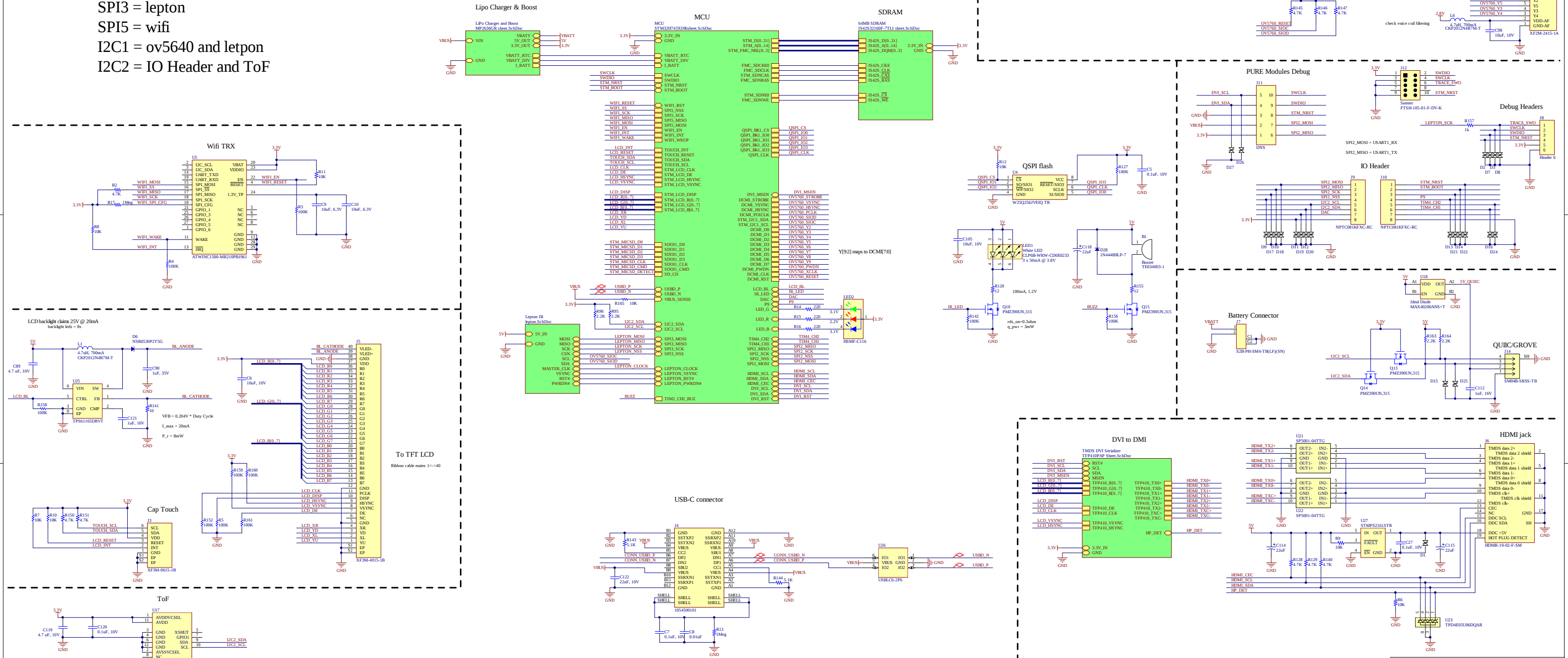


PureThermal OpenMV

PRELIMINARY

SPI2 = I/O Header
SPI3 = lepton
SPI5 = wifi
I2C1 = ov5640 and lepton
I2C2 = IO Header and ToF



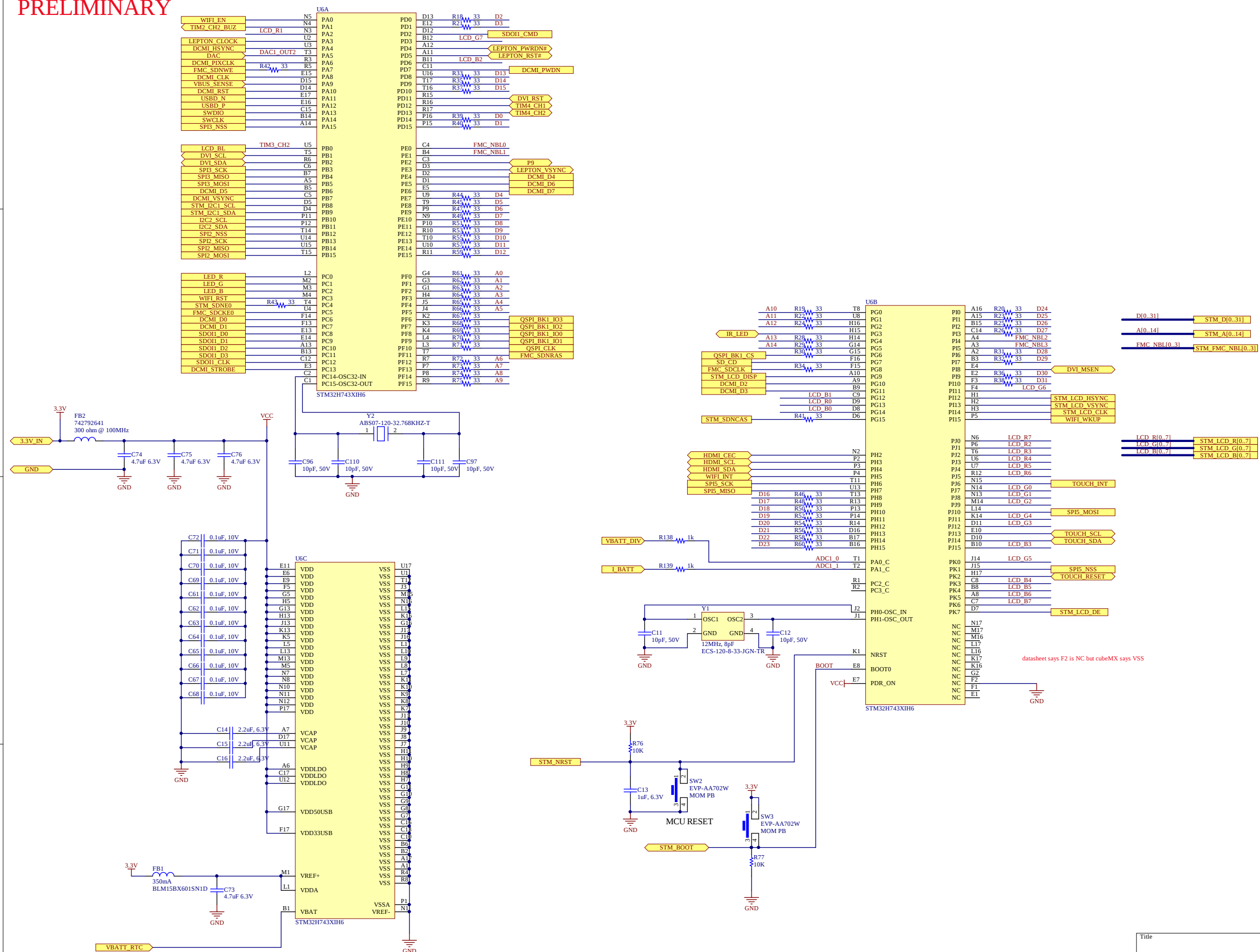
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PRELIMINARY

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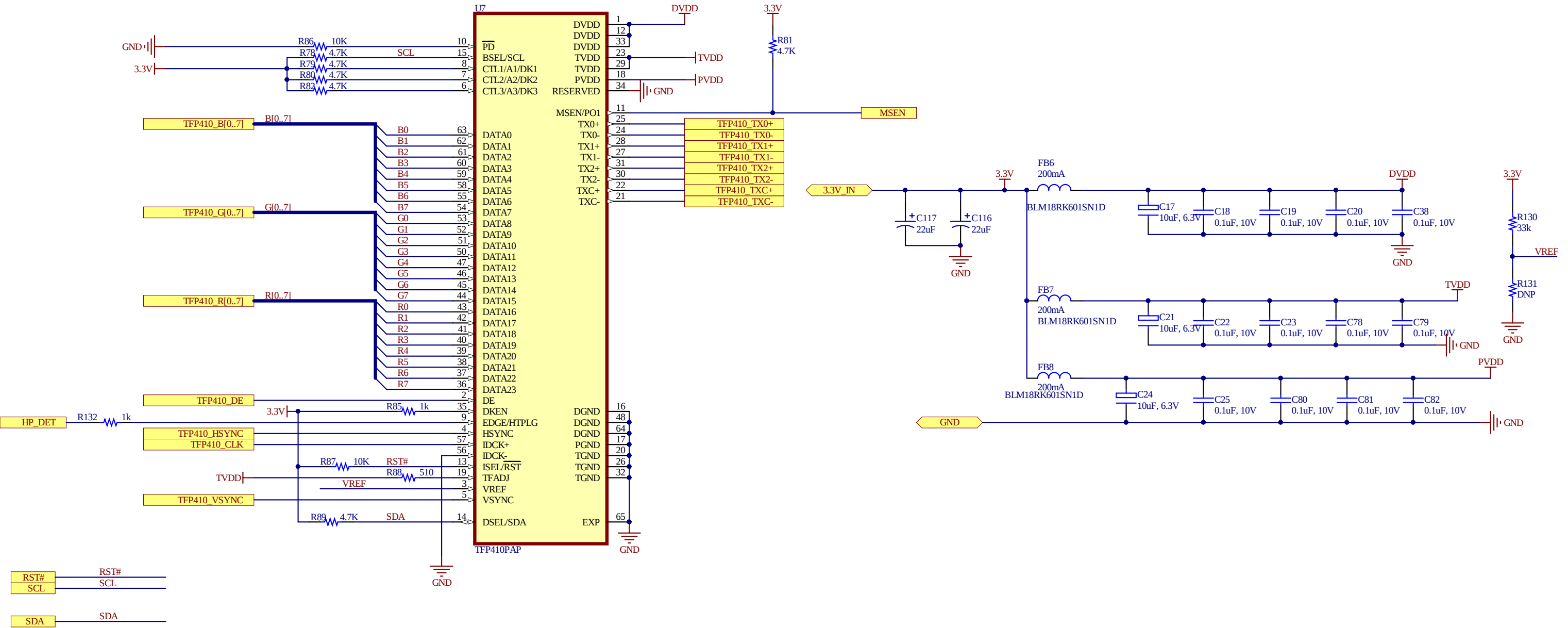
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STM32 MCU



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PRELIMINARY

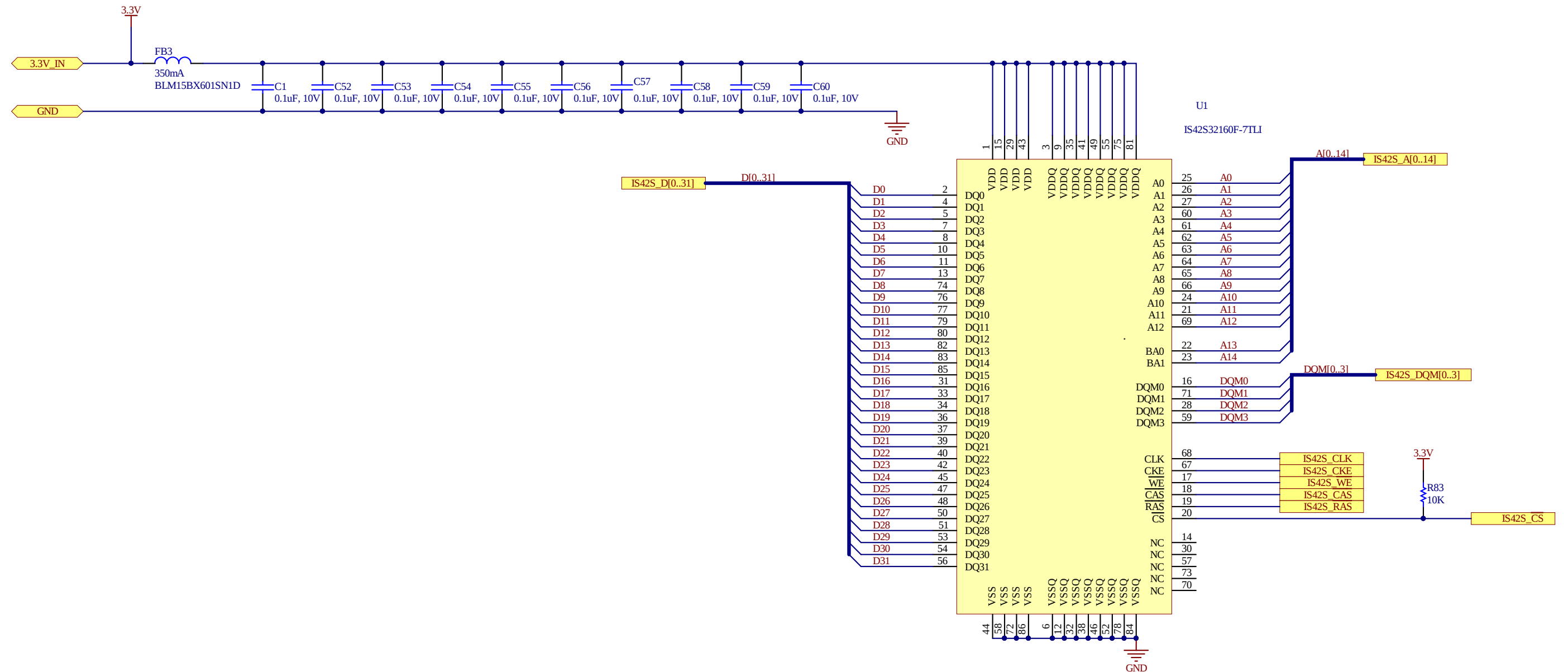


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PureThermal OpenMV

SDRAM

PRELIMINARY

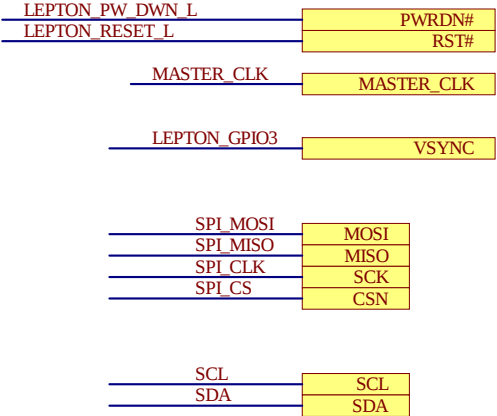
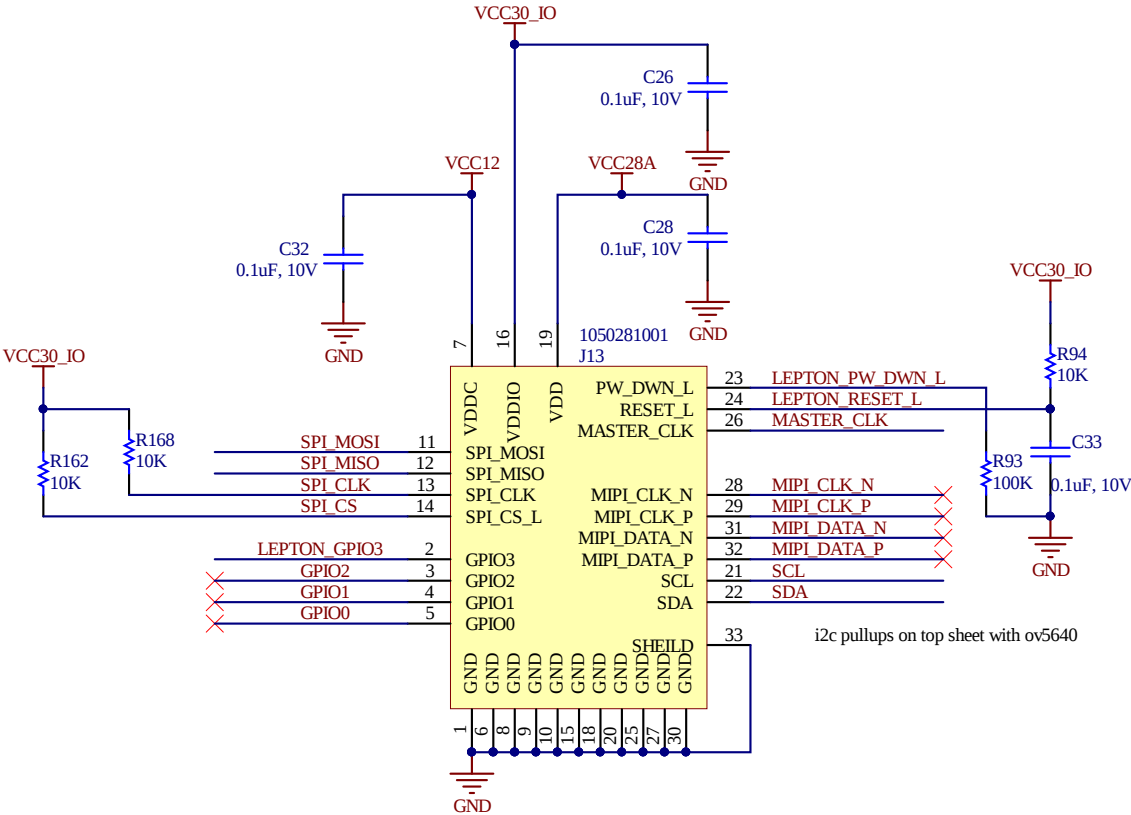
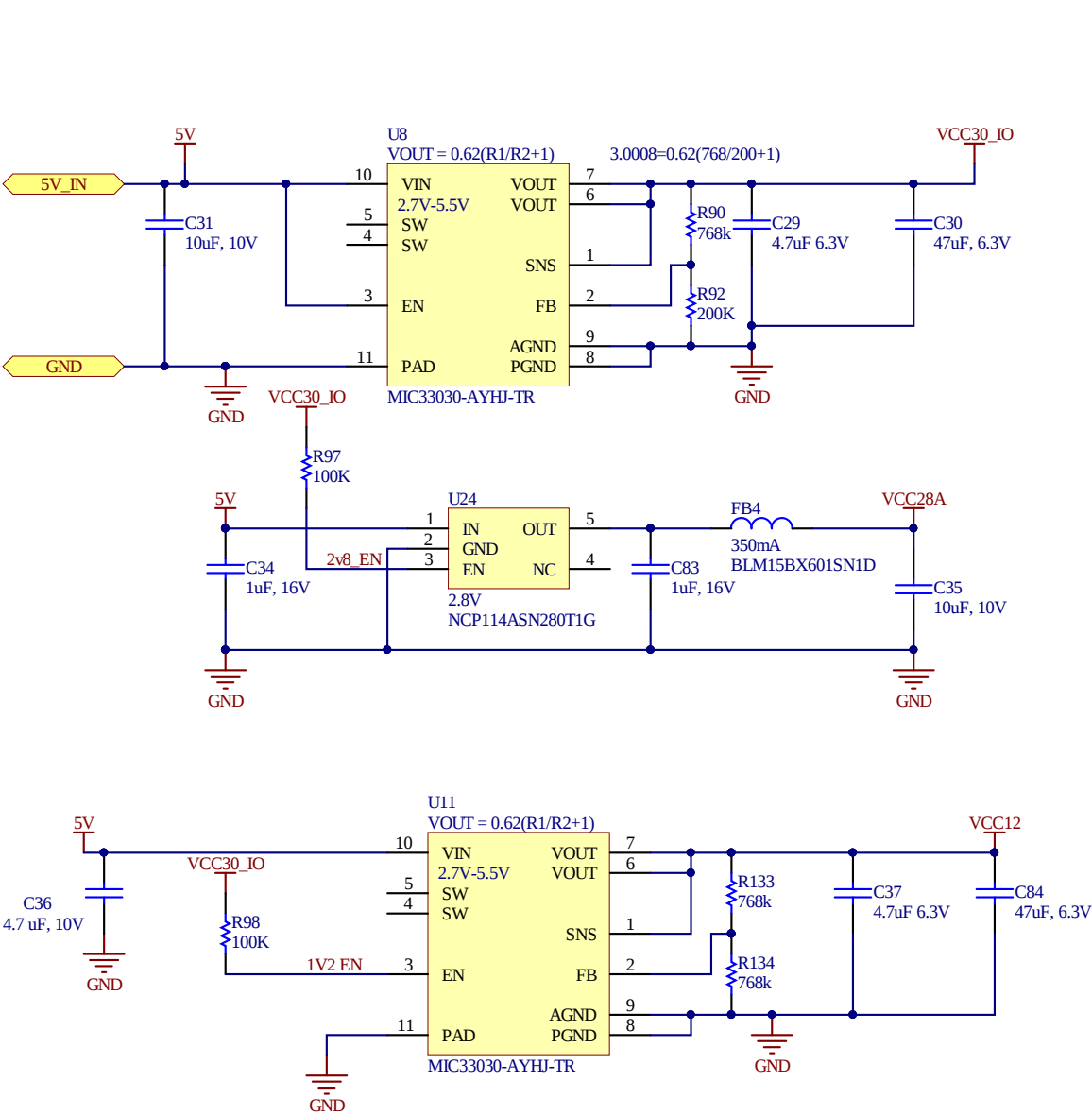


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PureThermal OpenMV

FLIR Lepton

PRELIMINARY



Title		
Size	Number	Revision
B		
Date:	5/18/2021	Sheet of
File:	C:\Users\lepton.SchDoc	Drawn By: