

BLF984P; BLF984PS

Power LDMOS transistor

Rev. 2 — 29 July 2020

AMPELEON

Product data sheet

1. Product profile

1.1 General description

A 450 W LDMOS RF power transistor for broadcast Doherty, class-AB transmitter and industrial applications. The excellent ruggedness of this device makes it ideal for digital and analog transmitter applications in the frequency range from 400 MHz to 860 MHz.

Table 1. Typical information

RF performance at $T_{case} = 25^\circ\text{C}$; $V_{DS} = 50\text{ V}$ in a class-AB test circuit, unless otherwise specified, typical values.

| Test signal | f (MHz) | $P_{L(AV)}$ (W) | G_p (dB) | η_D (%) | $\text{IMD}_{\text{shldr}}$ (dBc) | PAR (dB) |
|--|------------|--------------------|---------------|-----------------|--------------------------------------|-------------|
| RF performance in a common source 700 MHz narrowband test circuit | | | | | | |
| DVB-T (8k OFDM) | 700 | 80 | 22.5 | 34 | -30 [1] | 8 [2] |

[1] Measured [dBc] with delta marker at 4.3 MHz from center frequency.

[2] PAR (of output signal) at 0.01 % probability on CCDF; PAR of input signal = 9.5 dB at 0.01 % probability on CCDF.

Table 2. Typical information

RF performance at $T_{case} = 25^\circ\text{C}$; $V_{DS} = 50\text{ V}$; $I_{Dq} = 2 \times 325\text{ mA}$; $t_p = 100\text{ }\mu\text{s}$; $\delta = 10\%$; in a class-AB test circuit, unless otherwise specified.

| Test signal | f (MHz) | P_L (W) | G_p (dB) | η_D (%) | RL_{in} (dB) |
|--|------------|--------------|---------------|-----------------|---------------------------------|
| RF performance in a common source 700 MHz narrowband test circuit | | | | | |
| pulsed CW | 700 | 450 | 22 | 67.5 | 16 |

1.2 Features and benefits

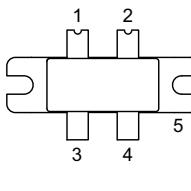
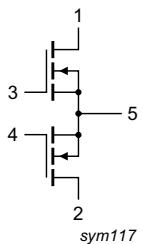
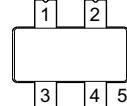
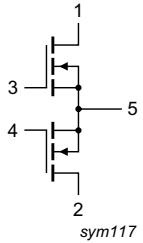
- Designed for broadband and Doherty operation
- High efficiency
- Integrated double sided ESD protection
- Excellent ruggedness
- High power gain
- Excellent reliability
- Easy power control
- Excellent stability
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

- Broadcast transmitter applications in the UHF band
- Digital and analog broadcasting
- Industrial, scientific and medical applications
- Applicable at frequencies from 400 MHz to 860 MHz

2. Pinning information

Table 3. Pinning

| Pin | Description | Simplified outline | Graphic symbol |
|----------------------------|-------------|--------------------|--|
| BLF984P (SOT1121A) | | | |
| 1 | drain1 | | |
| 2 | drain2 | | |
| 3 | gate1 | | |
| 4 | gate2 | | |
| 5 | source | [1] |   <i>sym117</i> |
| BLF984PS (SOT1121B) | | | |
| 1 | drain1 | | |
| 2 | drain2 | | |
| 3 | gate1 | | |
| 4 | gate2 | | |
| 5 | source | [1] |   <i>sym117</i> |

[1] Connected to flange.

3. Ordering information

Table 4. Ordering information

| Type number | Package | | |
|-------------|---------|--|----------|
| | Name | Description | Version |
| BLF984P | - | flanged LDMOST ceramic package; 2 mounting holes; 4 leads | SOT1121A |
| BLF984PS | - | earless flanged ceramic package; 4 leads | SOT1121B |

4. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|----------------------|------------|-----|------|------|
| V_{DS} | drain-source voltage | | - | 108 | V |
| V_{GS} | gate-source voltage | | -6 | +11 | V |
| T_{stg} | storage temperature | | -65 | +150 | °C |
| T_j | junction temperature | [1] | - | 225 | °C |

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

5. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | Typ | Unit |
|---------------|--|--|------|------|
| $R_{th(j-c)}$ | thermal resistance from junction to case | $T_{case} = 85^\circ\text{C}$; $P_{L(AV)} = 80 \text{ W}$ [1] | 0.23 | K/W |

[1] $R_{th(j-c)}$ is measured under RF conditions.

6. Characteristics

Table 7. DC characteristics

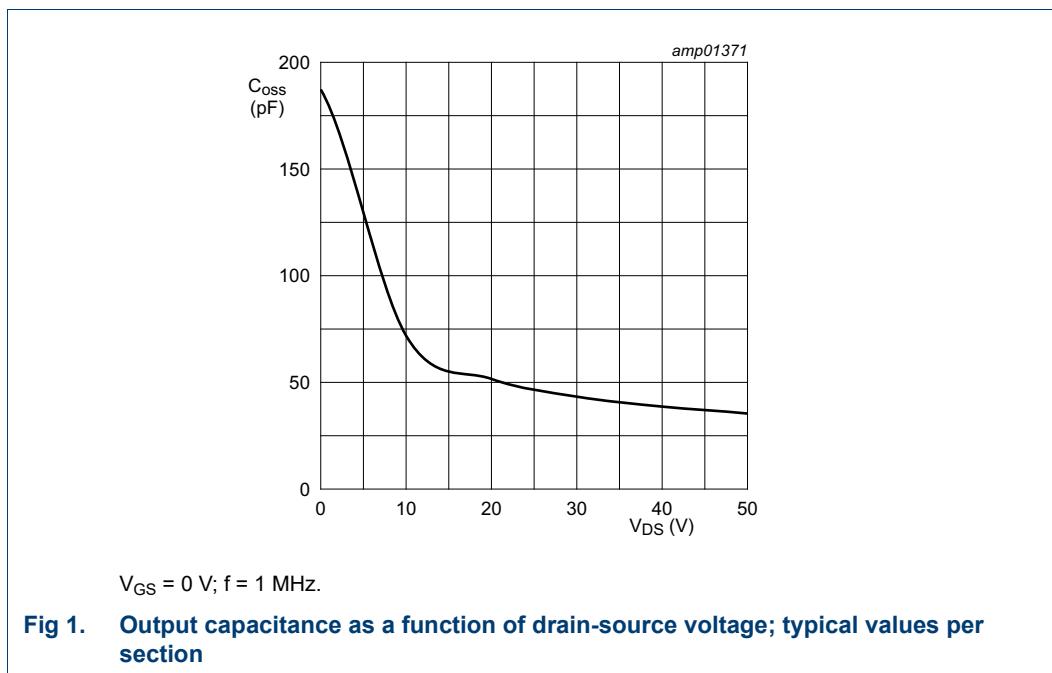
$T_j = 25^\circ\text{C}$; per section unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---------------|----------------------------------|--|-----|-----|-----|------------------|
| $V_{(BR)DSS}$ | drain-source breakdown voltage | $V_{GS} = 0 \text{ V}$; $I_D = 1.2 \text{ mA}$ | 108 | - | - | V |
| $V_{GS(th)}$ | gate-source threshold voltage | $V_{DS} = 10 \text{ V}$; $I_D = 120 \text{ mA}$ | 1.5 | 2.0 | 2.5 | V |
| I_{DSS} | drain leakage current | $V_{GS} = 0 \text{ V}$; $V_{DS} = 50 \text{ V}$ | - | - | 1.4 | μA |
| I_{DSX} | drain cut-off current | $V_{GS} = V_{GS(th)} + 3.75 \text{ V}$; $V_{DS} = 10 \text{ V}$ | - | 22 | - | A |
| I_{GSS} | gate leakage current | $V_{GS} = 10 \text{ V}$; $V_{DS} = 0 \text{ V}$ | - | - | 140 | nA |
| $R_{DS(on)}$ | drain-source on-state resistance | $V_{GS} = V_{GS(th)} + 3.75 \text{ V}$; $I_D = 4.2 \text{ A}$ | - | 180 | - | $\text{m}\Omega$ |

Table 8. AC characteristics

$T_j = 25^\circ\text{C}$; per section unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-----------|----------------------|--|-----|------|-----|------|
| C_{rs} | feedback capacitance | $V_{GS} = 0 \text{ V}$; $V_{DS} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 0.42 | - | pF |
| C_{iss} | input capacitance | $V_{GS} = 0 \text{ V}$; $V_{DS} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 176 | - | pF |
| C_{oss} | output capacitance | $V_{GS} = 0 \text{ V}$; $V_{DS} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 35 | - | pF |

**Table 9. RF characteristics**RF characteristics in Ampleon production test circuit, T_{case} = 25 °C; unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|------------------------------|-------------|------|------|------|------|
| DVB-T (8k OFDM), class-AB operation | | | | | | |
| V _{DS} | drain-source voltage | | - | 50 | - | V |
| I _{DQ} | quiescent drain current | per section | - | 325 | - | mA |
| P _{L(AV)} | average output power | f = 700 MHz | 78.4 | 80 | 81.6 | W |
| G _p | power gain | f = 700 MHz | 21.8 | 22.5 | - | dB |
| η _D | drain efficiency | f = 700 MHz | 31 | 34 | - | % |
| ACPR | adjacent channel power ratio | f = 700 MHz | - | -30 | -27 | dBc |
| PAR | peak-to-average ratio | f = 700 MHz | 7.2 | 8 | 8.4 | dB |

7. Test information

7.1 Ruggedness in class-AB operation

The BLF984P and BLF984PS are capable of withstanding a load mismatch corresponding to VSWR ≥ 40 : 1 through all phases under the following conditions: V_{DS} = 60 V; f = 700 MHz; P_{L(AV)} = 80 W; DVB-T; PAR = 8 dB.

7.2 Test circuit

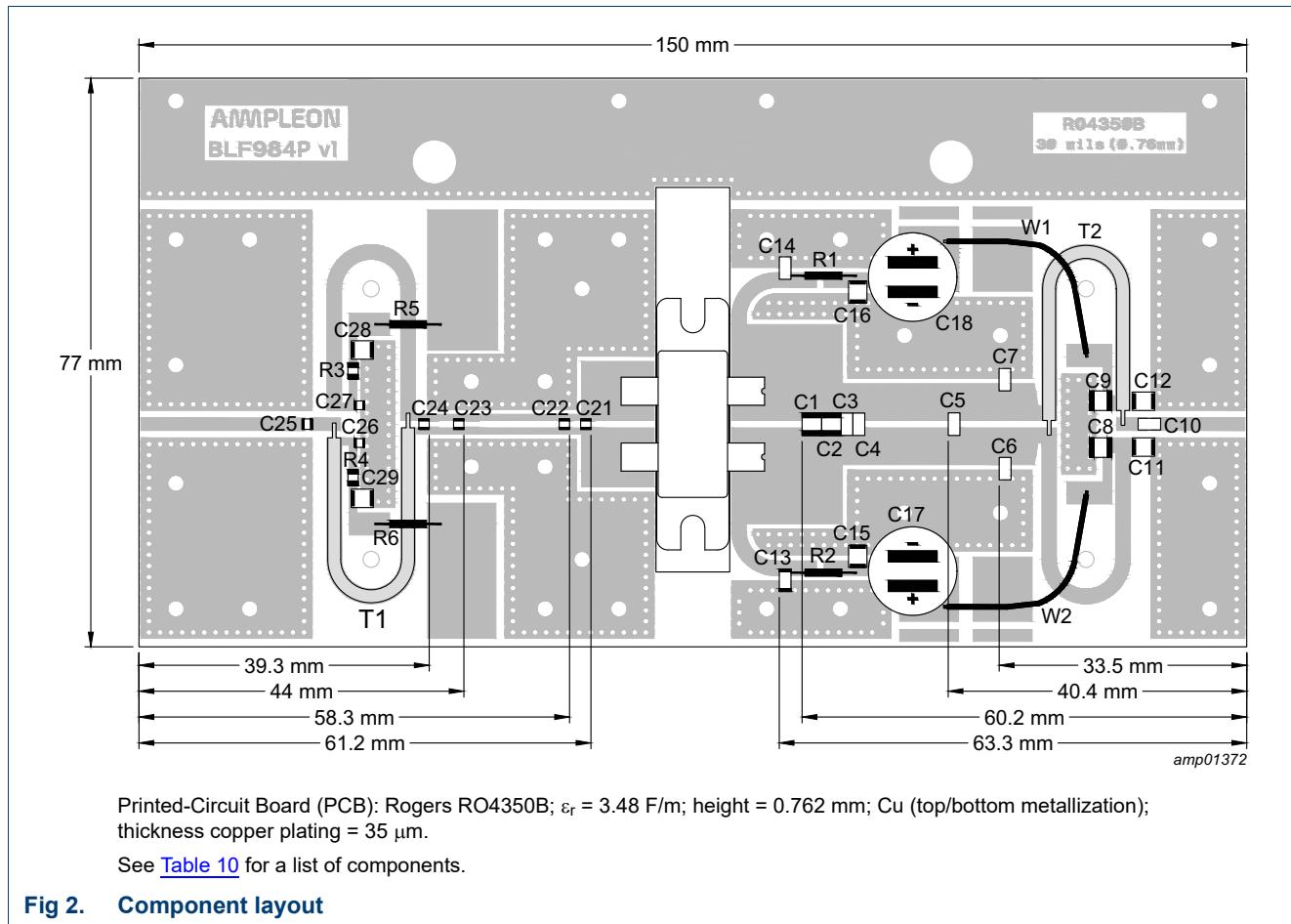


Table 10. List of components

For test circuit see [Figure 2](#).

| Component | Description | Value | Remarks |
|-----------------------|-----------------------------------|---------------------------|--------------------------------------|
| C1, C2 | multilayer ceramic chip capacitor | 5.1 pF | ATC800B5R1BT500XT |
| C3, C4 | multilayer ceramic chip capacitor | 6.8 pF | ATC800B6R8BT500XT |
| C5 | multilayer ceramic chip capacitor | 8.2 pF | ATC800B8R2BT500XT |
| C6, C7 | multilayer ceramic chip capacitor | 10 pF | ATC800B100FT500XT |
| C8, C9, C10, C13, C14 | multilayer ceramic chip capacitor | 100 pF | ATC800B101JT500XT |
| C11, C12, C28, C29 | multilayer ceramic chip capacitor | 4.7 μF , 100 V | Murata: GMR31CC72A475KE11L, SMD 1210 |
| C15, C16 | multilayer ceramic chip capacitor | 10 μF , 100 V | SMD 1210 |
| C17, C18 | electrolytic capacitor | 470 μF , 63 V | axial |
| C21 | multilayer ceramic chip capacitor | 11 pF | ATC800A110GT250XT |
| C22 | multilayer ceramic chip capacitor | 4.7 pF | ATC800A4R7BT250XT |
| C23 | multilayer ceramic chip capacitor | 22 pF | ATC800A220JT250XT |
| C24 | multilayer ceramic chip capacitor | 18 pF | ATC800A180JT250XT |
| C25, C26, C27 | multilayer ceramic chip capacitor | 100 pF | ATC800A101JT250XT |
| R1, R2 | wire resistor | 10 Ω , 0.6 W | |

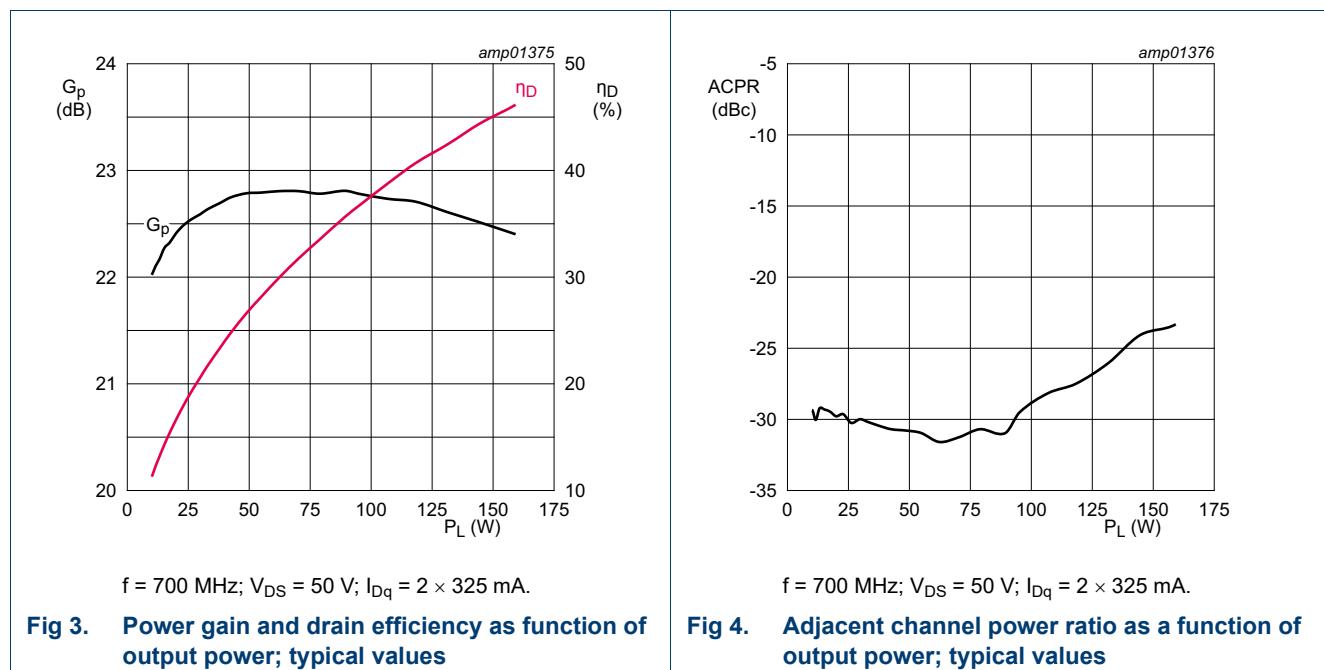
Table 10. List of components ...continued
For test circuit see [Figure 2](#).

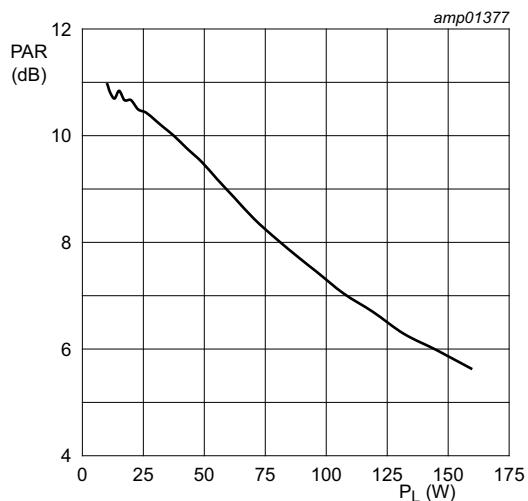
| Component | Description | Value | Remarks |
|-----------|-----------------|--------------|--|
| R3, R4 | resistor | 5.6 Ω | SMD 0805 |
| R5, R6 | wire resistor | 100 Ω, 0.6 W | |
| T1 | semi rigid coax | 25 Ω, 50 mm | EZ90-25 |
| T2 | semi rigid coax | 25 Ω, 50 mm | EZ90-25 |
| W1, W2 | wire | | 0.75 mm ² conductor cross section |

7.3 Graphical data

7.3.1 DVB-T

Measured in a narrowband RF production test circuit

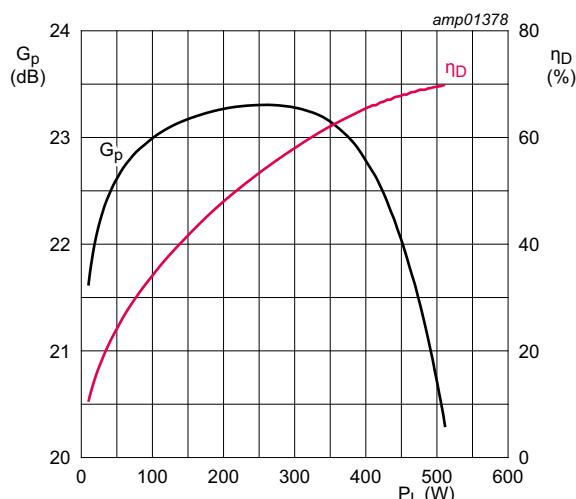




$f = 700 \text{ MHz}; V_{DS} = 50 \text{ V}; I_{Dq} = 2 \times 325 \text{ mA}$.

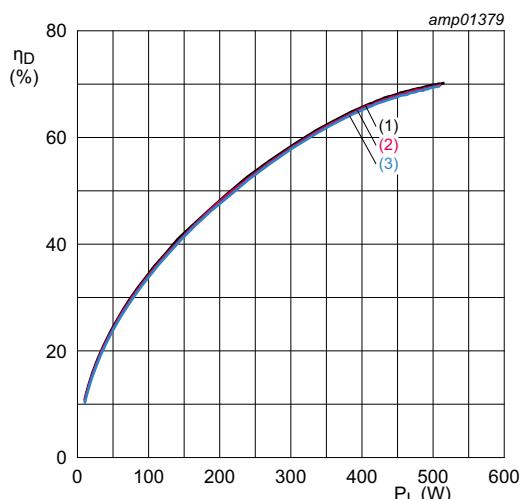
Fig 5. Peak-to-average power ratio as a function of output power; typical values

7.3.2 Pulsed CW



$V_{DS} = 50 \text{ V}; f = 700 \text{ MHz}; I_{Dq} = 2 \times 325 \text{ mA}; t_p = 100 \mu\text{s}; \delta = 10 \text{ \%}$.

Fig 6. Power gain and drain efficiency as function of output power; typical values



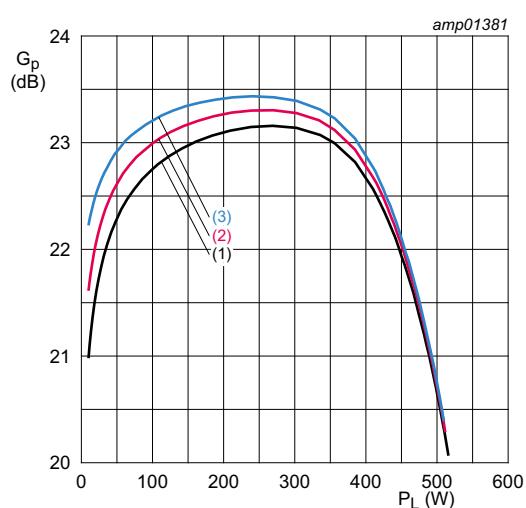
$V_{DS} = 50 \text{ V}; f = 700 \text{ MHz}; t_p = 100 \mu\text{s}; \delta = 10 \text{ \%}$.

(1) $I_{Dq} = 225 \text{ mA}$

(2) $I_{Dq} = 325 \text{ mA}$

(3) $I_{Dq} = 425 \text{ mA}$

Fig 7. Drain efficiency as a function of output power; typical values



$V_{DS} = 50$ V; $f = 700$ MHz; $t_p = 100$ μ s; $\delta = 10$ %.

- (1) $I_{Dq} = 225$ mA
- (2) $I_{Dq} = 325$ mA
- (3) $I_{Dq} = 425$ mA

Fig 8. Power gain as a function of output power; typical values

8. Package outline

Flanged LDMOST ceramic package; 2 mounting holes; 4 leads

SOT1121A

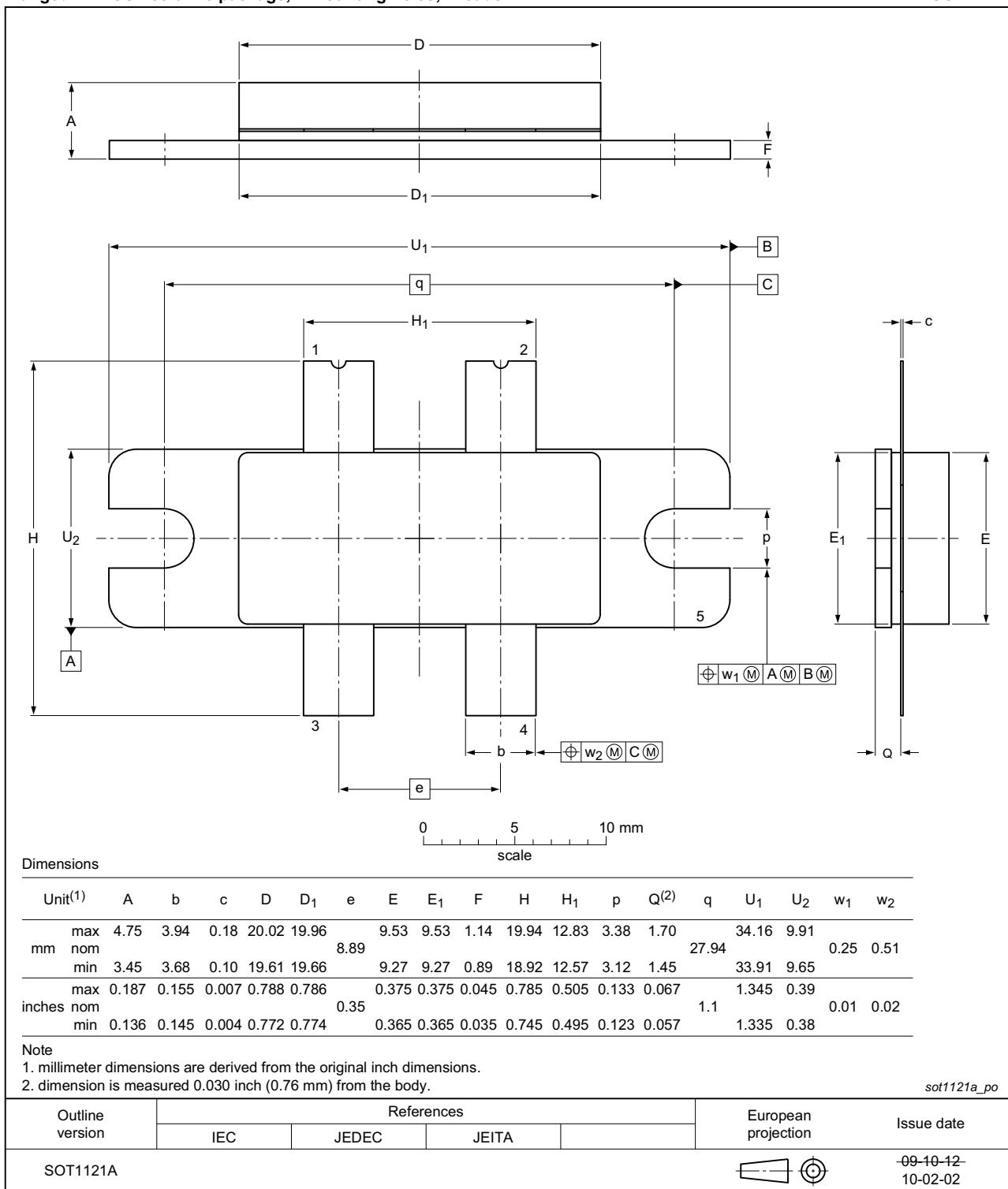


Fig 9. Package outline SOT1121A

Earless flanged ceramic package; 4 leads

SOT1121B

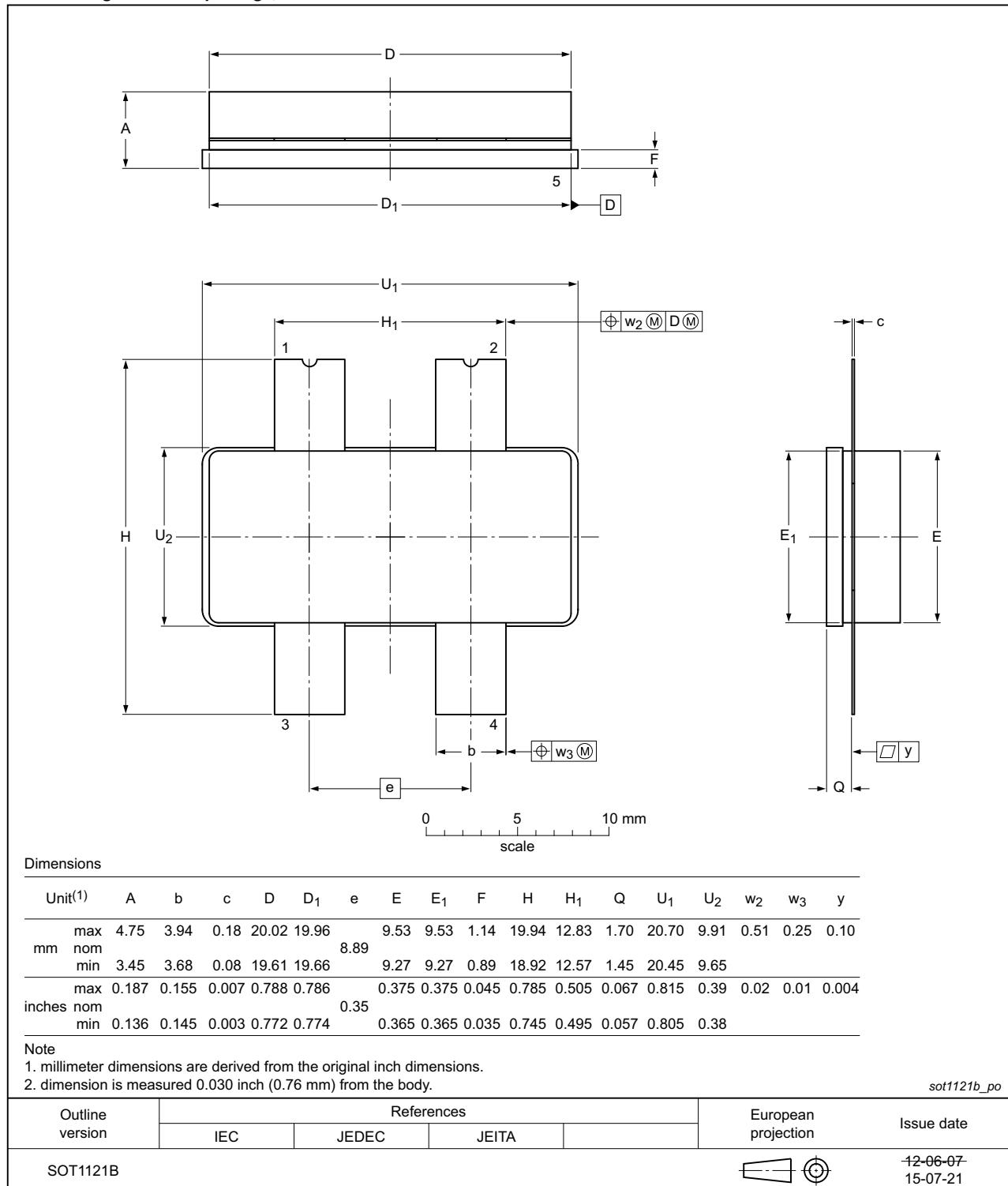


Fig 10. Package outline SOT1121B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A* or equivalent standards.

Table 11. ESD sensitivity

| ESD model | Class |
|--|-------------------------|
| Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002 | C2A [1] |
| Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001 | 2 [2] |

[1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.

[2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 12. Abbreviations

| Acronym | Description |
|---------|---|
| CCDF | Complementary Cumulative Distribution Function |
| DVB-T | Digital Video Broadcast - Terrestrial |
| ESD | ElectroStatic Discharge |
| LDMOS | Laterally Diffused Metal-Oxide Semiconductor |
| LDMOST | Laterally Diffused Metal-Oxide Semiconductor Transistor |
| MTF | Median Time to Failure |
| OFDM | Orthogonal Frequency Division Multiplexing |
| PAR | Peak-to-Average Ratio |
| RoHS | Restriction of Hazardous Substances |
| SMD | Surface Mounted Device |
| UHF | Ultra High Frequency |
| VSWR | Voltage Standing Wave Ratio |

11. Revision history

Table 13. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------------|--|--------------------|---------------|----------------------|
| BLF984P_BLF984PS v.2 | 20200729 | Product data sheet | - | BLF984P_BLF984PS v.1 |
| Modifications: | <ul style="list-style-type: none"> • Table 8 on page 3: typos corrected | | | |
| BLF984P_BLF984PS v.1 | 20200430 | Product data sheet | - | - |

12. Legal information

12.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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