CARDINAL COMPONENTS

CPP_5 Series CMOS Clock Oscillator



Features

CMOS Output (will interface with TTL devices)



5.0 x 3.2 x 1.2mm LCC Ceramic Package

- Enable/Disable Function (optional Standby function)
- 3.3V or 5.0V nominal Supply Voltage
- Size: 5 x 3.2mm
- Factory programmed

Applications

Driving A/Ds, D/As, FPGAs Digital Video Ethernet, GbE Medical Storage Area Networking COTS Broad Band Access SONET/ SDH/ DWDM Test & Measurement

Electrical Characteristics					
Parameter	Min	Тур	Max	Unit	Condition
Frequency Range	1	-	133	MHz	(3.3V:1-100MHz)
Frequency Stability ²	±25	-	±100	ppm	For all supply voltages, load changes, aging for 1 year at $25^{\circ}C \pm 2^{\circ}C$, shock, vibration and temperatures.
Operating Temperature Range options ²	0 -20 -40	- - -	+70 +70 +85	°C	
Supply Voltage ^{1, 2} V _{DD}	2.97	-	5.5	V	See Part Number options on page 2
Supply Current I _{DD} (No Load)	-	-	45 25	mA	V _{DD} = 5.0V V _{DD} = 3.3V
Output Type		СМС	DS		Cload = 50pF max, V _{DD} = 4.5~5.5V, ≤ 66MHz Cload = 25pF max, V _{DD} = 4.5~5.5V, > 66MHz Cload = 30pF max, V _{DD} = 2.97~3.63V, ≤ 40MHz Cload = 15pF max, V _{DD} = 2.97~3.63V, > 40MHz
		TTI	L		Cload = 50pF max; V _{DD} = 4.5~5.5V, ≤ 40MHz
Duty Cycle	-	-	-	%	See Page 2
Output V _{OH} (TTL Level)	2.4	-	-	V	VDD = 4.5~5.5V
(CMOS Level)	Vdd - 0.4			V	All voltages
Output V _{OL}	-	-	0.4	V	See Load Circuit and waveform page
Output T _{RISE} and T _{FALL}	-	-	-	ns	See page 2
Startup Time	-	-	2	ms	Time for output to reach specified frequency
V _{DISABLE}	-	-	0.8 0.2Vdd	v	VDD = 4.5~5.5V VDD = 2.97~3.63V
VENABLE	2.0 0.7Vdd	-		v	VDD = 4.5~5.5V VDD = 2.97~3.63V
Enable Time	-	-	2	ms	
Disable Time - Pin 1 low to Output Hi-Z	-	T/2	T+10	ns	T = Frequency Period
Disable Current	-	- 0.4	-	mA	Enable/Disable: Pad 1 low, output disabled; See above Supply Cur- rent Standby option: Pad 1 low, output disabled, oscillator shutdown
RMS Period Jitter	-	40 30	50 40	ps	≤ 33MHz > 33MHz
Period Jitter, Pk-Pk		100 75	250 175	ps	>1,000,000 samples ≤ 33MHz > 33MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

² Specified by part number

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Place an appropriate power supply bypass capacitor next to device for correct operation



Min	Тур	Max	Unit	
45		55		Fo ≤ 50 MHz, CL ≤ 50pF
45		55	%	50 MHz < Fo ≤ 66MHz; CL ≤ 15pF
40		60	70	66 MHz < Fo ≤ 125MHz, CL ≤ 25pF
40		60		125 MHz < Fo \leq 133MHz, CL \leq 15pF
Min	Тур	Max	Unit	
45		55		Fo ≤ 66 MHz, CL ≤ 25pF
			%	66 MHz < Fo ≤ 125MHz; CL ≤ 25pF
40		60		125 MHz < Fo ≤ 133MHz, CL ≤ 15pF
Min	Тур	Max	Unit	
	тур	WIAA	Onit	
45 40		55 60	%	Fo ≤ 40 MHz, CL ≤ 30pF 40 MHz < Fo ≤ 100MHz; CL ≤ 15pF
	45 45 40 40 Min 45 40 40 Min 45	45 45 40 40 Min Typ 45 40 Min Typ 45 45 45 45	45 55 45 55 40 60 40 60 40 55 40 60 40 55 40 60 40 60 40 60 40 60 40 60 40 60 40 60 40 8 40 8 40 8 55 60 60 60 8 55 40 55	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

Parameter	Min	Тур	Мах	Unit	
			1.8		0.8V~2.0V, VDD = 4.5~5.5V, CL=50pF
			1.2		0.8V~2.0V, VDD = 4.5~5.5V, CL=25pF
			0.9		0.8V~2.0V, VDD = 4.5~5.5V, CL=15pF
Rise/Fall Time				ns	
			3.4		0.2Vdd~0.8Vdd, Vdd = 4.5~5.5V, CL=50pF
			4.0		0.2VDD~0.8VDD, VDD = 2.97~3.63V, CL=30pF
			2.4		0.2Vpp~0.8Vpp, Vpp = 2.97~3.63V, CL=15pF

Part Number Example: CPPC5LZ-A7BP-50.0TS Series Model Logic Package Size (mm) Supply Voltage Vcc Packaging Operating Temperature Range Frequency Stability (ppm) Frequency (MHz) Enable/Disable										
СРР	С	5	L	Z	A7	BP	50.0	TS		
	C=CMOS T = TTL	5 = 5 x 3.2	L = 3.3V Blank= 5.0V	Blank = Tape Only Z= Tape/reel	Blank = 0 to +70°C A5 = -20 to +70°C A7 = -40 to +85°C	BR = ±25 BP = ±50 B6 = ±100	5V: 1 - 133 3.3V: 1 - 100	TS = Tristate PD = Powerdown		

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Mechanical Dimensions (mm)





[SIDE VIEW]



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Pin#	Function		
1	Tri-state		
2	GND		
3	Output		
4	VDD		

Pin 1	Output
Open	Active
Logic '1'	Active
Ground	Tri-state

Pad Layout mm shown

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

To ensure optimal oscillator performance, place a by-pass capacitor of $0.01 \sim 0.1 \mu F$ as close to the part as possible between Vdd and GND pads.

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

Cardinal Components Inc. certifies this device is in accordance with the RoHS and REACH directives.

2.54

1.50

Cardinal Components guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.09 grams Moisture Sensitivity Level: 1 As defined in J-STD-020D Second Level Interconnect code: e4

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2

For Optimum Jitter Performance, Cardinal recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

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CPP_5 Series CMOS Clock Oscillator

Electrical Test / Load Circuit



Notes:

CL: 15pF Includes the input capacitance of oscilloscope * $0.01 \sim 0.1 \mu F$ external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 100°C

ESD Rating

Model	Min. Voltage	Condition		
Human Body Model	2000V	MIL-STD-883 3015.7		
Machine Model	200V	EIAJ ED-4701/304		

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

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Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm tape, 8mm pitch.

See Note 1



USER DIRECTION OF UNREELING

Tape Variable Dimensions Table 2								
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko	
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1	

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA– 481-B

Tape Constant Dimensions Table 1									
Tape Size	Do	D1 typ	E1	Po	P2	S1 min	T typ	T1 max	
12mm	1.5	1.5	1.75	4.0	2.0	0.6	0.3	0.1	
1211111	+0.1 -0.0	1.5	±0.1	±0.1	±0.1	0.0	0.5	0.1	



Reel Dimensions (may vary) Table 3									
		A	С	D					
Reel Size	Inches	mm	Inches	mm	mm	mm			
7	7.0	177.8	2.50	63.5	13.0	Tape size +0.4			
10	10.0	254.0	4.00	101.6	+0.5	+0.4			
13	13.0	330.2	3.75	95.3	-0.2	-0.0			

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Contacting Cardinal Components

Cardinal Components 19013 36th Ave. West Lynnwood, WA 98036-5761 U.S.A. Tel: 973-785-1333

email: sales@cardinalxtal.com

URL: www.cardinalxtal.com