



## Product Change Notification / SYST-30BSKD560

---

### Date:

01-Dec-2022

### Product Category:

Smart Energy SOC

### PCN Type:

Document Change

### Notification Subject:

Data Sheet - PIC32CXMTG Data Sheet

### Affected CPNs:

[SYST-30BSKD560\\_Affected\\_CPN\\_12012022.pdf](#)

[SYST-30BSKD560\\_Affected\\_CPN\\_12012022.csv](#)

### Notification Text:

SYST-30BSKD560

Microchip has released a new Datasheet for the PIC32CXMTG Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32CXMTG Data Sheet](#).

**Notification Status:** Final

**Description of Change:** Note: Microchip is in the process of implementing inclusive language in our documentation. During this update, you may see the old terms and the new terms in our documentation text. We apologize for any confusion.

Minor editorial changes throughout:

- 1.) 3.2. Pinout and Multiplexing: Corrected pin 62 name in table 128-pin EP-TQFP Multiplexing.
- 2.) 5. Input/Output Lines: Shutdown (SHDN) Pin: corrected pin name. Flash Memory Hardware ERASE Signal: updated.
- 3.) 14. ROM Code and Boot Strategies: GPNVM Bits Overview: updated table.
- 4.) 15. Supply Controller (SUPC): SUPC\_SR: index 12 now 'reserved'
- 5.) 19. Clock Generator: PLL Controls: updated
- 6.) 20. Power Management Controller (PMC): Block Diagram: updated.
  
- 7.) 25. Special Function Registers (SFR): Register Write Protection: list of registers updated. Register Summary: offset 0xA0 now reserved (was Core Debug Configuration Register). SFR\_PWM\_DEBUG: index 1 now 'reserved' (was CORE1)
- 8.) 28. Chip Identifier (CHIPID): Chip ID Registers: updated table.
- 9.) 33. Flexible Serial Communication Controller (FLEXCOM): Throughout: Deleted references to Host High-Speed mode. Bus Clear Command, FIFO Pointer Error, FLEX\_TWI\_CR: modified. Sniffer Mode: updated Sniffer description. FLEX\_TWI\_SMR: updated BSEL

description.

10.) 34. Quad Serial Peripheral Interface (QSPI): Corrected clock name from GCK to GCLK throughout.

11.) 37. Analog-to-Digital Converter (ADC) Controller: ADC\_MR: updated reset value.

12.) 38. Analog Comparator Controller (ACC): ACC\_ISR: updated reset value.

13.) True Random Number Generator (TRNG): TRNG\_WPSR: modified SWETYP description (value 5).

14.) 46. Pulse Width Modulation Controller (PWM): Fault Protection: figure updated.

15.) 48. Electrical Characteristics: VDD3V3 Power-On-Reset: corrected unit in table. Analog Comparator Characteristics: corrected unit in table

**Impacts to Data Sheet:** See above details.

**Reason for Change:** To improve productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 01 Dec 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[PIC32CXMTG Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

### Terms and Conditions:

If you wish to [receive Microchip PCNs via email](#) please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to [change your PCN profile, including opt out](#), please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

PIC32CX2051MTG128-I/X9B

PIC32CX1025MTG128-I/X9B

PIC32CX2051MTG128T-I/X9B

PIC32CX1025MTG128T-I/X9B