



Product Change Notification / SYST-13BPTL440

Date:

20-Sep-2023

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Document Change

Notification Subject:

Data Sheet - dsPIC33CH128MP508 Family Data Sheet

Affected CPNs:

[SYST-13BPTL440_Affected_CPN_09202023.pdf](#)

[SYST-13BPTL440_Affected_CPN_09202023.csv](#)

Notification Text:

SYST-13BPTL440

Microchip has released a new Datasheet for the dsPIC33CH128MP508 Family Data Sheet of devices. If you are using one of these devices please read the document located at [dsPIC33CH128MP508 Family Data Sheet](#).

Data Sheet - dsPIC33CH128MP508 Family Data Sheet

Notification Status: Final

Description of Change: Updates Register 3-24, Register 3-110, Register 3-111, Register 3-185, Register 4-90, Register 6-1, Register 6-12, Register 6-13, Register 6-19, Register 9-13, Register 10-1, Register 11-5, Register 12-7 (POSxHLD), Register 12-10 (VELxHLD), Register 12-15, Register 12-16, Register 12-19, Register 12-20, Register 13-1, Register 13-3, Register 13-9, Register 13-10, Register 13-11, Register 18-1, Register 18-3, Register 21-5 Register 21-13, and Register 21-34. - Adds Register 3-4, Register 3-5, Register 4-4 - Removes Register 12-7 (POSxHLDL), Register 12-7 (VELxHLDL). - Updates Table 2, Table 3, Table 4, Table 5, Table 6, Table 7, Table 8, Table 9, Table 1-1, Table 3-18, Table 3-31, Table 3-47, Table 4-15, Table 4-27, Table 6-3, Table 8-2, Table 8-3, Table 24-4, Table 24-19, Table 24-21, Table 24-30, Table 24-33, Table 24-35, Table 24-43, Table 24-44, Table 25-4 and Table 25-11. Adds Table 24-5, Table 24-47, Table 24-48, Table 24-49. - Removes Table 24-44: - Updates Figure 1-2, Figure 2-3, Figure 4-18, Figure 20-1. - Adds Figure 21-2, Figure 24-18, Figure 24-19, Figure 24-20. - Updates Communication Interfaces, Section 2.5 "External Oscillator Pins", Section 3.5.4.7 "Cross Core Interrupts", Section 3.10.3 "Temperature Sensor", Section 6.6 "Low-Power RC (LPRC) Oscillator", Section 6.8 "Reference

Clock Output", Section 13.0 "UniversalAsynchronous Receiver Transmitter (UART)", Section 13.4 "Protocol Extensions", Section 14.0 "Serial Peripheral Interface (SPI)", Section 21.5 "RegulatorControl and Sleep Mode", Section 21.7 "Brown-out Reset (BOR)" Section 21.8 "Dual Watchdog Timer (WDT)". - Adds Functional Safety Readiness – ISO 26262/IEC 61508/IEC 60730, Terminology Cross Reference, Section 2.6 "External Oscillator Layout Guidance", Section 3.7"Cross-Core Signal Mapping", Section 3.10.2 "Sampling Time Requirements", Section 4.7.2 "Sampling Time Requirements", and Section 6.10 "Primary Oscillator Pin Functionality". - Updates Equation 15-1

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 19 Sep 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Revision History:

September 19, 2023: Issued document PCN.

September 20, 2023: Re-issued document PCN to update the affected CPN list.

Attachments:

[dsPIC33CH128MP508 Family Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

DSPIC33CH128MP202-E/2N
DSPIC33CH128MP202-E/SS
DSPIC33CH128MP202-H/2N
DSPIC33CH128MP202-H/SS
DSPIC33CH128MP202-I/2N
DSPIC33CH128MP202-I/SS
DSPIC33CH128MP202T-I/2N
DSPIC33CH128MP202T-I/SS
DSPIC33CH128MP203-E/M5
DSPIC33CH128MP203-H/M5
DSPIC33CH128MP203-I/M5
DSPIC33CH128MP203T-I/M5
DSPIC33CH128MP205-E/M4
DSPIC33CH128MP205-E/PT
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DSPIC33CH128MP205-H/PT
DSPIC33CH128MP205-I/M4
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DSPIC33CH128MP206-I/MR
DSPIC33CH128MP206-I/PT
DSPIC33CH128MP206-I/PTC01
DSPIC33CH128MP206T-I/MR
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