

No.: Date:

Affected Products			
Product Series	Mercury+ XU7		
Affected Product Model	All product models		
Affected Parts	EN105150	ME-XU7-15EG-2I-D12E-R4.1	
	EN105151	ME-XU7-6EG-1I-D11E-R4.1	
	EN105152	ME-XU7-9EG-2I-D12E-R4.1	
	All custom models of ME-XU7		
Replacement Parts	EN105304	ME-XU7-15EG-2I-D12E-R5.0	
	EN105305	ME-XU7-6EG-1I-D11E-R5.0	
	EN105306	ME-XU7-9EG-2I-D12E-R5.0	
	All custom models of ME-XU7		

Type of PCN, Desc	cription and expected impact for customers	
Type of PCN	 A - Post on Enclustra Website Removal of Product Model (if no customer is affected) Minor Revision without impact on customer Other, specify: B - Inform current customers Increase of minimum order quantity (MOQ) Status Changes to end-of-life (EOL) Removal of Product Model (if customer is affected) Major Revision roll-out Minor Revision with impact for customer Other, specify: C - Inform all affected customers Security related bug / Major non-conformance to specification Other, specify: 	
Description of change	Replacement of power converter	
Reason for change	EOL component	
Expected impact on customer	Starting with the revision 5 of the Mercury+ XU7 SoC modules, PL_PERST#_LS on AB1 pin of FPGA to support PL side PCIe is added.	
Forecasted key milestones	Last Time Buy (LTB): N/A due to unavailability of components. Last Time Ship (LTS) date: N/A, according to components availability.	

Details of change	
Details	Refer to "Mercury XU7 Known Issues and Changes" and "Mercury XU7 User Manual" available on Enclustra download site.

Contact

Details

If you have any questions or require additional information, please contact <u>info@enclustra.com</u>. Web: <u>www.enclustra.com</u>