





IF170A, IF170B, IF170C, IF170D N-Channel JFET

Features

- InterFET <u>N0132L Geometry</u>
- Low noise: 1.0 nV/VHz typical
- High gain: 22mS typical
- Low gate leakage: 750fA typical @10V
- High radiation tolerance
- RoHS, REACH, CMR compliant
- Custom test and binning options available
- SMT, TH, and bare die package options
- Edge case SPICE modeling: InterFET SPICE

Industry Standard Crosses

- 2SK152, 2SK170, 2N6451, 2N6452, 2N3972, MMBF4393
- NSVJ3557SA3, NSVJ5908DSG5, NSVJ2394SA3

InterFET Similar Parts

• IF1320, IFN152, SMP6451, SMP6452, SMP3972, SMP4393

InterFET Dual Parts

• IF389A, IF389B, IF389C, IF389D, IFN146, IF1322A

Applications

- General: Amplifiers; Switches; Voltage regulators; Oscillators; Signal mixers; Noise generators
- Military/Aero: Radar; Communications; Satellites; Missiles guidance; Hydrophone preamplifiers.
- Medical: Medical imaging systems; Medical monitors and recorders; Ultrasound equipment
- Audio: Tone control circuits; Headphone amplifiers; Audio filters; Electret Microphone

Description

The -30V InterFET IF170x JFET is targeted for sensitive amplifier stages for mid-frequencies designs. Higher breakdown voltage parts are available through InterFET custom ordering. IF389x is the dual matched option.

Part Number	Description	Case	Packaging
IF170AT72, IF170BT72,			
IF170CT72, IF170DT72	Through-Hole	TO-72	Bulk
IF170AT92, IF170BT92,			
IF170CT92, IF170DT92	Through-Hole	TO-92	Bulk
IF170AST3, IF170BST3,			
IF170CST3, IF170DST3	Surface Mount	SOT23	Bulk
IF170AST3TR, IF170BST3TR,	7" Tape and Reel: 1,000 and 3,000 Pieces		Minimum 1,000 Pieces
IF170CST3TR, IF170DST3TR	13" Tape and Reel: 9,000 Pieces	SOT23	Tape and Reel
IF170ACOT, IF170BCOT,			
IF170CCOT, IF170DCOT	Chip Orientated Tray (COT Waffle Pack)	СОТ	400/Waffle Pack
IF170ACFT, IF170BCFT,			
IF170CCFT, IF170DCFT	Chip Face-up Tray (CFT Waffle Pack)	CFT	400/Waffle Pack

Ordering Information Custom Part and Binning Options Available



NOTICE: Please refer to the end of this document for information on product materials, compliance, safety, and legal statements.







TO-92 Bottom View



NOTE: S/D pins are interchangeable Source Drain connections







Electrical Characteristics

Maximum Ratings (@ TA = 25°C, Unless otherwise specified)

	Parameters	TO-72	SOT-23	TO-92	Unit
V_{RGS}	Reverse Gate Source and Gate Drain Voltage	-30	-30	-30	V
I _{FG}	Continuous Forward Gate Current	50	50	50	mA
PD	Continuous Device Power Dissipation ¹	500	350	500	mW
Р	Power Derating ¹	3.3	2.8	4	mW/°C
Тı	Operating Junction Temperature	-65 to 175	-55 to 150	-55 to 150	°C
T _{STG}	Storage Temperature	-65 to 175	-55 to 150	-55 to 150	°C

Support

¹ Thermal power dissipation and derating values obtained with gate pin (substrate) thermally connected to pad and/or internal layer.

Static Characteristics (@ TA = 25°C, Unless otherwise specified)

			IF170A		IF170B		IF170C		IF170D		
	Parameters	Conditions	Min	Max	Min	Max	Min	Max	Min	Max	Unit
V(BR)GSS	Gate to Source Breakdown Voltage	I_G = -1 μ A, V_{DS} = 0V	-30		-30		-30		-30		V
I _{GSS}	Gate to Source Reverse Current	V _{DS} = 0V, V _{GS} = -10V		-0.1		-0.1		-0.1		-0.1	nA
Vgs(off)	Gate to Source Cutoff Voltage	V _{DS} = 10V, I _D = 1nA	-0.2	-2.0	-0.2	-2.0	-0.2	-2.0	-0.2	-2.0	V
I _{DSS}	Drain to Source Saturation Current	$V_{DS} = 10V, V_{GS} = 0V$ (Pulsed)	2.6	6.5	6.0	12.0	10.0	20.0	18.0	30.0	mA

Dvnamic Characteristics (@ TA = 25°C, Unless otherwise specified)

	Parameters	Conditions	Min	Max	Unit
G _{FS}	Full Forward Transconductance	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1kHz	14		mS
GFS(TYP)	Typical Transconductance	V _{DS} = 15V, I _D = 1mA	6		mS
C _{iss}	Input Capacitance	$V_{DS} = 10V, I_D = 2mA,$ f = 1MHz		20	pF
Crss	Reverse Transfer Capacitance	$V_{DS} = 10V, I_D = 2mA,$ f = 1MHz		4.5	pF
en	Noise Voltage	$V_{DS} = 10V$, $I_D = 2mA$, f = 1kHz	1.0 (Typical)		nV/√Hz





Technical Support Now

Typical IF170x Characteristics







Order Now

Technical

Support

Typical IF170x Characteristics (Continued)





Technical

Support

Order

Now

SOT23 (TO-236AB) Mechanical and Layout Data

Package Outline Data





Suggested Pad Layout





- 1. All linear dimensions are in millimeters.
- 2. Package weight approximately 0.12 grams
- 3. Molded plastic case UL 94V-0 rated
- For Tape and Reel specifications refer to InterFET CTC-021 Tape and Reel Specification, Document number: IF39002
- 5. Bulk product is shipped in standard ESD shipping material
- 6. Refer to JEDEC standards for additional information.

- 1. All linear dimensions are in millimeters.
- 2. The suggested land pattern dimensions have been provided for reference only. A more robust pattern may be desired for wave soldering.



Technical Support

Order

Now



TO-72 Mechanical and Layout Data

Package Outline Data



Suggested Through-Hole Layout



- 1. All linear dimensions are in millimeters.
- 2. Four leaded device. Not all leads are shown in drawing views.
- 3. Package weight approximately 0.31 grams
- 4. Bulk product is shipped in standard ESD shipping material
- 5. Refer to JEDEC standards for additional information.

- 1. All linear dimensions are in millimeters.
- 2. The suggested land pattern dimensions have been provided as a straight lead reference only. A more robust pattern may be desired for wave soldering and/or bent lead configurations.



Technical Support Now

IF170A-B-C-D

TO-92 Mechanical and Layout Data

Package Outline Data





Suggested Through-Hole Layout





- 1. All linear dimensions are in millimeters.
- 2. Package weight approximately 0.19 grams
- 3. Molded plastic case UL 94V-0 rated
- 4. Bulk product is shipped in standard ESD shipping material
- 5. Refer to JEDEC standards for additional information.

- 1. All linear dimensions are in millimeters.
- 2. The suggested land pattern dimensions have been provided as a straight lead reference only. A more robust pattern may be desired for wave soldering and/or bent lead configurations.







Compliance and Legal

Environment

InterFET parts follow the latest RoHS Compliance, REACH Compliance, Proposition 65 Statement, TSCA Statement, and Chemical Disposal and Waste Mitigation requirement and guidelines. For more on InterFET's Environmental Commitment please visit www.InterFET.com/environmental/.

Technical

Support

Package materials

Parameters SOT23		SOIC8	TO-92	Metal Case		
Alloy	CDA194	C194 1/2H	C194 1/2H	Kovar		
Cu	Balance	97% min	97% min			
Fe	2.1 - 2.6%	2.1 - 2.6%	2.1 - 2.6%	53%		
Zn	0.05 – 0.2%	0.05 – 0.2%	0.05 - 0.15%			
Р	0.015 - 0.15%	0.015 - 0.15%	0.015 - 0.15%			
Pb	0.03% max	0.03% max	0.03% max			
Ni				29%		
Со				17%		
Mn				0.3%		
Si				0.2%		
С				<0.01%		
Au				Plating		

Package tests

Parameters SOT23		SOIC8	TO-92	Metal Case	
MSL	Level 1	TBD	N/A	N/A	
ESD	Class M4 Machine Model Class 3B HBM				

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