

Product Summary

Device	BV _{DSS}	R _{DS(on)} Max	I _D Max T _A = +25°C
Q1	20V	0.99Ω @ V _{GS} = 4.5V	500mA
		1.2Ω @ V _{GS} = 2.5V	460mA
		1.8Ω @ V _{GS} = 1.8V	375mA
		2.4Ω @ V _{GS} = 1.5V	320mA
Q2	-20V	1.9Ω @ V _{GS} = -4.5V	-360mA
		2.4Ω @ V _{GS} = -2.5V	-320mA
		3.4Ω @ V _{GS} = -1.8V	-270mA
		5Ω @ V _{GS} = -1.5V	-225mA

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

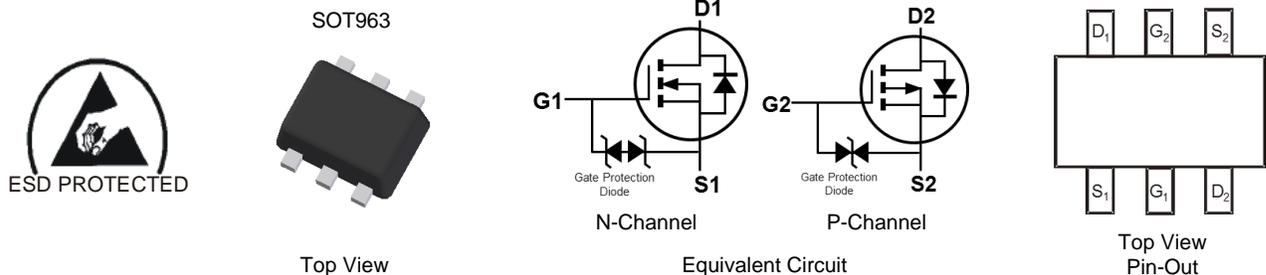
- General-purpose interfacing switches
- Power management functions
- Analog switches

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage
 - N-Channel: 1.0V Maximum
 - P-Channel: -1.0V Maximum
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surface Mount Package 1mm × 1mm
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Package: SOT963
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.027 grams (Approximate)

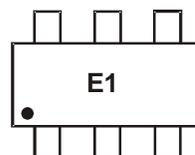


Ordering Information (Note 4, 5)

Part Number	Package	Packing	
		Qty.	Carrier
DMC2991UDJ-7	SOT963	10,000	Tape & Reel
DMC2991UDJ-7B	SOT963	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
 5. The options -7 and -7B stand for different taping orientations.

Marking Information



E1= Product Type Marking Code

Maximum Ratings Q1 N-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 6) $V_{GS} = 4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	500	mA
		$T_A = +70^\circ\text{C}$		400	
Maximum Continuous Body Diode Forward Current (Note 7)			I_S	430	mA
Pulsed Drain Current (Note 7)			I_{DM}	1.8	A

Maximum Ratings Q2 P-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 6) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	-360	mA
		$T_A = +70^\circ\text{C}$		-290	
Maximum Continuous Body Diode Forward Current (Note 7)			I_S	-360	mA
Pulsed Drain Current (Note 7)			I_{DM}	-1.1	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 6)			P_D	0.38	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		$R_{\theta JA}$	329	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range			T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.
7. Device mounted on minimum recommended pad layout test board, 10 μs pulse duty cycle = 1%.

Electrical Characteristics Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 16V, V _{GS} = 0V @T _C = +25°C
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	0.4	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	0.36	0.99	Ω	V _{GS} = 4.5V, I _D = 100mA
		—	0.46	1.2		V _{GS} = 2.5V, I _D = 50mA
		—	0.65	1.8		V _{GS} = 1.8V, I _D = 20mA
		—	0.92	2.4		V _{GS} = 1.5V, I _D = 10mA
Diode Forward Voltage	V _{SD}	—	0.6	1.0	V	V _{GS} = 0V, I _S = 10mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	21.5	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	4.9	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	3.7	—	pF	
Total Gate Charge	Q _g	—	0.35	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	0.07	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.08	—	nC	
Turn-On Delay Time	t _{D(on)}	—	5.6	—	ns	V _{DD} = 10V, V _{GS} = 4.5V, R _L = 47Ω, R _g = 10Ω, I _D = 200mA
Turn-On Rise Time	t _R	—	4.9	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	60.6	—	ns	
Turn-Off Fall Time	t _F	—	27.6	—	ns	

Electrical Characteristics Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -16V, V _{GS} = 0V @T _C = +25°C
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	—	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	1.0	1.9	Ω	V _{GS} = -4.5V, I _D = -100mA
		—	1.25	2.4		V _{GS} = -2.5V, I _D = -50mA
		—	1.44	3.4		V _{GS} = -1.8V, I _D = -20mA
		—	1.8	5		V _{GS} = -1.5V, I _D = -10mA
Diode Forward Voltage	V _{SD}	—	-0.5	-1.1	V	V _{GS} = 0V, I _S = -10mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	17	—	pF	V _{DS} = -16V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	4.1	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.7	—	pF	
Total Gate Charge	Q _g	—	0.3	—	nC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -250mA
Gate-Source Charge	Q _{gs}	—	0.04	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.1	—	nC	
Turn-On Delay Time	t _{D(on)}	—	7.3	—	ns	V _{DD} = -15V, V _{GS} = -4.5V, R _g = 2Ω, I _D = -200mA
Turn-On Rise Time	t _R	—	20.7	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	185	—	ns	
Turn-Off Fall Time	t _F	—	97	—	ns	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

Typical Characteristics - N-CHANNEL

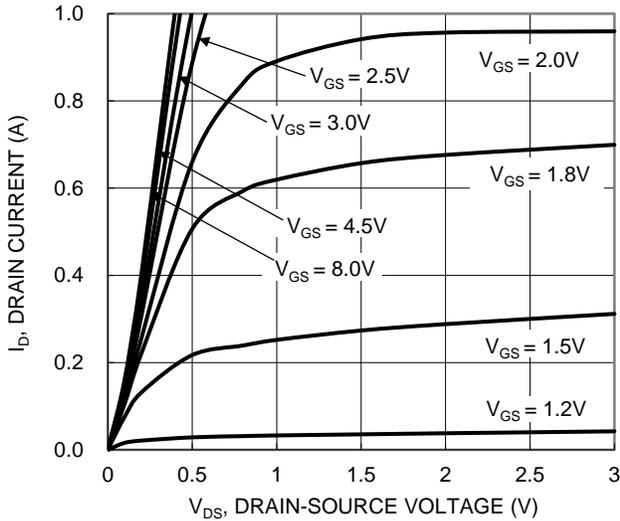


Figure 1. Typical Output Characteristic

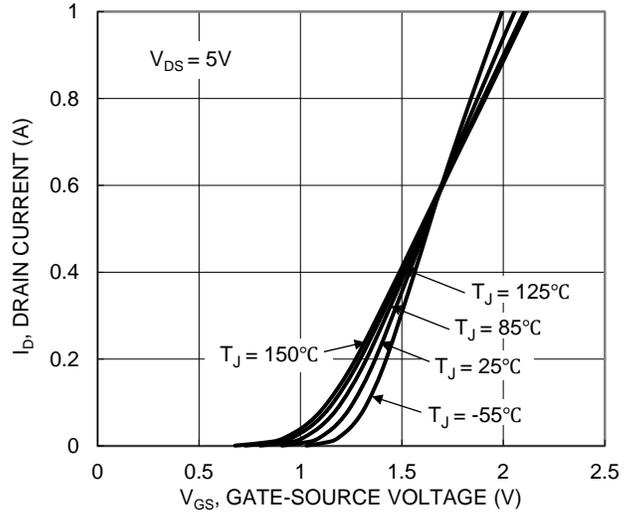


Figure 2. Typical Transfer Characteristic

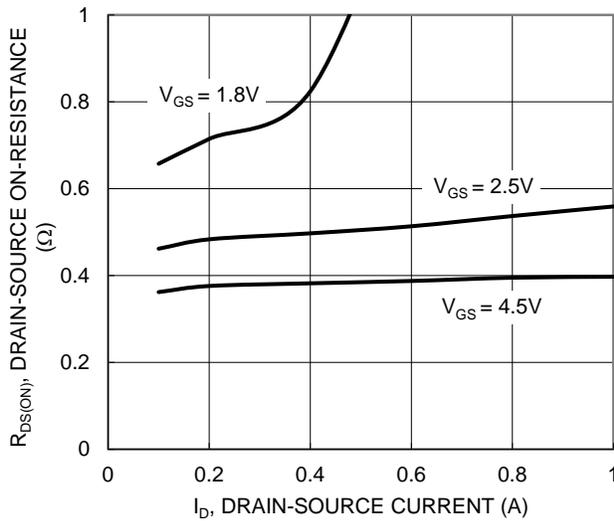


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

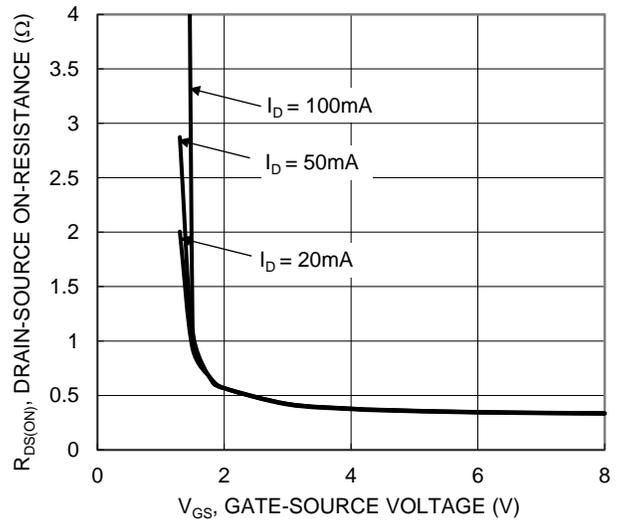


Figure 4. Typical Transfer Characteristic

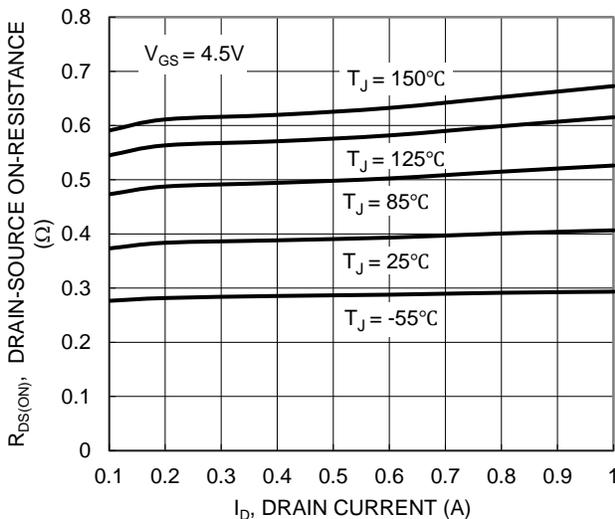


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

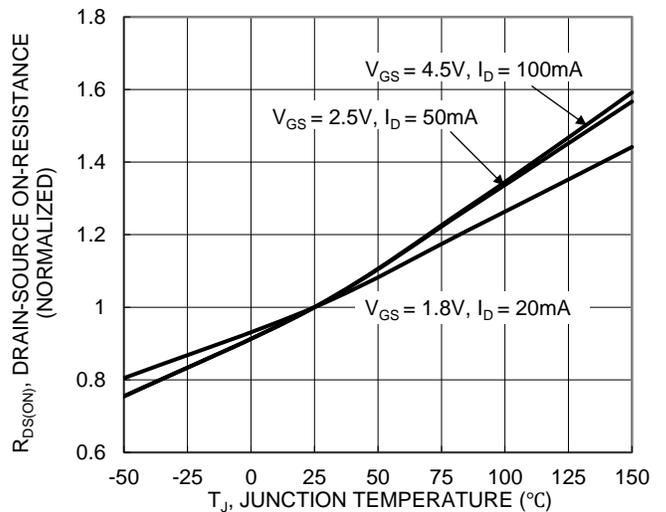


Figure 6. On-Resistance Variation with Junction Temperature

Typical Characteristics - N-CHANNEL (continued)

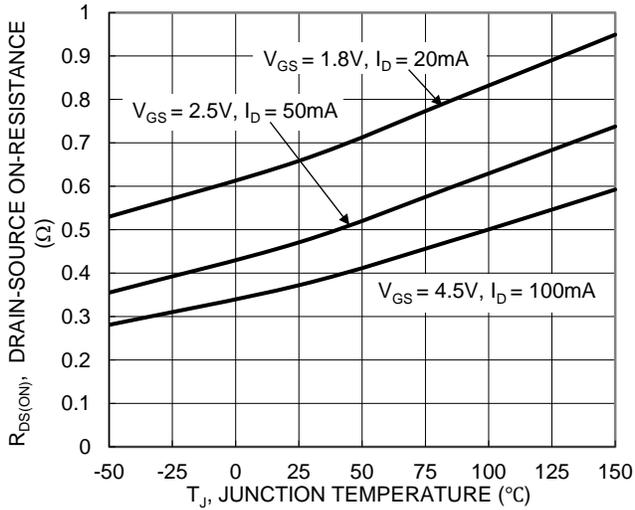


Figure 7. On-Resistance Variation with Junction Temperature

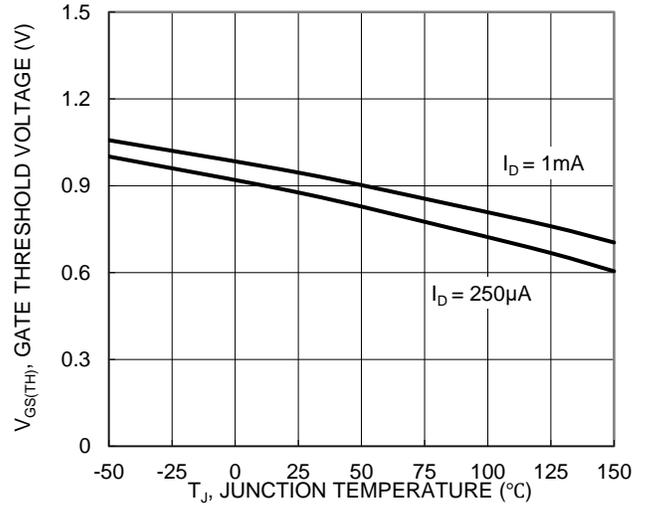


Figure 8. Gate Threshold Variation vs. Junction Temperature

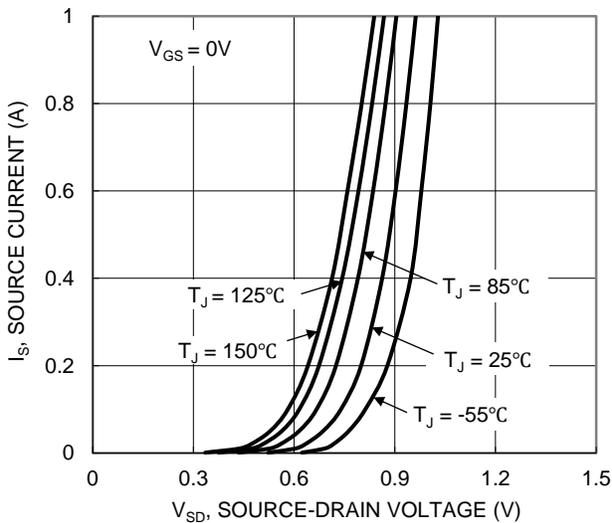


Figure 9. Diode Forward Voltage vs. Current

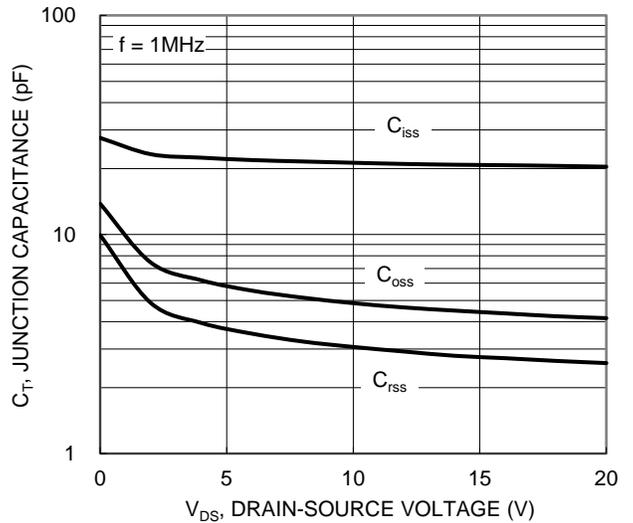


Figure 10. Typical Junction Capacitance

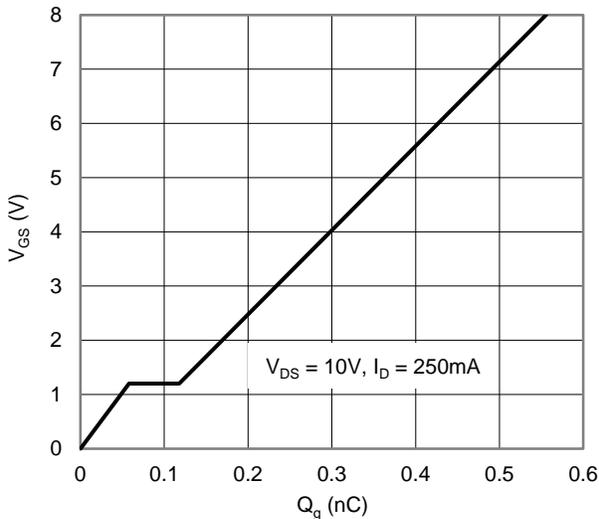


Figure 11. Gate Charge

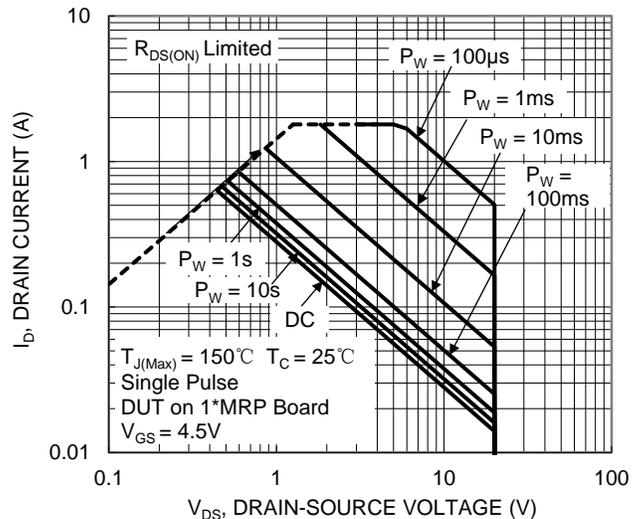


Figure 12. SOA, Safe Operation Area

Typical Characteristics - P-CHANNEL

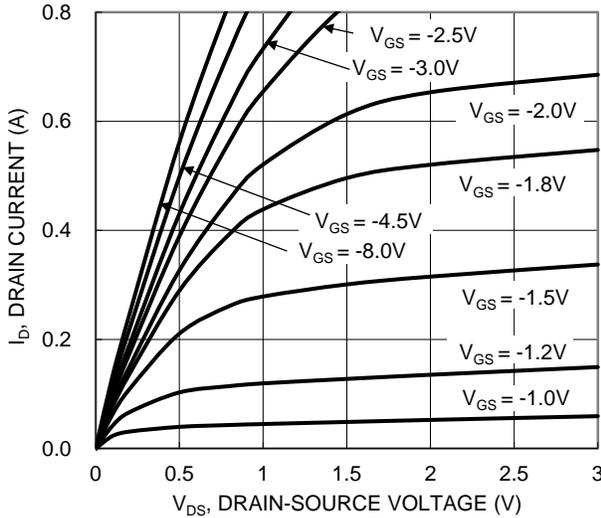


Figure 13. Typical Output Characteristic

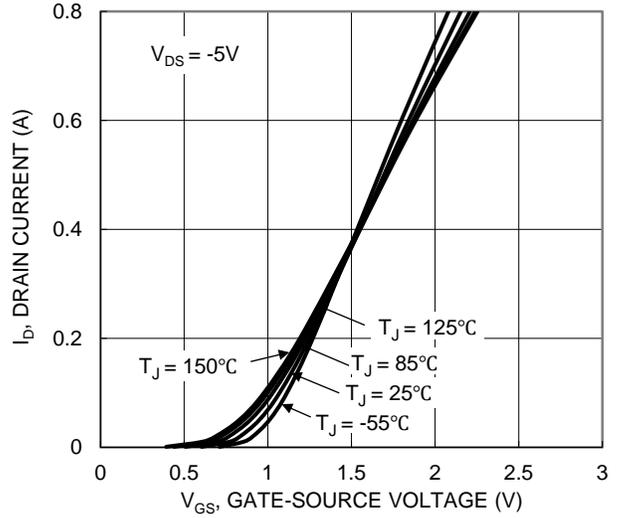


Figure 14. Typical Transfer Characteristic

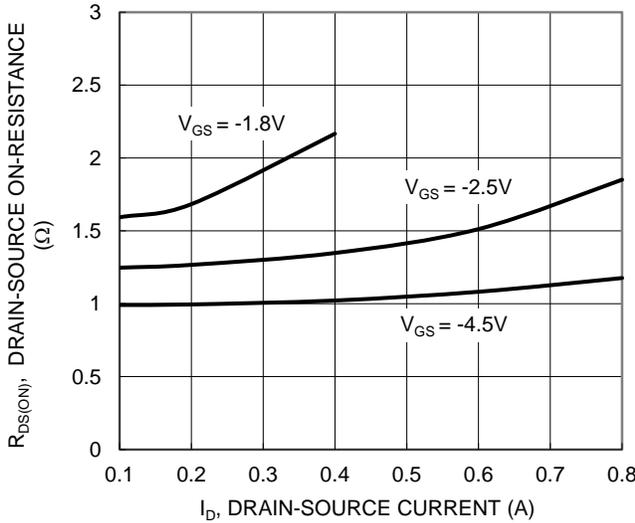


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

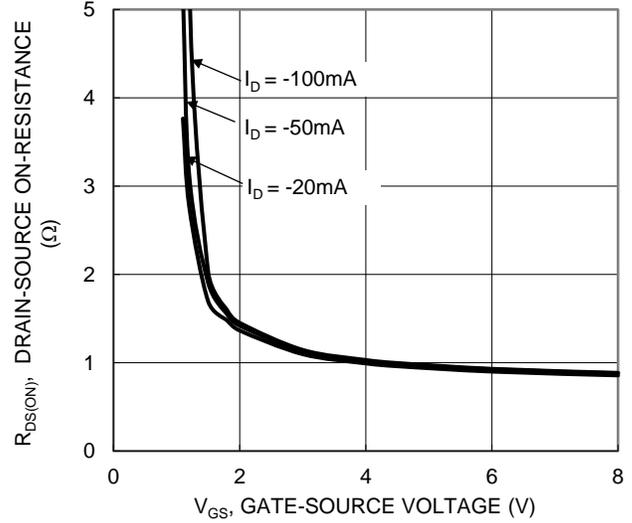


Figure 16. Typical Transfer Characteristic

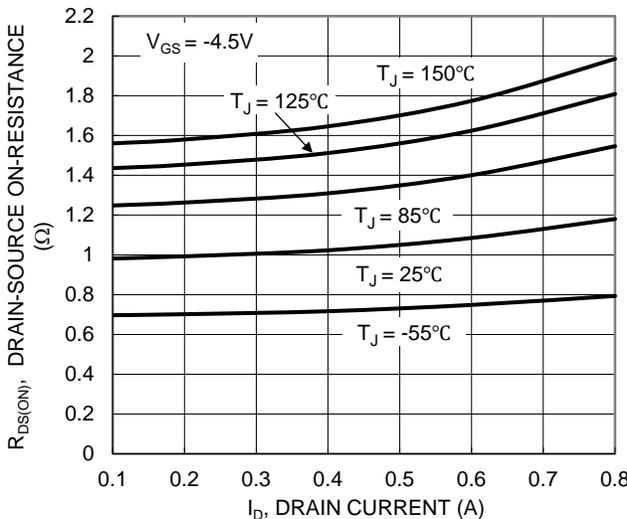


Figure 17. Typical On-Resistance vs. Drain Current and Junction Temperature

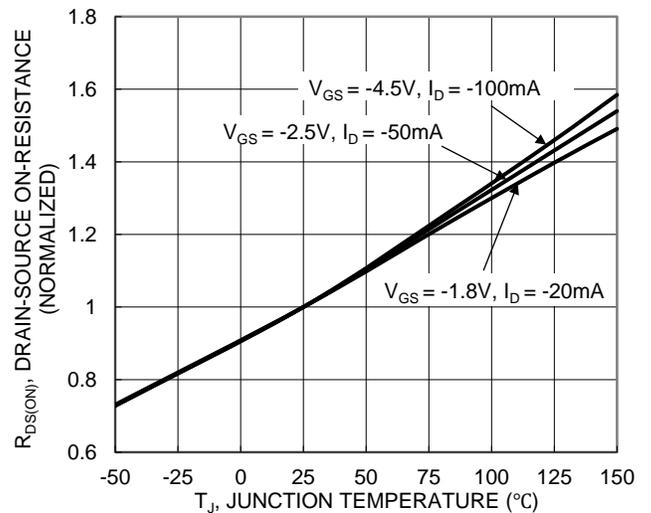


Figure 18. On-Resistance Variation with Junction Temperature

Typical Characteristics - P-CHANNEL (continued)

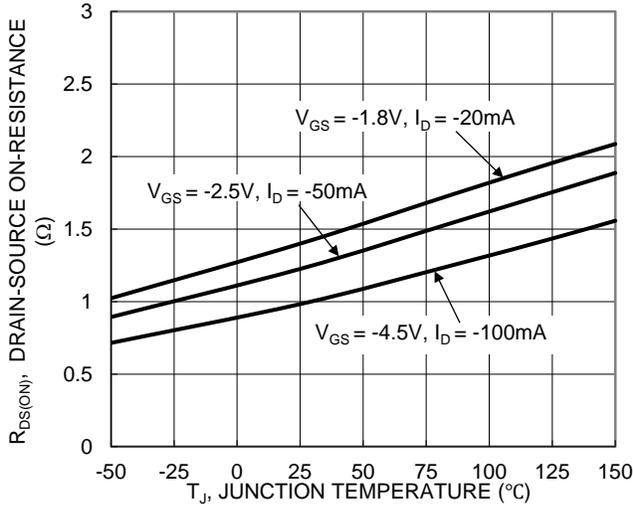


Figure 19. On-Resistance Variation with Junction Temperature

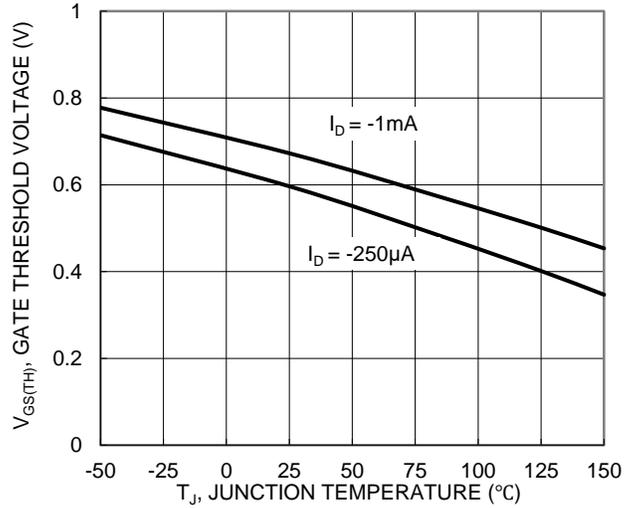


Figure 20. Gate Threshold Variation vs. Junction Temperature

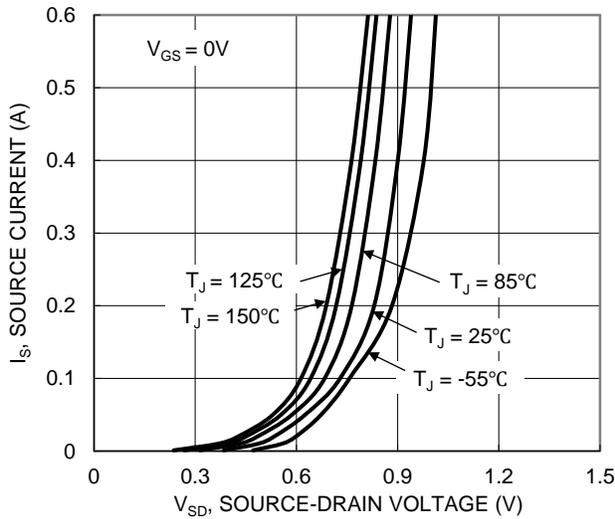


Figure 21. Diode Forward Voltage vs. Current

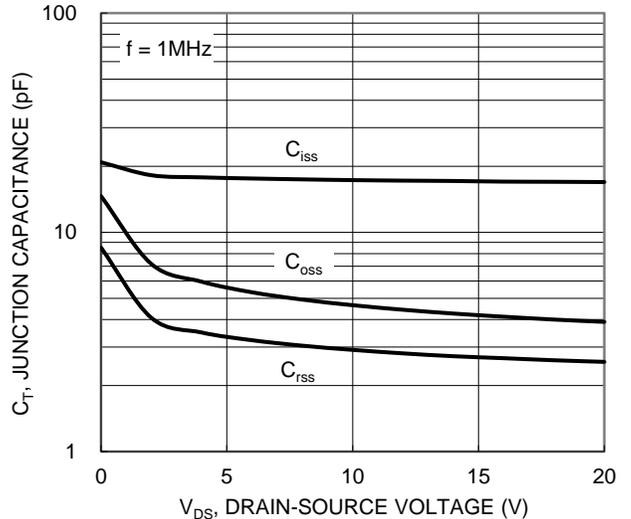


Figure 22. Typical Junction Capacitance

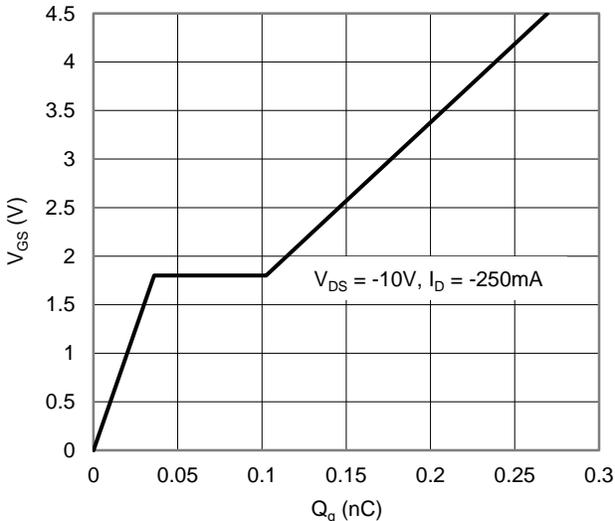


Figure 23. Gate Charge

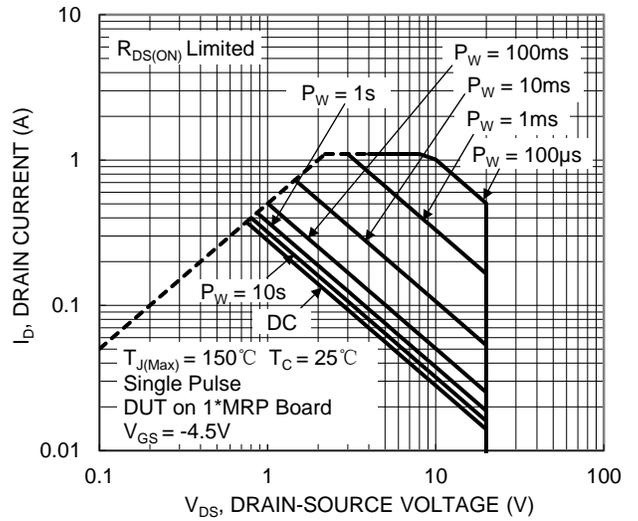


Figure 24. SOA, Safe Operation Area

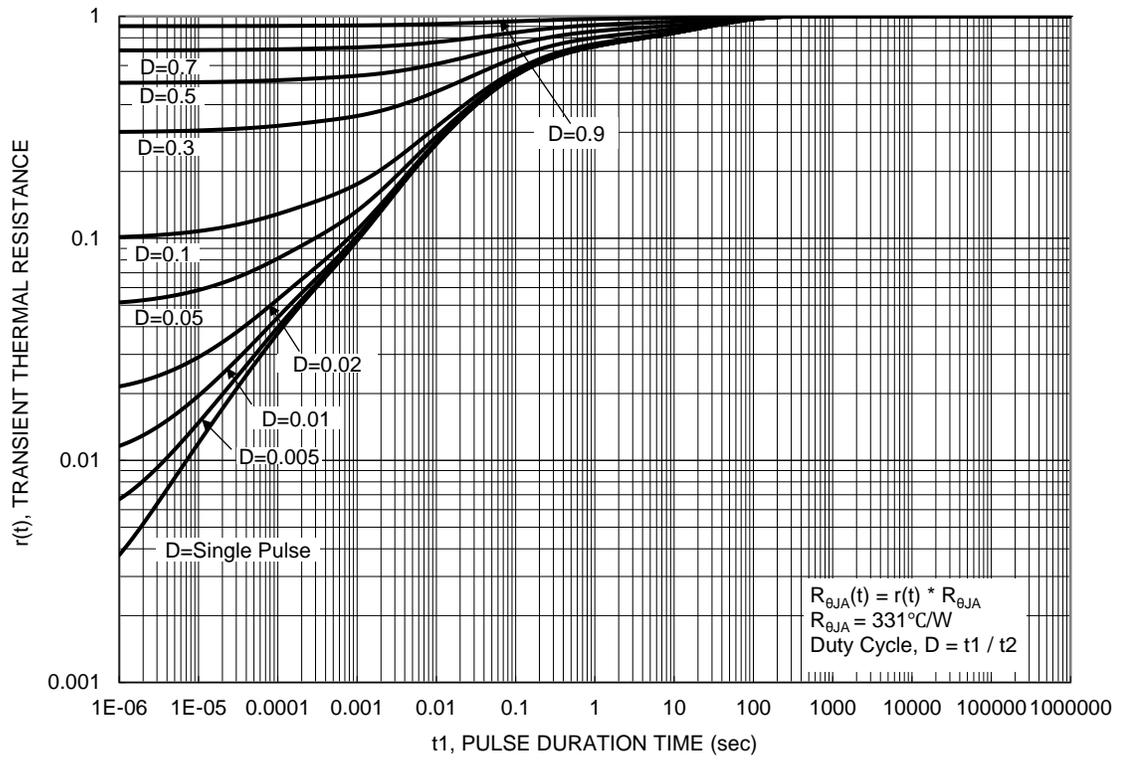
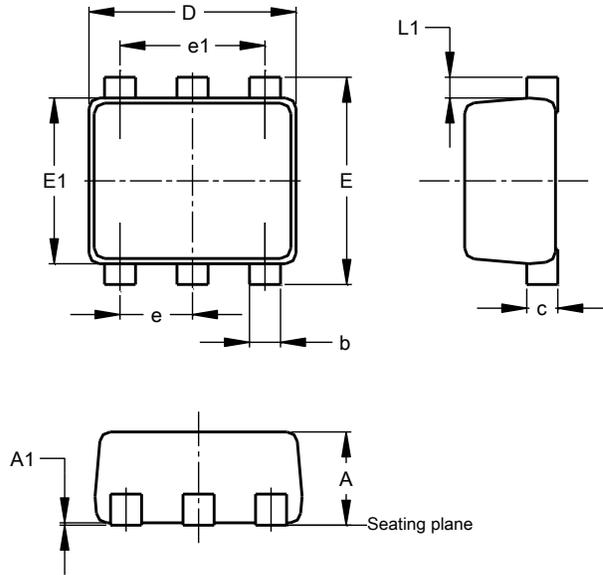


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT963

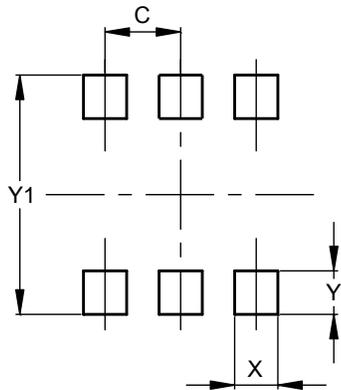


SOT963			
Dim	Min	Max	Typ
A	0.40	0.50	0.45
A1	0.00	0.05	--
b	0.10	0.20	0.15
c	0.120	0.180	0.150
D	0.95	1.05	1.00
E	0.95	1.05	1.00
E1	0.75	0.85	0.80
e	--	--	0.35
e1	--	--	0.70
L1	0.05	0.15	0.10
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT963



Dimensions	Value (in mm)
C	0.350
X	0.200
Y	0.200
Y1	1.100

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