

Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY				
	V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
N-Channel	40	0.039 at V _{GS} = 10 V	6.6	6.6
		0.050 at V _{GS} = 4.5 V	5.8	

FEATURES

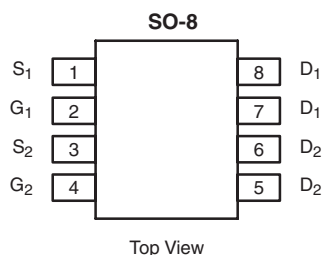
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



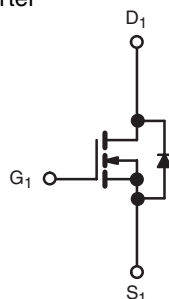
RoHS
COMPLIANT
HALOGEN
FREE
Available

APPLICATIONS

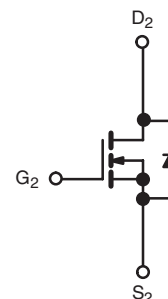
- CCFL Inverter



Ordering Information: Si4906DY-T1-E3 (Lead (Pb)-free)
Si4906DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V _{GS}	± 16	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	6.6	A
	T _C = 70 °C		5.3	
	T _A = 25 °C		5.3 ^{b, c}	
	T _A = 70 °C		4.2 ^{b, c}	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	30	
Source-Drain Current Diode Current	T _C = 25 °C	I _S	2.5	
	T _A = 25 °C		1.7 ^{b, c}	
Pulsed Source-Drain Current		I _{SM}	30	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	13	mJ
Single-Pulse Avalanche Energy		E _{AS}	8.5	
Maximum Power Dissipation	T _C = 25 °C	P _D	3.1	W
	T _C = 70 °C		2	
	T _A = 25 °C		2 ^{b, c}	
	T _A = 70 °C		1.28 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit		Unit
			Typical	Maximum	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	52	62.5	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	32	40	

Notes:

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 110 °C/W.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		40		mV/°C	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 4.6			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.8		2.2	V	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 16 V			100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μA	
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	20			A	
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A		0.032	0.039	Ω	
		V _{GS} = 4.5 V, I _D = 4 A		0.041	0.050		
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 5 A		15		S	
Dynamic ^a							
Input Capacitance	C _{iss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		625		pF	
Output Capacitance	C _{oss}			88			
Reverse Transfer Capacitance	C _{rss}			50			
Total Gate Charge	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 5 A		14.4	22	nC	
Gate-Source Charge	Q _{gs}	V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 5 A		6.6	10		
Gate-Drain Charge	Q _{gd}			1.6			
Gate Resistance	R _g			2.3			
Turn-On Delay Time	t _{d(on)}	f = 1 MHz		2.3	3.5	Ω	
Rise Time	t _r	V _{DD} = 20 V, R _L = 4 Ω I _D ≅ 5 A, V _{GEN} = 10 V, R _g = 1 Ω		9	15	ns	
Turn-Off Delay Time	t _{d(off)}			51	77		
Fall Time	t _f			21	32		
Turn-On Delay Time	t _{d(on)}			6	10		
Rise Time	t _r	V _{DD} = 20 V, R _L = 4 Ω I _D ≅ 5 A, V _{GEN} = 4.5 V, R _g = 1 Ω		13	20		
Turn-Off Delay Time	t _{d(off)}			85	128		
Fall Time	t _f			17	26		
				7	11		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.5		A
Pulse Diode Forward Current ^a	I _{SM}				30		
Body Diode Voltage	V _{SD}	I _S = 1.7 A		0.79	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 1.7 A, di/dt = 100 A/μs, T _J = 25 °C		30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			30	45	nC	
Reverse Recovery Fall Time	t _a			17		ns	
Reverse Recovery Rise Time	t _b			13			

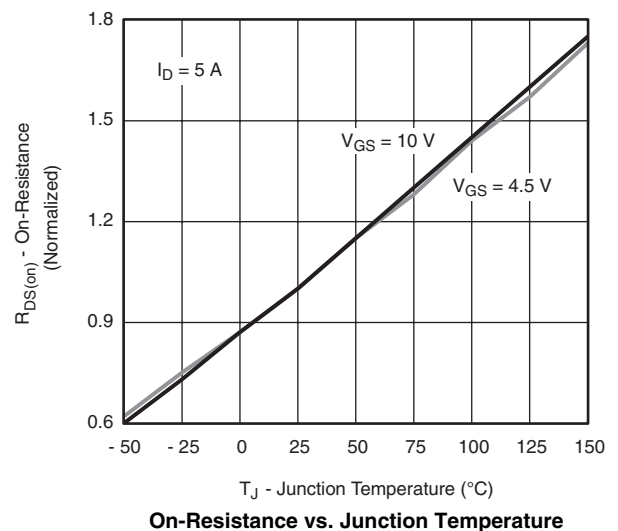
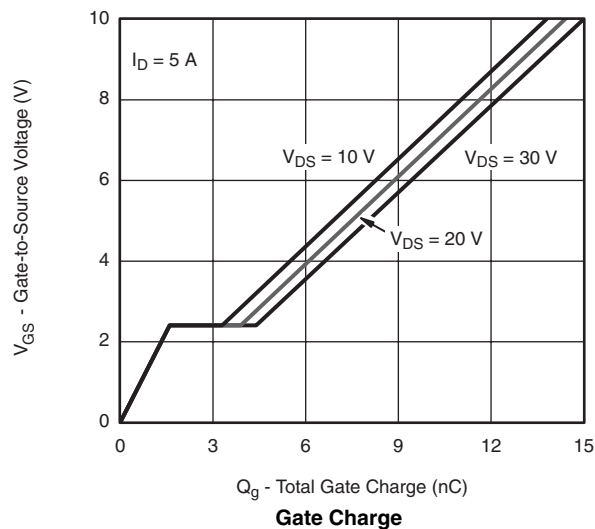
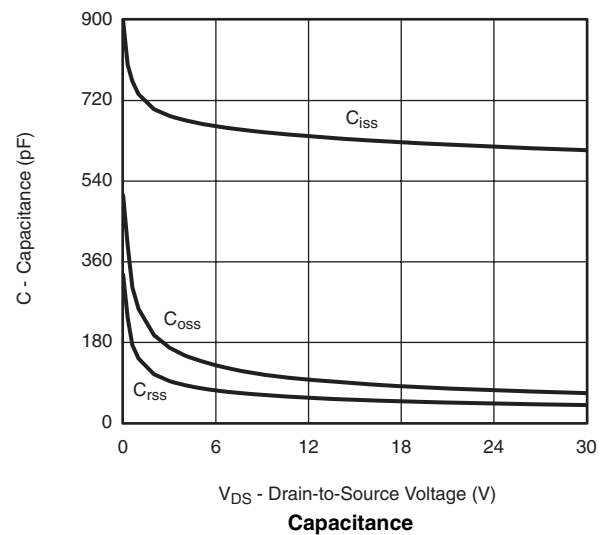
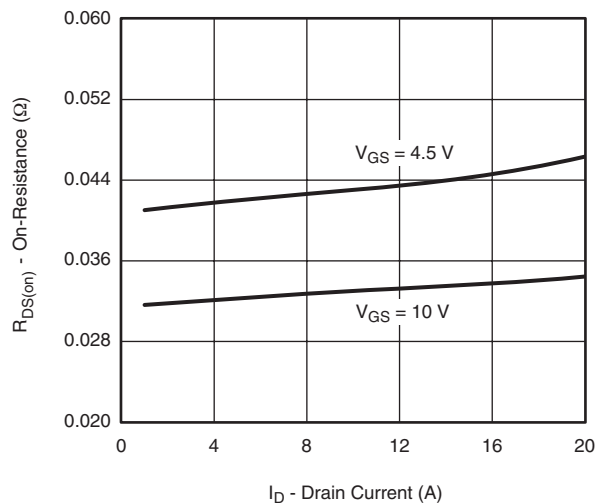
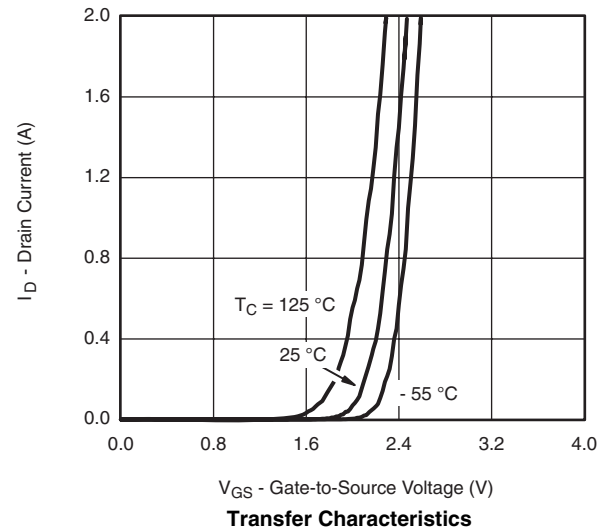
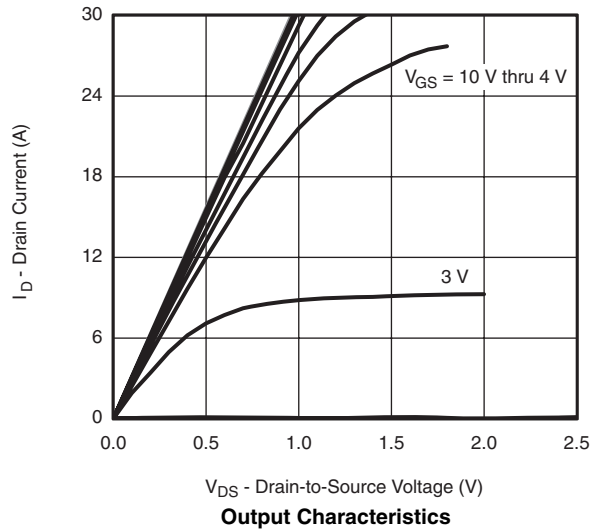
Notes:

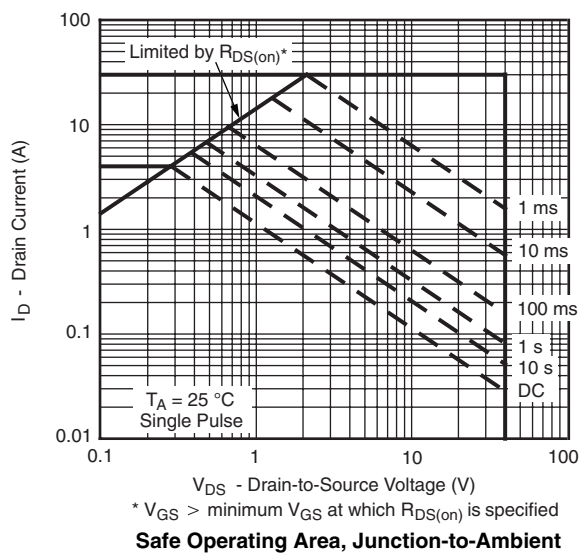
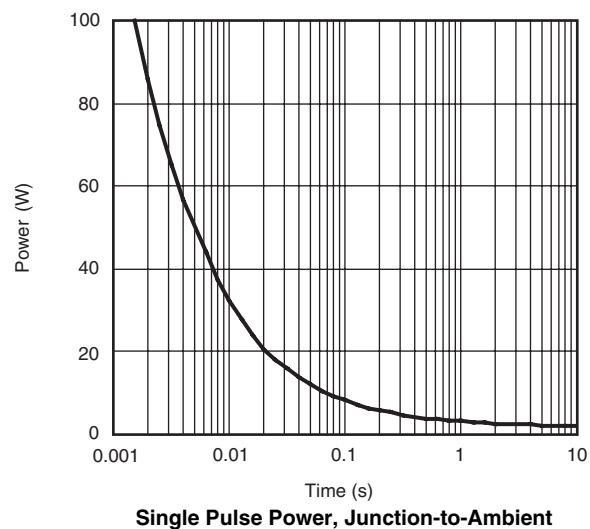
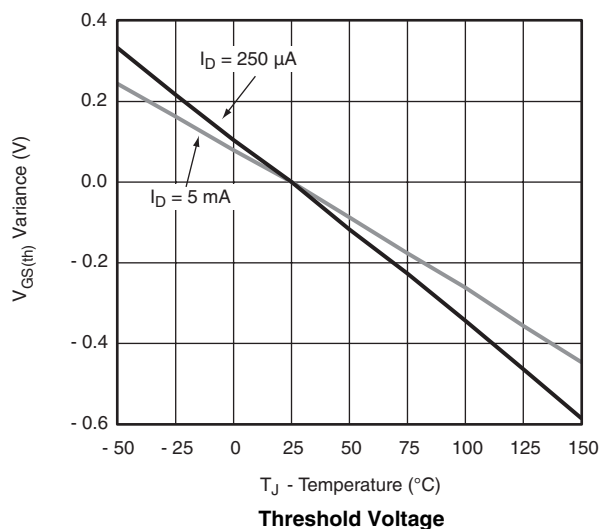
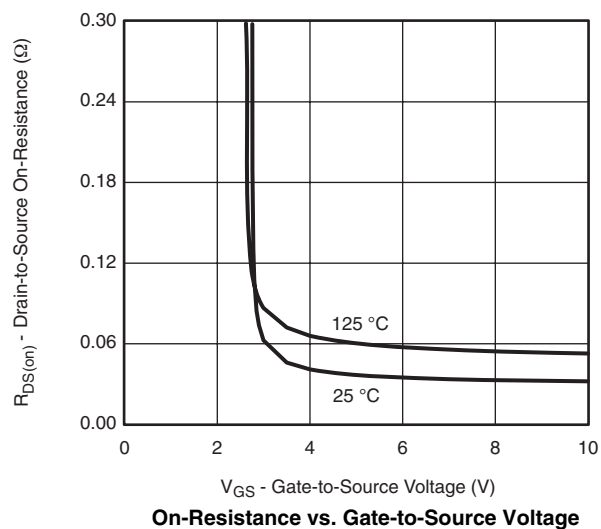
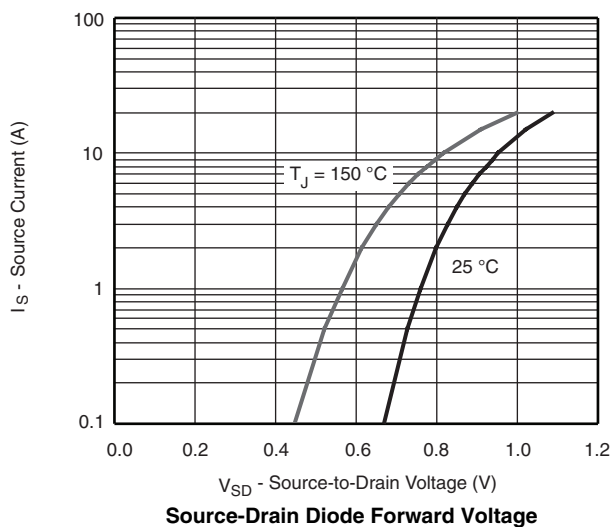
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

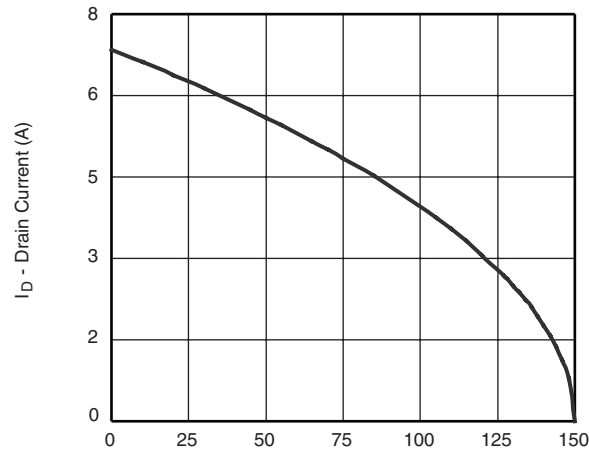
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

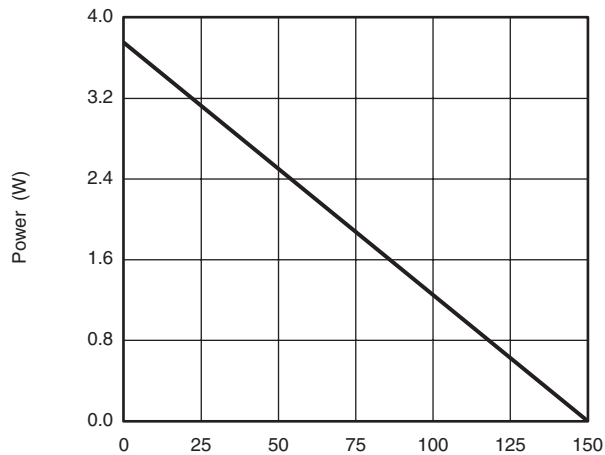


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

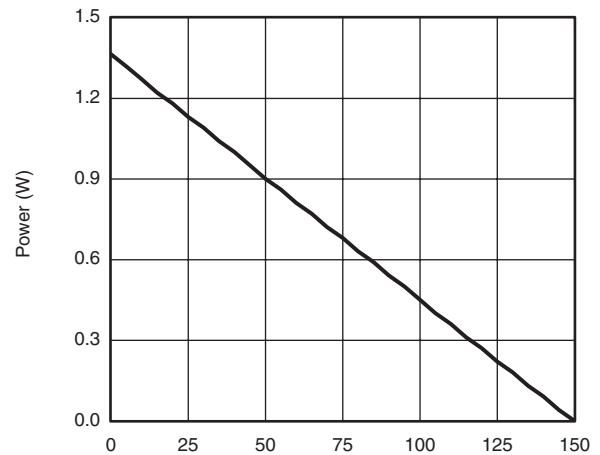
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T_C - Case Temperature (°C)
Current Derating*

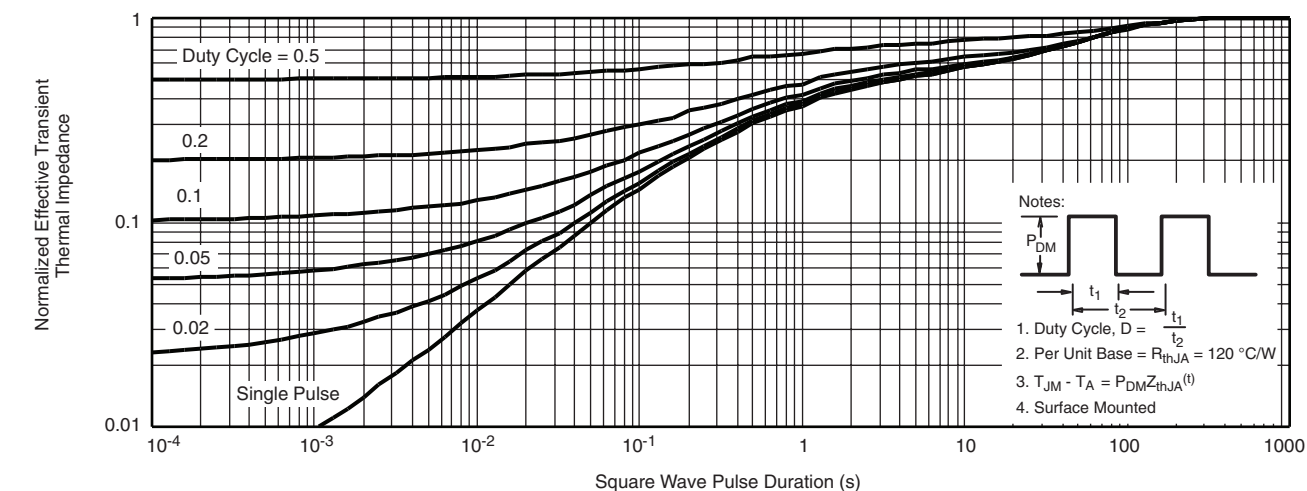
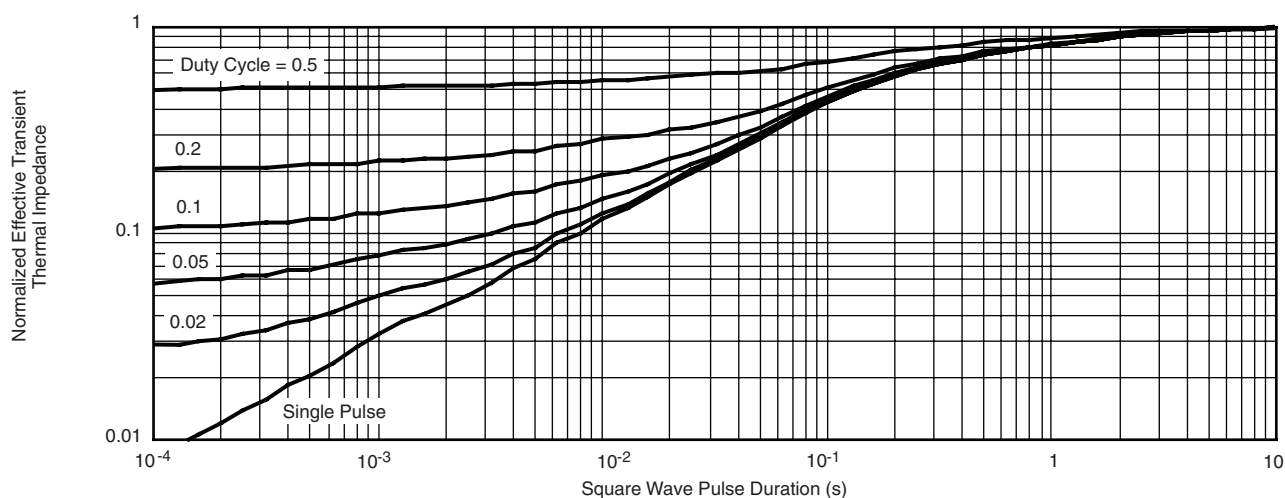


T_C - Case Temperature (°C)
Power Derating, Junction-to-Foot



T_A - Ambient Temperature (°C)
Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Foot**

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