



# PJS6600

## 30V Complementary Enhancement Mode MOSFET – ESD Protected

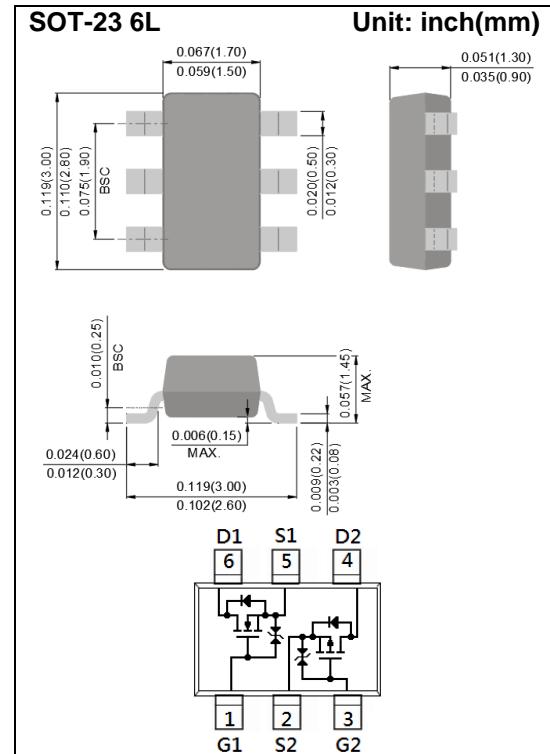
**Voltage**    30 / -30V    **Current**    1.6 /-1.1A

### Features

- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: SC0



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	N-Ch LIMIT	P-Ch LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	30	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	$\pm 8$	V
Continuous Drain Current	$I_D$	1.6	-1.1	A
Pulsed Drain Current <sup>(Note 4)</sup>	$I_{DM}$	6.4	-4.4	A
Power Dissipation	$T_a=25^\circ\text{C}$	$1.25$		W
	Derate above $25^\circ\text{C}$	10		$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150		$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>	$R_{\theta JA}$	100		$^\circ\text{C}/\text{W}$



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## N-Channel Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.5	0.78	1.3	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=1.6\text{A}$	-	145	200	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=1.1\text{A}$	-	185	270	
		$V_{\text{GS}}=1.8\text{V}, I_{\text{D}}=0.2\text{A}$	-	330	570	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	0.01	1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 8\text{V}, V_{\text{DS}}=0\text{V}$	-	1.4	$\pm 10$	$\mu\text{A}$
<b>Dynamic</b> <sup>(Note 5)</sup>						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=1.6\text{A}, V_{\text{GS}}=4.5\text{V}^{(\text{Note 1,2})}$	-	1.5	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	0.3	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	0.3	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	93	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	19	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	6	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=1.6\text{A}, V_{\text{GS}}=4.5\text{V}, R_{\text{G}}=6\Omega^{(\text{Note 1,2})}$	-	6.4	-	$\text{ns}$
Turn-On Rise Time	$t_{\text{r}}$		-	33	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	37	-	
Turn-Off Fall Time	$t_{\text{f}}$		-	32	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	1.0	A
Diode Forward Voltage	$V_{\text{sd}}$	$I_s = 1.0\text{A}, V_{\text{GS}}=0\text{V}$	-	0.81	1.2	V

### NOTES :

1. Pulse width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\text{JA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing



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## P-Channel Electrical Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.5	-0.98	-1.3	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-1.1A$	-	293	370	$m\Omega$
		$V_{GS}=-2.5V, I_D=-0.5A$	-	387	540	
		$V_{GS}=-1.8V, I_D=-0.1A$	-	750	970	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-30V, V_{GS}=0V$	-	-0.01	-1	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 8V, V_{DS}=0V$	-	$\pm 3.4$	$\pm 10$	$\mu A$
<b>Dynamic</b> <sup>(Note 5)</sup>						
Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-1.1A,$ $V_{GS}=-4.5V$ <sup>(Note 1,2)</sup>	-	1.6	-	$nC$
Gate-Source Charge	$Q_{gs}$		-	0.5	-	
Gate-Drain Charge	$Q_{gd}$		-	0.3	-	
Input Capacitance	$C_{iss}$	$V_{DS}=-15V, V_{GS}=0V,$ $f=1.0MHz$	-	125	-	$pF$
Output Capacitance	$C_{oss}$		-	22	-	
Reverse Transfer Capacitance	$C_{rss}$		-	6	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=-1.1A,$ $V_{GS}=-4.5V,$ $R_G=6\Omega$ <sup>(Note 1,2)</sup>	-	11	-	$ns$
Turn-On Rise Time	$t_r$		-	51	-	
Turn-Off Delay Time	$t_{d(off)}$		-	65	-	
Turn-Off Fall Time	$t_f$		-	46	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-1.0	A
Diode Forward Voltage	$V_{SD}$	$I_s=-1.0A, V_{GS}=0V$	-	-0.9	-1.2	V

### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
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## N-Channel TYPICAL CHARACTERISTIC CURVES

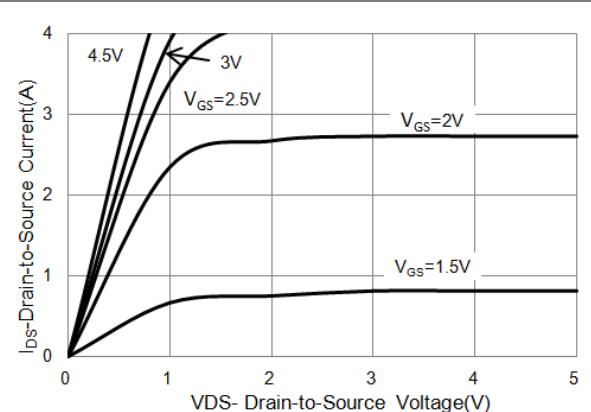


Fig.1 On-Region Characteristics

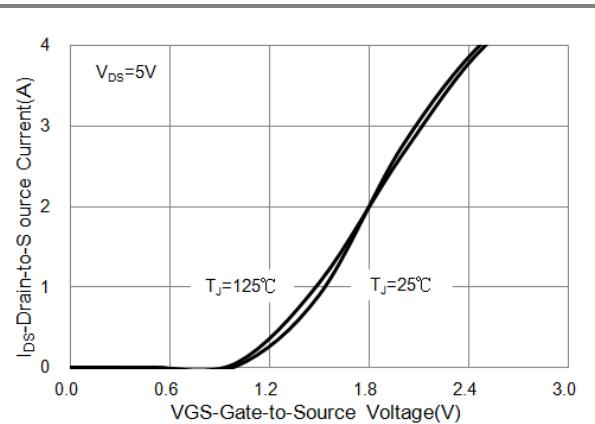


Fig.2 Transfer Characteristics

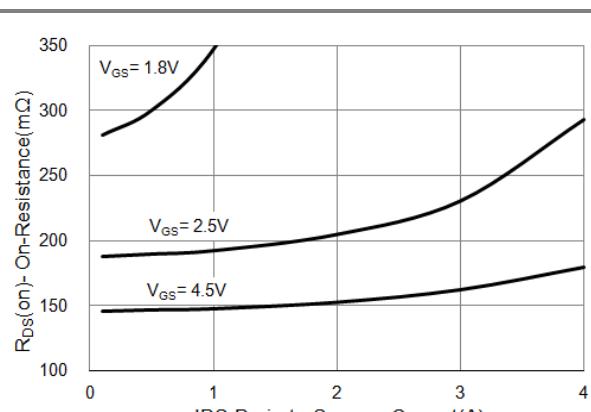


Fig.3 On-Resistance vs. Drain Current

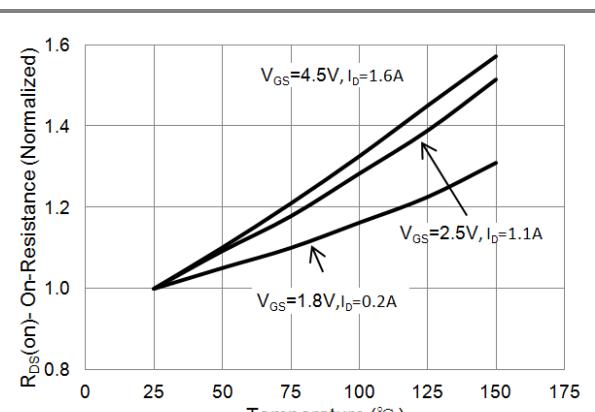


Fig.4 On-Resistance vs. Junction temperature

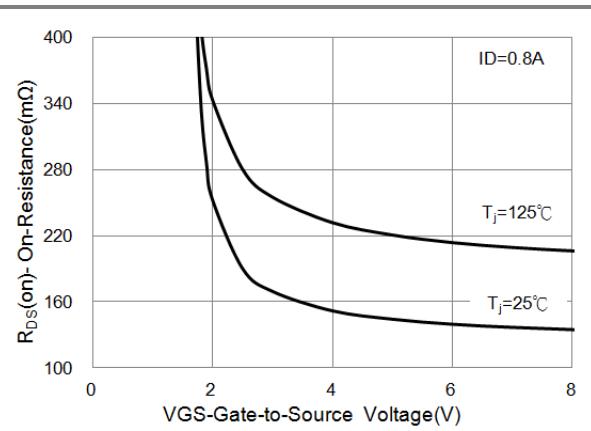


Fig.5 On-Resistance Variation with VGS.

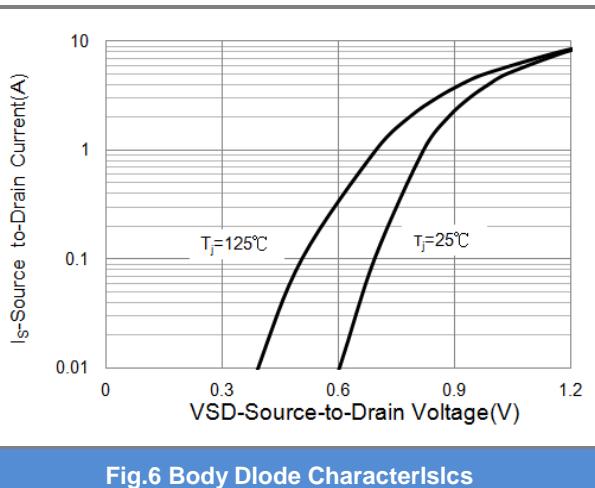


Fig.6 Body Diode Characteristics



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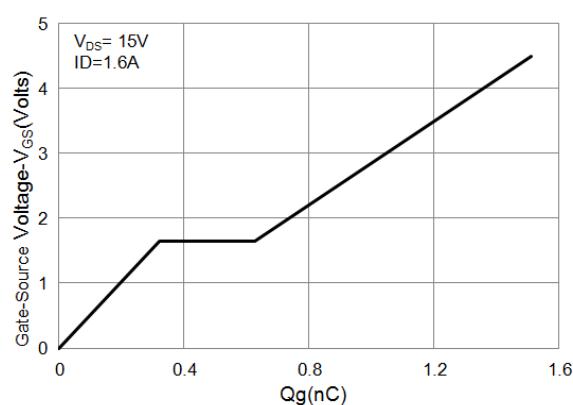


Fig.7 Gate-Charge Characteristics

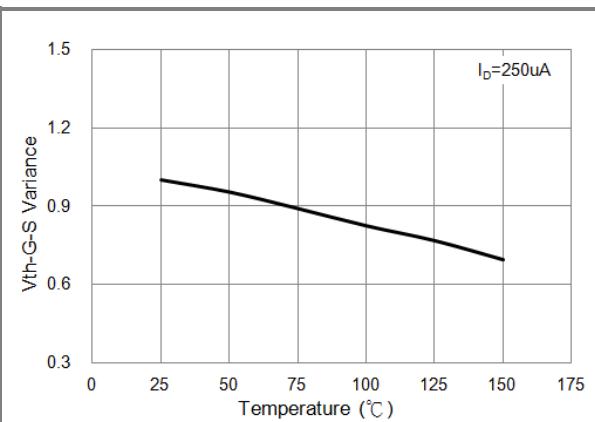


Fig.8 Threshold Voltage Variation with Temperature.

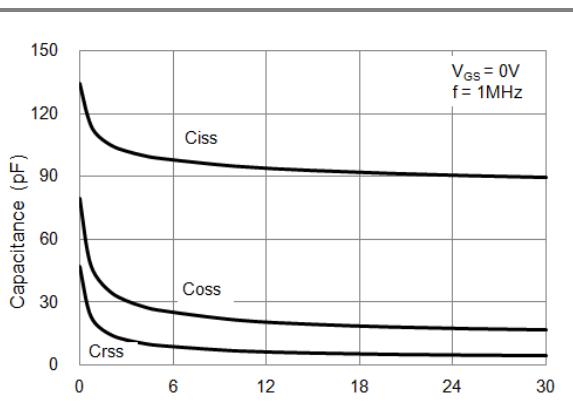


Fig.9 Capacitance vs. Drain-Source Voltage.



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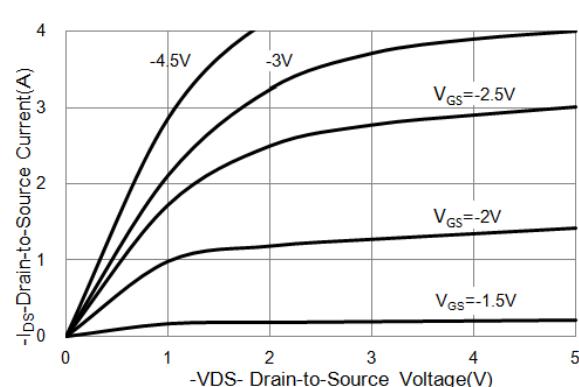


Fig.1 On-Region Characteristics

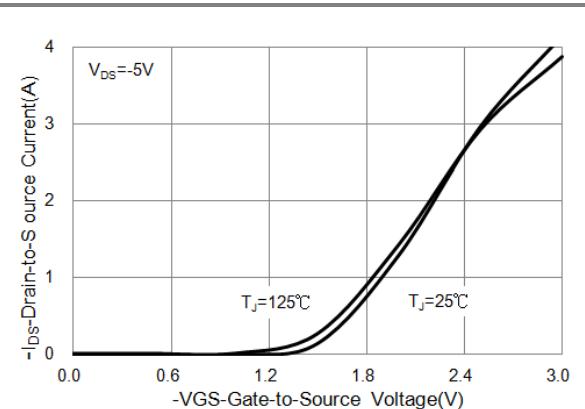


Fig.2 Transfer Characteristics

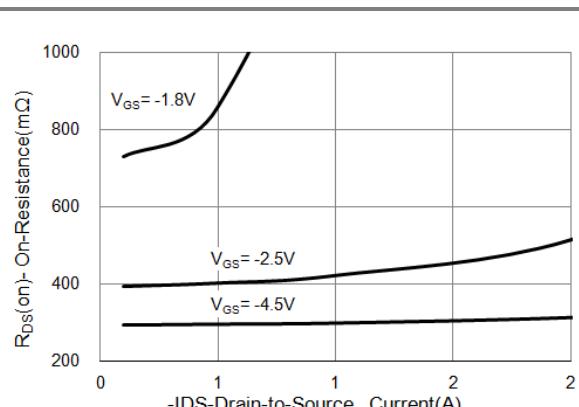


Fig.3 On-Resistance vs. Drain Current

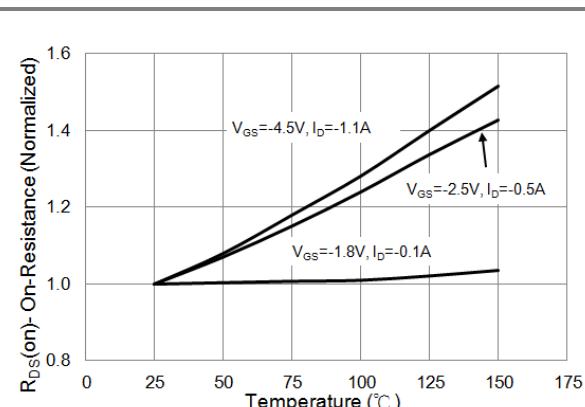


Fig.4 On-Resistance vs. Junction temperature

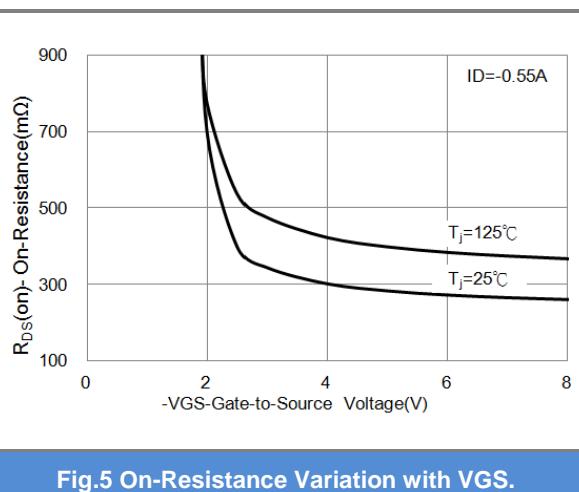


Fig.5 On-Resistance Variation with VGS.

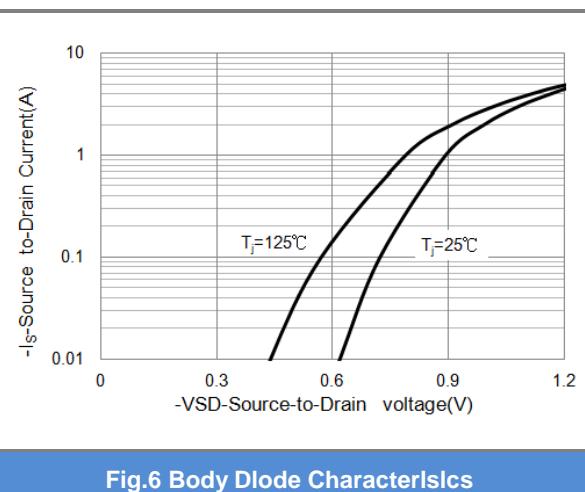
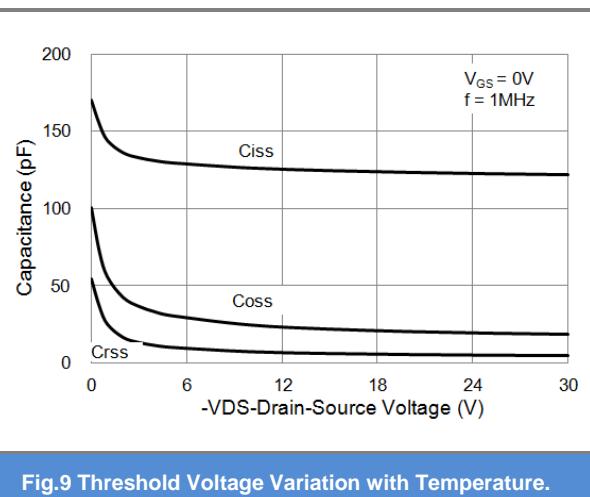
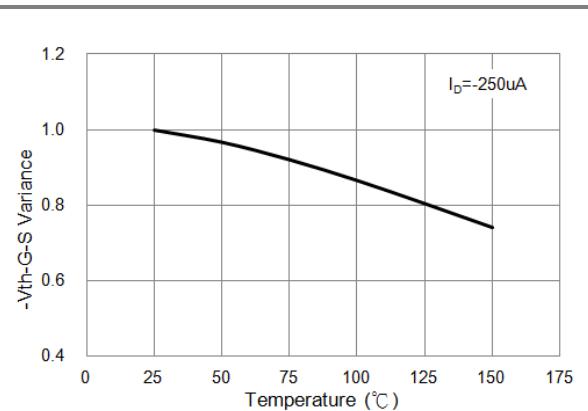
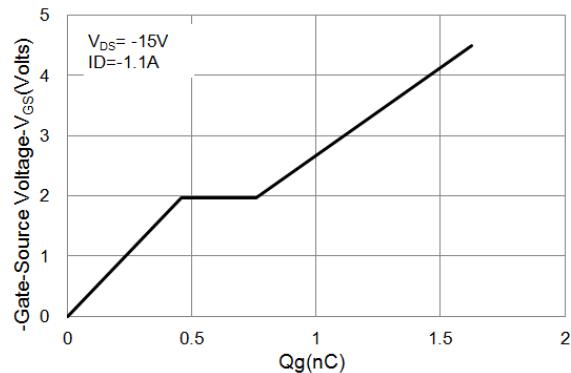


Fig.6 Body Diode Characteristics



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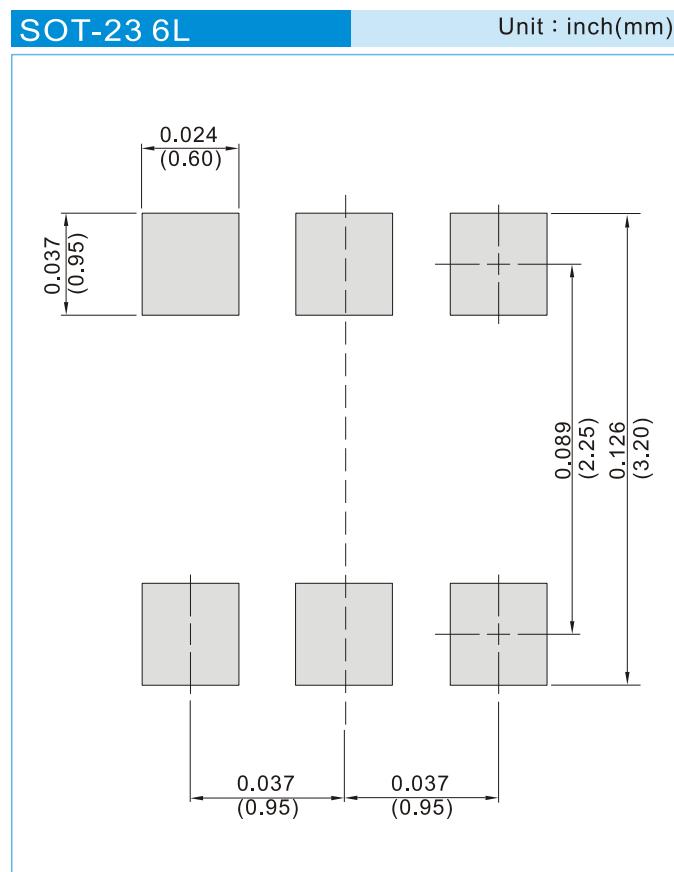


## PJS6600

### PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6600_S1_00001	SOT-23 6L	3K pcs / 7" reel	SC0	Halogen free RoHS compliant
PJS6600_S2_00001	SOT-23 6L	10K pcs / 13" reel	SC0	Halogen free RoHS compliant

### MOUNTING PAD LAYOUT





## PJS6600

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