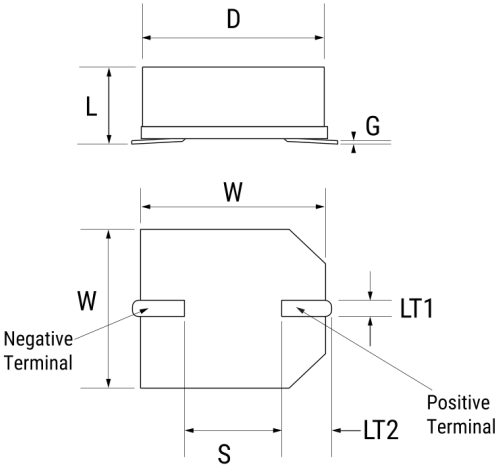




Click [here](#) for the 3D model.

Dimensions	
D	10.7mm +/-0.5mm
L	5.5mm MAX
W	10.8mm +/-0.2mm
S	5mm NOM
G	0mm +0.3/-0.1mm
LT1	1.2mm +/-0.1mm
LT2	3.9mm +0.5mm

Packaging Specifications	
Lead	V-Chip
Packaging	T&R, 380mm

General Information	
Series	FCS
Description	Surface Mount Double Layer Capacitor
Features	Ultra Low ESR, High Power Pulse
RoHS	Yes
AEC-Q200	No
Typical Component Weight	1 g

Specifications	
Capacitance	0.22 F
Capacitance Tolerance	-20/+80%
Voltage DC	3.5 VDC
Temperature Range	-25/+70°C
Rated Temperature	70°C
ESR	50000 mOhms (1kHz)
Discharge Current	0.2 mAmps (30 Minutes After Charging)

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.