PCN Num	ber:	2024	40116006.1			PCN Da	ite:	January 16, 2024	
Title:	Qualification of	of RFA	∖ Β u	B using qualified Process Technology, Die Revision and additional					
rice.	Assembly Site	fors	seled	ct devices					
Customer	Contact:		Ch	ange Management	team	Dept:		Quality Services	
Proposed	1 st Ship Date:		Ар	r 16, 2024		ated Sa Availab		Feb 16, 2024*	
*Sample ı	equests recei	ived a	a fte	r February 16, 20	24 will n	not be s	suppor	ted.	
Change Ty	/pe:								
	ly Site		☑ Design				Wafer Bump Material		
Assemb	ly Process		☐ Data Sheet				Wafer Bump Process		
Assemb	ly Materials		Part number change				Wafer Fab Site		
Mechar	ical Specification	on	☐ Test Site				Wafer	r Fab Materials	
□ Packing/Shipping/Labeling			☐ Test Process				Wafer	r Fab Process	
	PCN Details								
Description	n of Change:								
Texas Instruments is pleased to announce the addition of RFAB using the LBC9 qualified proce						9 qualified process			

Texas Instruments is pleased to announce the addition of RFAB using the LBC9 qualified process technology in addition to an Assembly site option for the devices listed below.

	Current Fab Site	•	Additional Fab Site				
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter		
SFAB	JI1	150 mm	RFAB	LBC9	300 mm		

The die was also changed as a result of the process change.

There are no construction differences for this notification.

Qual details are provided in the Qual Data Section.

Reason for Change:

These changes are part of our multiyear plan to transition products from our 150-millimeter factories to newer, more efficient manufacturing processes and technologies, underscoring our commitment to product longevity and supply continuity.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
⊠ No Change	☑ No Change	No Change	No Change

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
SH-BIP-1	SHE	USA	Sherman
RFAB	RFB	USA	Richardson

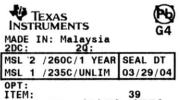
Die Rev:

Current	INE W
Die Rev [2P]	Die Rev [2P]
A, B	-

Assembly Site Information:

Assembly Site FMX	Assembly Site Origin (22L) MEX	Assembly Country Code (23L) MEX	Assembly City Aguascalientes
MLA	MLA	MYS	Kuala Lumpur

Sample product shipping label (not actual product label)





(1P) SN74LS07NSR (D) 0336 31T)LOT: 3959047MLA 4W) TKY(1T) 7523483SI2

(2P) REV: (V) 9933317 (20L) CSO: SHE (21L) CCO:USA (22L) ASO: MLA (23L) ACO: MYS (2P) REV:

5A LBL:

(L)T0:1750

Product Affected:

Group 1 Device list (RFAB/Process migration, Die Change & MLA as an additional **Assembly site):**

SN75157DR UA9637ACDR UA9637ACDRE4

Group 2 Device list (RFAB/Process migration & Die Change only):

SN75157P SN75157PSR

For alternate parts with similar or improved performance, please visit the product page on TI.com

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Name	Condition	Duration	Qual Device: UA9637ACDR	Qual Device: <u>SN75157DR</u>	Qual Device: SN75157PSR	QBS Reference (Process): TLV9062ID	QBS Reference (Package): TCAN1044VDRQ1 PG2.0	QBS Reference (Package): TCAN1044VDRQ1 PG1.1	QBS Reference (Package): <u>TL092CPS</u>
HAST	A2	Biased HAST	130C/85%RH	96 Hours	-	-	-	-	1/77/0	2/154/0	-
UHAST	A3	Autoclave	121C/15psig	96 Hours	-	-	-	-	1/77/0	2/154/0	3/230/0
TC	A4	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	-	1/77/0	2/154/0	3/231/0
HTSL	A6	High Temperature Storage Life	170C	420 Hours	-	-	-	-	-	-	3/231/0
HTSL	A6	High Temperature Storage Life	175C	500 Hours	-	-	-	-	1/45/0	2/90/0	-
HTOL	B1	Life Test	125C	1000 Hours	-	-	-	-	1/77/0	2/154/0	-
HTOL	B1	Life Test	150C	300 Hours	-	-	-	3/231/0	-	-	-
ELFR	B2	Early Life Failure Rate	125C	48 Hours	-	-	-	3/2400/11	-	-	-
WBP	C2	Bond Pull	76 Wires, 3 units min	Wires	1/76/0	1/76/0	1/76/0	-	-	-	-

Туре	#	Test Name	Condition	Duration	Qual Device: UA9637ACDR	Qual Device: SN75157DR	Qual Device: SN75157PSR	QBS Reference (Process): TLV9062ID	QBS Reference (Package): TCAN1044VDRQ1 PG2.0	QBS Reference (Package): TCAN1044VDRQ1 PG1.1	QBS Reference (Package): TL092CPS
SD	C3	PB Solderability	Precondition w.155C Dry Bake (4 hrs +/- 15 minutes)	-	-	-	-	-	-	1/15/0	-
SD	C3	PB-Free Solderability	Precondition w.155C Dry Bake (4 hrs +/- 15 minutes)	-	-	-	-	-	-	1/15/0	-
PD	C4	Physical Dimensions	Cpk>1.67	-	-	-	-	-	1/10/0	2/20/0	-
ESD	E2	ESD CDM	-	250 Volts	1/3/0	1/3/0	1/3/0	-	-	-	-
ESD	E2	ESD HBM	-	1000 Volts	-	1/3/0	-	-	-	-	-
LU	E4	Latch-Up	Per JESD78	-	-	1/3/0	-	-	-	-	-
CHAR	E5	Electrical Characterization	Min, Typ, Max Temp	-	-	1/30/0	-	-	-	-	-
CHAR	E5	Electrical Characterization	Per Datasheet Parameters	-	-	1/30/0	-	-	-	-	-

- QBS: Qual By Similarity
- Qual Device UA9637ACDR is qualified at MSL1 260C
- Qual Device SN75157DR is qualified at MSL1 260C
- Qual Device SN75157PSR is qualified at MSL1 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
 The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
 The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

TI Qualification ID: R-CHG-2205-015

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Name	Condition	Duration	Qual Device: <u>SN75157P</u>	QBS Reference (Process): TLV9062ID	QBS Reference (Package): NE5532P	QBS Reference (Package): UCC37322P	QBS Reference (Process, Product): SN75157DR
HAST	A2	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0	-	-
UHAST	A3	Autoclave	121C/15psig	96 Hours	-	-	-	3/231/0	-
TC	A4	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	3/231/0	-
HTSL	A6	High Temperature Storage Life	170C	420 Hours	-	-	-	3/231/0	-
HTOL	B1	Life Test	150C	300 Hours	-	3/231/0	3/231/0	-	-
ELFR	B2	Early Life Failure Rate	125C	48 Hours	-	3/2400/1 ¹	-	-	-
WBS	C1	Ball Shear	76 balls, 3 units min	Wires	1/76/0	-	-	-	-
WBP	C2	Bond Pull	76 Wires, 3 units min	Wires	1/76/0	-	-	-	-
SD	C3	PB-Free Solderability	8 Hours Steam Age	-	-	-	3/66/0	3/66/0	-
SD	C3	PB-Free Solderability	Precondition w.155C Dry Bake (4 hrs +/- 15 minutes); PB-Free Solder;	-	-	-	3/66/0	3/66/0	-
ESD	E2	ESD CDM	-	250 Volts	1/3/0	-	-	-	-

Туре	#	Test N ame	Condition	Duration	Qual Device: SN75157P	QBS Reference (Process): TLV9062ID	QBS Reference (Package): <u>NE5532P</u>	QBS Reference (Package): UCC37322P	QBS Reference (Process, Product): SN75157DR
ESD	E2	ESD HBM	-	1000 Volts	-	-	-	-	1/3/0
LU	E4	Latch-Up	Per JESD78	-	-	-	-	-	1/3/0
CHAR	E5	Electrical Characterization	Min, Typ, Max Temp	-	1/30/0	-	-	-	1/30/0
CHAR	E5	Electrical Characterization	Per Datasheet Parameters	-	1/30/0	-	-	-	1/30/0

- · QBS: Qual By Similarity
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

TI Qualification ID: R-CHG-2205-022

[1]-Die EOS

1 unit – Unresolved- Reran another group from same fab/assembly lot and passed.

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.