



600mA, High Efficiency uPOL Module

MUN3C1XR6-SB Series

FEATURES:

- High Density Power Module
- 600mA Output Current
- Input Voltage Range from 2.7V to 5.5V
- Fixed Output Voltage
- 100% Maximum Duty Cycle with Low Dropout
- Enable Function
- Automatic Power Saving/PWM Mode
- Protections (UVLO, OCP: Non-latching)
- Internal Soft Start
- Compact Size: 2.5mm*2.0mm*1.1mm
- Pb-free for RoHS compliant
- MSL 2, 260C Reflow

APPLICATIONS:

- Single Li-Ion Battery-Powered Equipment
- LDOs Replacement
- Cell phone / PDAs / Palmtops

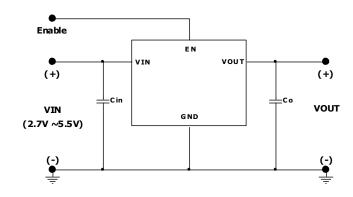
GENERAL DESCRIPTION:

The MUN3C1XR6-SB power module series is non-isolated dc-dc converters that can deliver up to 600mA of output current. The PWM switching regulator, high frequency power inductor are integrated in one hybrid package. It needs input and output capacitor only and no additional feedback circuit.

The MUN3C1XR6-SB power module series has automatic operation with PWM mode and power saving mode according to loading. Other features include remote enable function, internal soft-start, non-latching over current protection, and input under voltage locked-out capability.

The low profile and compact size package $(2.5\text{mm} \times 2.0\text{mm} \times 1.1\text{mm})$ is suitable for automated assembly by standard surface mount equipment. The MUN3C1XR6-SB power module is Pb-free and RoHS compliance.

TYPICAL APPLICATION CIRCUIT & PACKAGE:





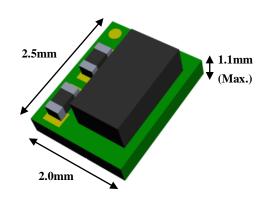


FIGURE 2 HIGH DENSITY LOW PROFILE UPOL MODULE

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ORDERING INFORMATION:

CAUTION: These devices have limited built-in ESD protection. The leads should be shorted together or the devices placed in conductive foam during the storage or handling to prevent electrostatic damage to internal circuit.

PART NUMBER	OUTPUT VOLTAGE	MARKING (Code: X)	ORDERING	QUANTITY
MHUN3C1BR6-SB	1.2V	В	MUN3C1BR6-SB	2000
MHUN3C1DR6-SB	1.35V	D	MUN3C1DR6-SB	2000
MHUN3C1ER6-SB	1.5V	E	MUN3C1ER6-SB	2000
MHUN3C1HR6-SB	1.8V	н	MUN3C1HR6-SB	2000



ELECTRICAL SPECIFICATIONS:

CAUTION: Do not operate at or near absolute maximum rating listed for extended periods of time. This stress may adversely impact product reliability and result in failures outside of warranty.

Parameter	Description	Min.	Тур.	Max.	Unit	
■ Absolute Maxim	■ Absolute Maximum Ratings					
VIN to GND		-	-	+6.0	V	
VOUT to GND		-	-	+6.0	V	
EN to GND		-	-	VIN+0.3	V	
Тс	Case Temperature of Inductor	-	-	+110	°C	
Tj	Junction Temperature	-40	-	+150	°C	
Tstg	Storage Temperature	-40	-	+125	°C	
	Human Body Model (HBM)	-	-	2k	V	
ESD Rating	Machine Model (MM)	-	-	200	V	
	Charge Device Model (CDM)	-	-	1k	V	
■ Recommendation	■ Recommendation Operating Ratings					
VIN	Input Supply Voltage	+2.7	-	+5.5	V	
Ta	Ambient Temperature	-40	-	+85	°C	
■ Thermal Inform	■ Thermal Information					
Rth(j _{choke} -a)	Thermal resistance from junction to ambient. (Note 1)	-	55	-	°C/W	

NOTES:

Rth(j_{choke}-a) is measured with the component mounted on an effective thermal conductivity test board on 0 LFM condition. The test board size is 30mm×30mm×1.6mm with 2 layers. The test condition is compliant with JEDEC EIJ/JESD 51 Standards.



ELECTRICAL SPECIFICATIONS: (Cont.)

Conditions: $T_A = 25$ °C, Vin = 3.3V, Cin = 4.7uF/6.3V/0603, Cout = 10uF/6.3V/0402 unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
■ Input	■ Input Characteristics						
		Vin = 3.3V, Vin connected EN by 100K ohm EN = GND	-	-	-	. uA	
I _{SD(IN)}	Input shutdown	Vout=1.2 V	-	0.1	-		
1 3D(1N)	current	Vout=1.35 V	-	0.1	-	u u A	
		Vout=1.5 V	-	0.1	-		
		Vout=1.8 V	-	0.1	-		
		Vin = 3.3V, Iout = 0A EN = VIN	-	-	-	uA	
IO(INI)	Input supply bias- current	Vout=1.2 V	-	50	-		
		Vout=1.35 V	-	56	-		
		Vout=1.5 V	-	60	-		
		Vout=1.8 V	-	70	-		
		Vin = 3.3V, EN = VIN Iout = 5mA	-	-	-		
	Input supply current	Vout=1.2 V	-	2.2	-	mA	
		Vout=1.35 V	-	2.5	-		
		Vout=1.5 V	-	2.7	-		
$I_{S(IN)}$		Vout=1.8 V	-	3.2	-		
		Vin = 3.3V, EN = VIN Iout = 600mA	-	-	-		
		Vout=1.2 V	-	285	-	mA	
		Vout=1.35 V	-	310	-		
		Vout=1.5 V	-	340	-		
		Vout=1.8 V	-	400	-		



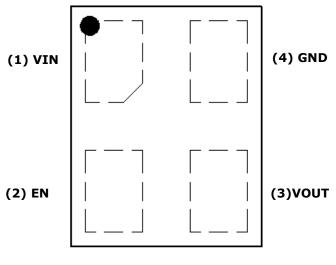
ELECTRICAL SPECIFICATIONS: (Cont.)

Conditions: $T_A = 25$ °C, Vin = 3.3V, Cin = 4.7uF/6.3V/0603, Cout = 10uF/6.3V/0603 unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
■ Outp	Output Characteristics					
I _{OUT(DC)}	Output continuous current range	Vin=3.3V	0	-	600	mA
V _{O(SET)}	Ouput Voltage Set Point	Vin=3.3V	-3.0	-	+3.0	% V _{O(SET)}
Δ Vout / Δ Vin	Line regulation accuracy	Vin = 3.3V to 5V Iout = 0A Iout = 600mA	-	-	1	% V _{O(SET)}
Δ V оит / Δ I оит	Load regulation accuracy	Iout = 0A to 600mA Vin = 3.3V,	-	1.5	-	% V _{O(SET)}
Cout(MAX)	Maximum capacitive load	Iout = 600mA, ESR \ge 1 m Ω	-	-	22	uF
■ Cont	rol Characteristic	CS				
Fosc	Oscillator frequency	PWM Operation	-	3.0	-	MHz
V	Enable rising threshold voltage		1.5	-	-	٧
V _{EN_TH}	Enable falling threshold voltage		-	-	0.4	V
■ Fault Protection						
V _{UVLO_TH}	Input under voltage lockout threshold	Falling	-	-	2.5	V
Тотр	Over temp protection		-	160	-	°C
Ішміт_тн	Current limit threshold	Peak value of inductor current,	1.3		2.5	Α



PIN CONFIGURATION:



TOP VIEW

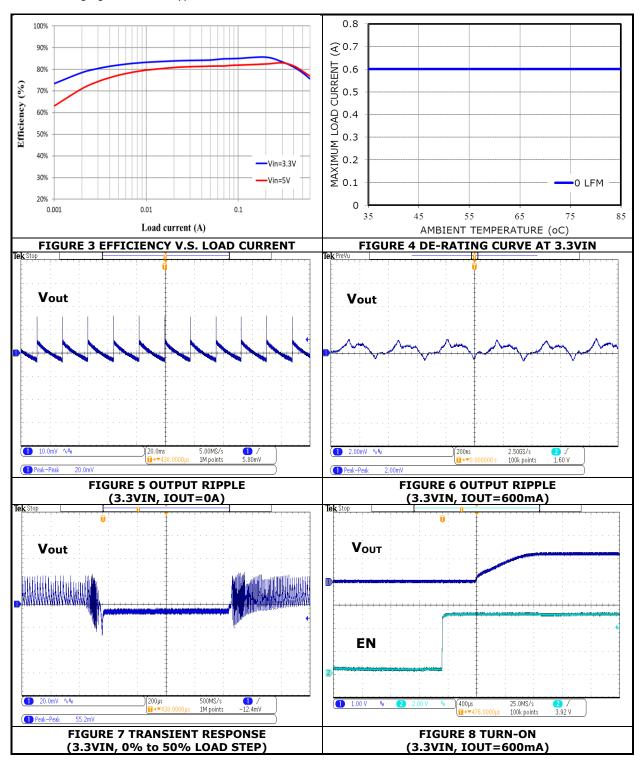
PIN DESCRIPTION:

Symbol	Pin No.	Description	
VIN	1	Power input pin. It needs to be connected to input rail with input capacitor. A 4.7uF capacitor at least for input.	
EN	2	On/Off control pin for module. EN = LOW, the module is off. EN = HIGH, the module is on.	
VOUT	3	Power output pin. Connect to output for the load with output capacitor. A 10uF capacitor at least for output.	
GND	4	Power ground pin for signal, input, and output return path. This pin needs to be connected to one or more ground plane directly. Connect to thermal exposed pad of GND for heat transferring.	



TYPICAL PERFORMANCE CHARACTERISTICS: (1.2VOUT)

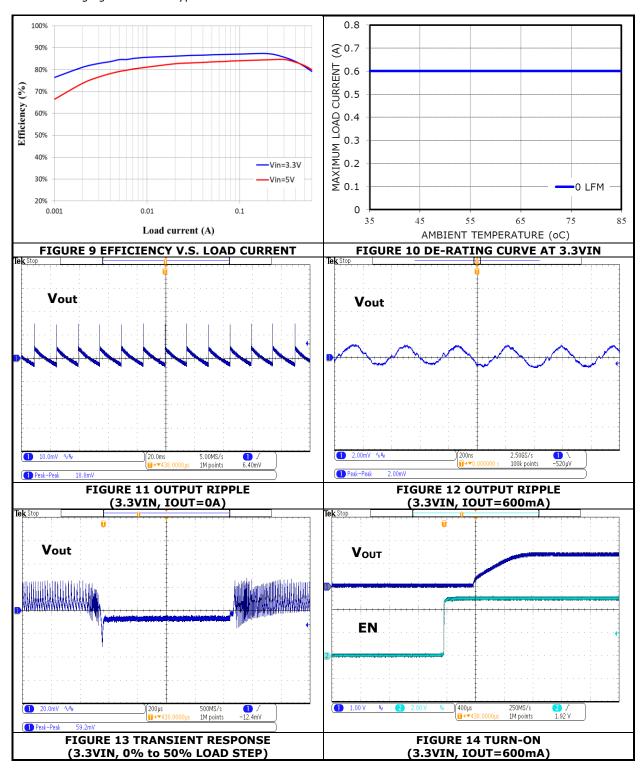
Conditions: $T_A = 25$ °C, unless otherwise specified. Test Board Information: $30\text{mm} \times 30\text{mm} \times 1.6\text{mm}$, 2 layers. The output ripple and transient response are measured by short loop probing and limited to 20 MegHz bandwidth. The following figures are the typical characteristic curves at 1.2Vout.





TYPICAL PERFORMANCE CHARACTERISTICS: (1.35VOUT)

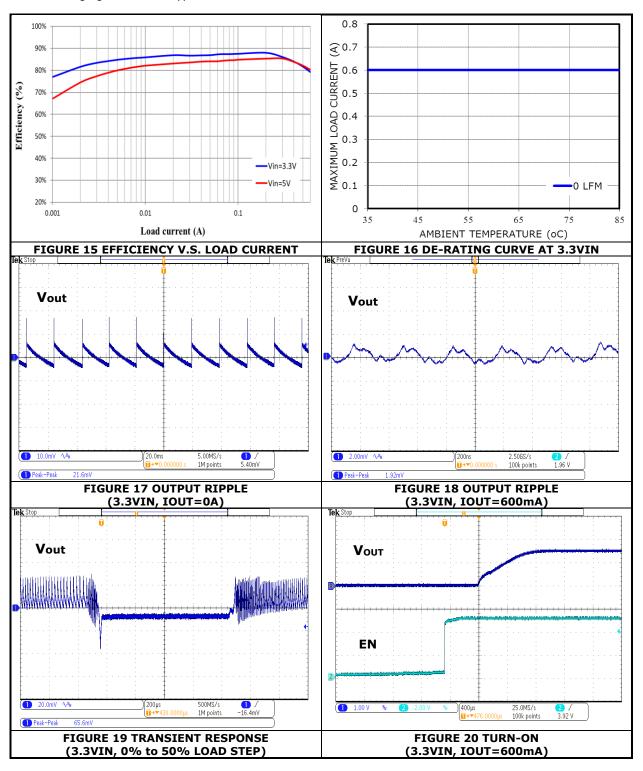
Conditions: $T_A = 25$ °C, unless otherwise specified. Test Board Information: 30mm \times 30mm \times 1.6mm, 2 layers. The output ripple and transient response are measured by short loop probing and limited to 20MegHz bandwidth. The following figures are the typical characteristic curves at 1.35Vout.





TYPICAL PERFORMANCE CHARACTERISTICS: (1.5VOUT)

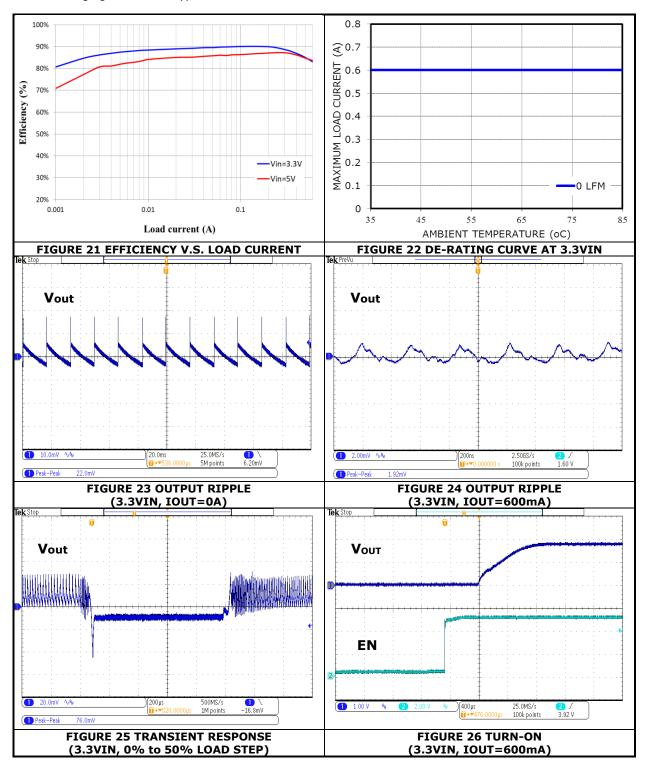
Conditions: $T_A = 25$ °C, unless otherwise specified. Test Board Information: 30mm \times 30mm \times 1.6mm, 2 layers. The output ripple and transient response are measured by short loop probing and limited to 20MegHz bandwidth. The following figures are the typical characteristic curves at 1.5Vout.





TYPICAL PERFORMANCE CHARACTERISTICS: (1.8VOUT)

Conditions: $T_A = 25$ °C, unless otherwise specified. Test Board Information: 30mm \times 30mm \times 1.6mm, 2 layers. The output ripple and transient response are measured by short loop probing and limited to 20MegHz bandwidth. The following figures are the typical characteristic curves at 1.8Vout.





APPLICATIONS INFORMATION:

REFERENCE CIRCUIT FOR GENERAL APPLICATION:

The Figure 27 shows the module application schematics for input voltage +5V or +3.3V and turn on by input voltage directly through enable resistor (Ren).

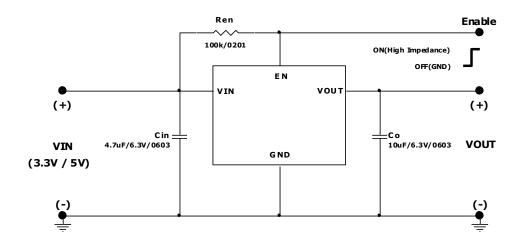


FIGURE 27 GENERAL APPLICATION CIRCUIT WITH TURN-ON BY INPUT VOLTAGE

SAFETY CONSIDERATIONS:

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line. The installer must observe all relevant safety standards and regulations. For safety agency approvals, install the converter in compliance with the end-user safety standard.

INPUT FILTERING:

The module should be connected to a source supply of low AC impedance and high inductance in which line inductance can affect the module stability. An input capacitor must be placed as near as possible to the input pin of the module so to minimize input ripple voltage and ensure module stability.

OUTPUT FILTERING:

To reduce output ripple and improve the dynamic response as the step load changes, an additional capacitor at the output must be connected. Low ESR polymer and ceramic capacitors are recommended to improve the output ripple and dynamic response of the module.



APPLICATIONS INFORMATION: (Cont.)

Thermal Considerations:

All of thermal testing condition is complied with JEDEC EIJ/JESD 51 Standards. Therefore, the test board size is 30mm×30mm×1.6mm with 2 layers. The case temperature of module sensing point is shown as Figure 28. Then $Rth(j_{choke}-a)$ is measured with the component mounted on an effective thermal conductivity test board on 0 LFM condition. The MUN3C1XR6-SB series modules are designed for using when the case temperature is below 110°C regardless the change of output current, input/output voltage or ambient temperature.

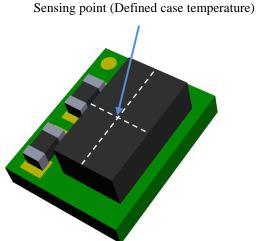
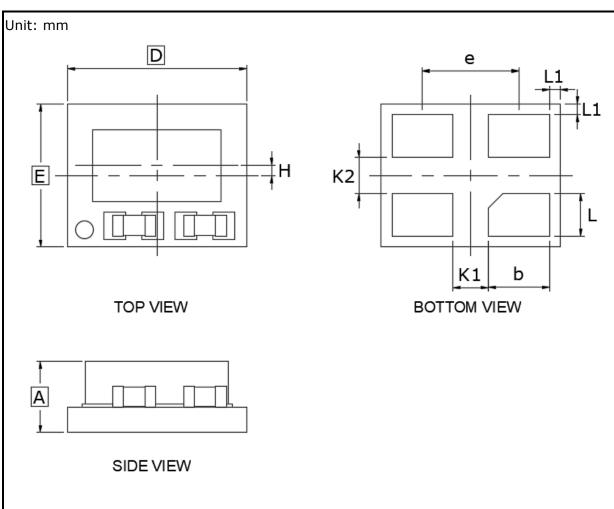


Figure 28. CASE TEMPERATURE SENSING POINT



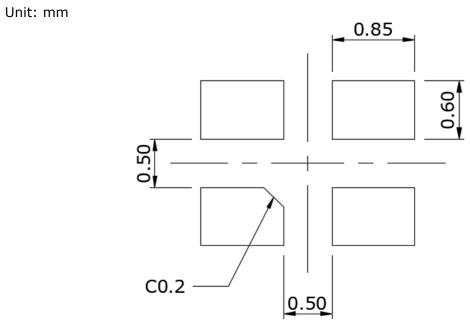
PACKAGE OUTLINE DRAWING:



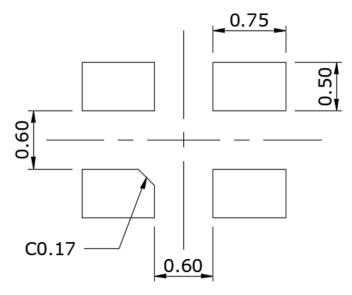
DIM	MIL	LIMETER	S
וייונטן	MIN	NOM	MAX
Α	-	-	1.10
b	0.75	0.85	0.95
D	2.35	2.5	2.65
E	1.85	2.0	2.15
е	1.25	1.35	1.45
Н	0.06	0.14	0.22
K1	0.40	0.5	0.60
K2	0.40	0.5	0.60
L	0.50	0.60	0.70
L1	0.00	0.15	0.30



LAND PATTERN REFERENCE:



RECOMMENDED LAND PATTERN



*RECOMMENDED STENCIL PATTERN BASED ON $100\mu m$ THICKNESS STENCIL (Reference only)

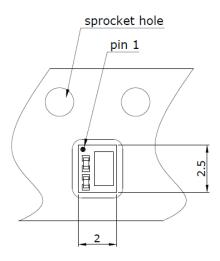
 $*65\sim100\%$ printed solder coverage of land pad opening is acceptable.



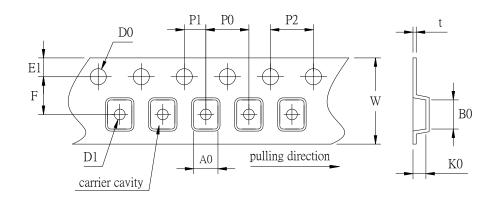
PACKING REFERENCE:

Unit: mm

Package In Tape Loading Orientation



Tape Dimension



A0	2.32 ± 0.10	E1	1.75 ± 0.10
В0	2.82 ± 0.10	K0	1.15 ± 0.10
F	3.50 ± 0.05	P0	4.00 ± 0.10
W	8.00 +0.30/-0.10	P1	2.00 ± 0.05
D0	φ1.50 +0.1/-0.0	P2	4.00 ± 0.10
D1	φ1.0 Min.	t	0.25 ± 0.05

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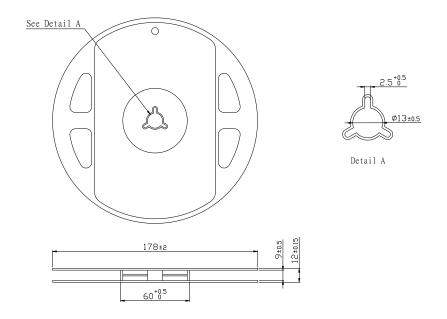


Unit: mm

PACKING REFERENCE: (Cont.)



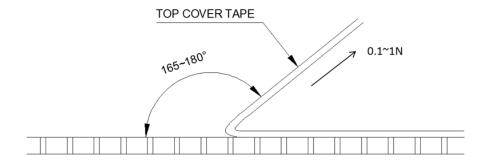
Reel Dimension



Peel Strength of Top Cover Tape

The peel speed shall be about 300mm/min.

The peel force of top cover tape shall between 0.1N to 1.0N



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REVISION HISTORY:

Date	Revision	Changes
2018.08.30	00	Initial released for preliminary datasheet.
2018.12.05	01	Add 1.35V output voltage
2019.07.02	02	Add thermal resistance from junction to ambient Rth(jchoke-a)