onsemi

Dual PNP Bias Resistor Transistors R1 = 47 k\Omega, R2 = 22 k Ω

PNP Transistors with Monolithic Bias Resistor Network

MUN5137DW1, NSBA144WDXV6, NSBA144WDP6

This series of digital transistors is designed to replace a single device and its external resistor bias network. The Bias Resistor Transistor (BRT) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base–emitter resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space.

Features

- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

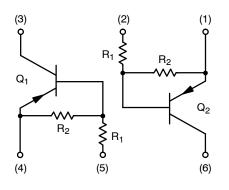
MAXIMUM RATINGS

(T_A = 25°C, common for Q1 and Q2, unless otherwise noted)

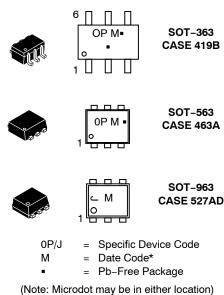
Rating	Symbol	Max	Unit
Collector-Base Voltage	V _{CBO}	50	Vdc
Collector-Emitter Voltage	V _{CEO}	50	Vdc
Collector Current – Continuous	Ι _C	100	mAdc
Input Forward Voltage	V _{IN(fwd)}	40	Vdc
Input Reverse Voltage	V _{IN(rev)}	10	Vdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

PIN CONNECTIONS



MARKING DIAGRAMS



*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering, marking, and shipping information on page 2 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 2.

ORDERING INFORMATION

Device	Package	Shipping [†]		
MUN5137DW1T1G	SOT-363	3,000 / Tape & Reel		
NSVMUN5137DW1T1G	SOT-363	3,000 / Tape & Reel		
NSBA144WDXV6T1G	SOT-563	4,000 / Tape & Reel		

DISCONTINUED (Note 1)

NSBA144WDP6T5G SOT-963 8,0	00 / Tape & Reel
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†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

1. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on <u>www.onsemi.com</u>.

THERMAL CHARACTERISTICS

	Characteristic	Symbol	Мах	Unit
MUN5137DW1 (SOT-363)	One Junction Heated			
Total Device Dissipation T _A = 25°C Derate above 25°C	(Note 2) (Note 3) (Note 2) (Note 3)	PD	187 256 1.5 2.0	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 2) (Note 3)	$R_{ hetaJA}$	670 490	°C/W
MUN5137DW1 (SOT-363)	Both Junction Heated (Note 4)			
Total Device Dissipation T _A = 25°C Derate above 25°C	(Note 2) (Note 3) (Note 2) (Note 3)	PD	250 385 2.0 3.0	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 2) (Note 3)	R _{θJA}	493 325	°C/W
Thermal Resistance, Junction to Lead	(Note 2) (Note 3)	R _{θJL}	188 208	°C/W
Junction and Storage Tem	perature Range	T _J , T _{stg}	-55 to +150	°C
NSBA144WDXV6 (SOT-5	63) One Junction Heated			
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C	(Note 2) (Note 2)	PD	357 2.9	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 2)	R _{θJA}	350	°C/W
NSBA144WDXV6 (SOT-5	63) Both Junction Heated (Note 4)			
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	(Note 2) (Note 2)	PD	500 4.0	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 2)	R _{θJA}	250	°C/W
Junction and Storage Tem	perature Range	T _J , T _{stg}	–55 to +150	°C
NSBA144WDP6 (SOT-96	3) One Junction Heated			
Total Device Dissipation T _A = 25°C Derate above 25°C	(Note 5) (Note 6) (Note 5) (Note 6)	PD	231 269 1.9 2.2	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 5) (Note 6)	R _{θJA}	540 464	°C/W

THERMAL CHARACTERISTICS

	Characteristic	Symbol	Мах	Unit		
NSBA144WDP6 (SOT-963) Both Junction Heated (Note 4)						
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C	(Note 5) (Note 6) (Note 5) (Note 6)	PD	339 408 2.7 3.3	mW mW/°C		
Thermal Resistance, Junction to Ambient	(Note 5) (Note 6)	$R_{ extsf{ heta}JA}$	369 306	°C/W		
Junction and Storage Tem	perature Range	T _J , T _{stg}	–55 to +150	°C		

2. FR-4 @ Minimum Pad.

3. FR-4 @ 1.0 x 1.0 Inch Pad.

4. Both junction heated values assume total power is sum of two equally powered channels.

FR-4 @ 100 mm², 1 oz. copper traces, still air.
 FR-4 @ 500 mm², 1 oz. copper traces, still air.

ELECTRICAL CHARACTERISTICS	$(T_A = 25^{\circ}C, \text{ common for } Q_1 \text{ and } C$	2, unless otherwise noted)
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Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector-Base Cutoff Current $(V_{CB} = 50 \text{ V}, I_E = 0)$	I _{CBO}	_	_	100	nAdc
Collector–Emitter Cutoff Current $(V_{CE} = 50 \text{ V}, I_B = 0)$	ICEO	-	-	500	nAdc
Emitter-Base Cutoff Current $(V_{EB} = 6.0 \text{ V}, I_C = 0)$	I _{EBO}	-	_	0.13	mAdc
Collector–Base Breakdown Voltage $(I_C = 10 \ \mu A, I_E = 0)$	V _{(BR)CBO}	50	_	-	Vdc
Collector–Emitter Breakdown Voltage (Note 7) $(I_C = 2.0 \text{ mA}, I_B = 0)$	V _{(BR)CEO}	50	_	-	Vdc
ON CHARACTERISTICS					
DC Current Gain (Note 7) (I _C = 5.0 mA, V _{CE} = 10 V)	h _{FE}	80	140	-	
Collector-Emitter Saturation Voltage (Note 7) $(I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA})$	V _{CE(sat)}	_	_	0.25	Vdc
Input Voltage (off) $(V_{CE} = 5.0 \text{ V}, I_C = 100 \mu\text{A})$	V _{i(off)}	-	1.7	-	Vdc
Input Voltage (on) $(V_{CE} = 0.2 \text{ V}, I_C = 3.0 \text{ mA})$	V _{i(on)}	_	2.7	_	Vdc
Output Voltage (on) (V _{CC} = 5.0 V, V _B = 4.0 V, R _L = 1.0 k Ω)	V _{OL}	-	_	0.2	Vdc
Output Voltage (off) ($V_{CC} = 5.0 \text{ V}, V_B = 0.5 \text{ V}, R_L = 1.0 \text{ k}\Omega$)	V _{OH}	4.9	-	-	Vdc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

R1

 R_1/R_2

32.9

1.7

47

2.1

61.1

2.6

kΩ

7. Pulsed Condition: Pulse Width = 300 msec, Duty Cycle \leq 2%.

Input Resistor

Resistor Ratio

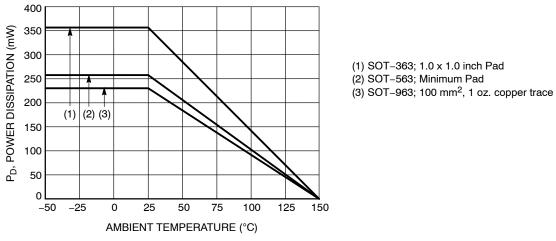
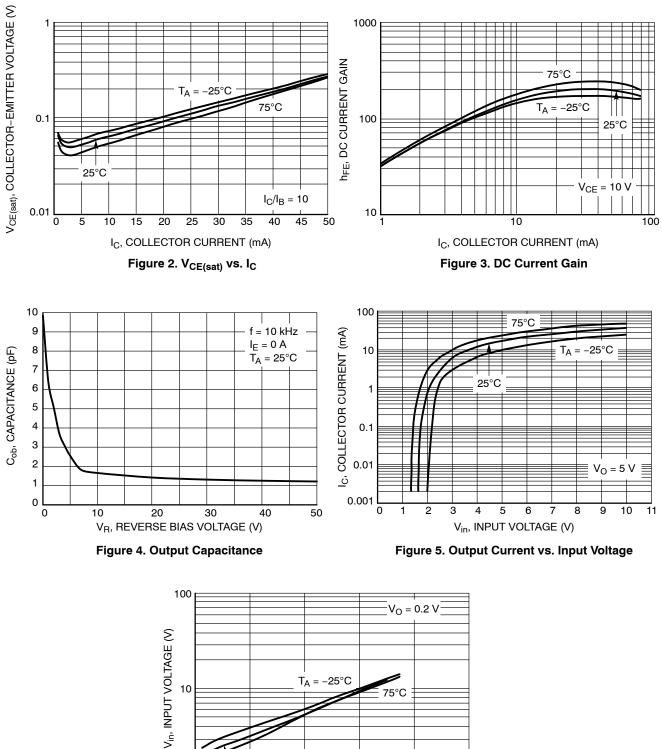
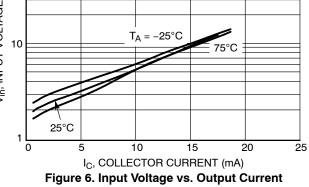


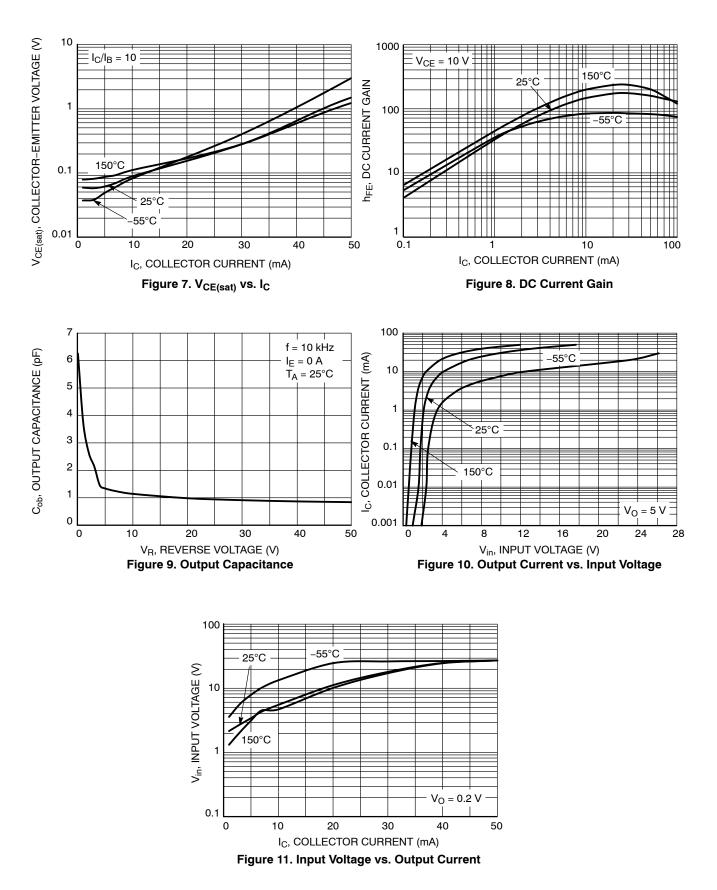
Figure 1. Derating Curve

TYPICAL CHARACTERISTICS MUN5137DW1, NSBA144WDXV6





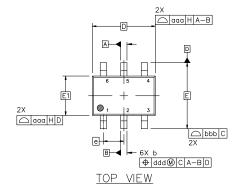
TYPICAL CHARACTERISTICS NSBA144WDP6



SC-88 2.00x1.25x0.90, 0.65P CASE 419B-02 **ISSUE Z**

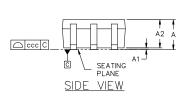
DATE 18 APR 2024

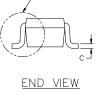
DUSEM



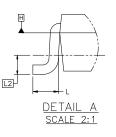
NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME 1. Y14.5-2018.
- 2.
- ALL DIMENSION ARE IN MILLIMETERS. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 3. PER END.
- 4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF DATUMS A AND B ARE DETERMINED AT DATUM H.
- 5.
- DIMENSIONS & AND C APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP. 6.
- DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. 7 ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION & AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

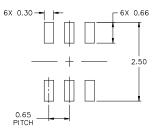




DETAIL A



	MILLIMETERS				
DIM	MIN. NOM. MAX.				
A			1.10		
A1	0.00		0.10		
A2	0.70	0.90	1.00		
b	0.15	0.20	0.25		
с	0.08	0.15	0.22		
D	2.00 BSC				
E	2.10 BSC				
E1	1.25 BSC				
е		0.65 BSC)		
L	0.26	0.36	0.46		
L2	0.15 BSC				
aaa	0.15				
bbb	0.30				
ccc	0.10				
ddd		0.10			



RECOMMENDED MOUNTING FOOTPRINT*

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

XXX = Specific Device Code = Date Code* Μ

GENERIC **MARKING DIAGRAM***

XXXM-

0

6

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or position may vary depending upon manufacturing location.

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLES ON PAGE 2

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SC-88 2.00x1.25x0.90, 0.65P CASE 419B-02 ISSUE Z

DATE 18 APR 2024

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 2: CANCELLED	STYLE 3: CANCELLED	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	STYLE 8: CANCELLED	STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
STYLE 13:	STYLE 14:	STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:
PIN 1. ANODE	PIN 1. VREF	PIN 1. ANODE 1	PIN 1. BASE 1	PIN 1. BASE 1	PIN 1. VIN1
2. N/C	2. GND	2. ANODE 2	2. EMITTER 2	2. EMITTER 1	2. VCC
3. COLLECTOR	3. GND	3. ANODE 3	3. COLLECTOR 2	3. COLLECTOR 2	3. VOUT2
4. EMITTER	4. IOUT	4. CATHODE 3	4. BASE 2	4. BASE 2	4. VIN2
5. BASE	5. VEN	5. CATHODE 2	5. EMITTER 1	5. EMITTER 2	5. GND
6. CATHODE	6. VCC	6. CATHODE 1	6. COLLECTOR 1	6. COLLECTOR 1	6. VOUT1
STYLE 19:	STYLE 20:	STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:
PIN 1. I OUT	PIN 1. COLLECTOR	PIN 1. ANODE 1	PIN 1. D1 (i)	PIN 1. Vn	PIN 1. CATHODE
2. GND	2. COLLECTOR	2. N/C	2. GND	2. CH1	2. ANODE
3. GND	3. BASE	3. ANODE 2	3. D2 (i)	3. Vp	3. CATHODE
4. V CC	4. EMITTER	4. CATHODE 2	4. D2 (c)	4. N/C	4. CATHODE
5. V EN	5. COLLECTOR	5. N/C	5. VBUS	5. CH2	5. CATHODE
6. V REF	6. COLLECTOR	6. CATHODE 1	6. D1 (c)	6. N/C	6. CATHODE
STYLE 25:	STYLE 26:	STYLE 27:	STYLE 28:	STYLE 29:	STYLE 30:
PIN 1. BASE 1	PIN 1. SOURCE 1	PIN 1. BASE 2	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. SOURCE 1
2. CATHODE	2. GATE 1	2. BASE 1	2. DRAIN	2. ANODE	2. DRAIN 2
3. COLLECTOR 2	3. DRAIN 2	3. COLLECTOR 1	3. GATE	3. COLLECTOR	3. DRAIN 2
4. BASE 2	4. SOURCE 2	4. EMITTER 1	4. SOURCE	4. EMITTER	4. SOURCE 2
5. EMITTER	5. GATE 2	5. EMITTER 2	5. DRAIN	5. BASE/ANODE	5. GATE 1
6. COLLECTOR 1	6. DRAIN 1	6. COLLECTOR 2	6. DRAIN	6. CATHODE	6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



ONSEMI

STATE 1 STATE 2 PIN 1 CATE 15 FEB 2024 PIN 1<				ISSUE J				
 1. DIMENSIONING AND TOLERANDONG CONFORM TO ASME 2. ALL DIMENSION ARE IN MULIMETERS. 2. ALL DIMENSION ARE IN MULIMETERS. 3. ALL DIMENSION ARE IN MULIMETERS. 3. ALL DIMENSION ARE IN MULIMETERS. 4. ALL DIMENSION ARE IN MULIMETERS. 5. MARKEN ARE AND AR				100020			DA	TE 15 FEB 2024
 TYLE 5. ALL DIMENSION ARE IN MILLIMETERS. ALL DIMENSION ARE IN MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LAD FINISH THE MINIMUM LEAD THICKNESS INCLUDES INCLUDES				NOTES:				
 2. ALL DUČENSION ARE: IN MILLIMETERS LAD FINISH THICKNESS, MINIMUM LEAD THICKNESS IS THE MINIMUM HICKNESS OF BASE MATERAL. 3. MATRIM, LEAD THICKNESS IS THE MINIMUM HICKNESS OF BASE MATERAL. THE MITTER I STYLE 1/ TOP_NEW STYLE 2/ TOP_NEW STYLE 2/ STYLE 1/ STYLE 2/ STYLE 2						RANCING	CONFORM	M TO ASME
THUCKNESS MINIMUM LEAD THUCKNESS IS THE MINIMUM HICKNESS OF BASE MATERIAL. THUCKNESS OF BASE AND THE THUCKNESS OF BASE MATERIAL. THUCKNESS OF BASE AND THE THUCKNESS OF BASE AND THAT THAT						MILLIMETI	ERS.	
FIN F				THICKNESS.	MINIMUM LE	AD THICK		
PIN 1 FOR ADDITIONAL INFORMATION ON OUR Ph-FREE STYLE 1/ TOP. VEW STYLE 2/ S. BASE 2/ S. BASE 2/ S. BASE 2/ S. BASE 2/ S. BASE 2/ S. BASE 2/ S. CATHODE STYLE 3/ S. CATHODE STYLE 3/ S. CATHODE STYLE 3/ S. CATHODE STYLE 4/ S. COLLECTOR 2/ S. CATHODE STYLE 3/ S. CATHODE STYLE 5/ S. CATHODE STYLE 5/ S. CATHODE STYLE 5/ S. CATHODE STYLE 5/ S. CATHODE STYLE 7/ S. COLLECTOR STYLE 9/ S. CATHODE STYLE 7/ S. CATHODE STYLE 9/ S. CATHODE STYLE 7/ S. CATHODE STYLE 9/ S. CATHODE STYLE 9/ S	-	— D — — A	A -	 - 6X	DIM	М	ILLIMETE	RS
PIN 1		B				MIN.	NDM.	MAX.
PIN 1 Image: Construction of the constru					A	0.50	0.55	0.60
STYLE 1: TOP_VIEW STYLE 3: TOP_VIEW STYLE 3: TOP_VIEW STYLE 3: TOP_VIEW STYLE 3: TOP_VIEW STYLE 1: PIN 1: EMITTER 1 2: BASE 2: 3: COLLECTOR 2: 5: BASE 2: 5: CATHODE 3: 6: COLLECTOR 1: 6: CATHODE 3: 6: CATHODE 3: 6: CATHODE 4: 5: CATHODE 4: 5: CATHODE 5: 6: COLLECTOR 5: 6: COLLECTOR 5: 6: CATHODE 4: 6: CATHODE 5: 6: CATHODE 7: 7: CATHODE 7: CATHODE 7: 7: CATHODE 7: CATHODE 7: CATHODE 7: 7: CATHODE 7: CATHODE 7: 7: CATHODE 7: CATHODE 7: 7: CATHODE 7:		•			b	0.17	0.22	0.27
STYLE I: TOP VEW STYLE 2: TOP VEW STYLE 3: SIDE VEW D 1.50 1.60 1.70 E D.D. VEW SIDE VEW SIDE VEW E 0.50 BSC H 1.50 1.60 1.70 E D.D. VEW SIDE VEW SIDE VEW E 0.30	REFERENCE				C	0.08	0.13	0.18
STYLE 1: TOP_VEW SIDE_VIEW Image: Construct of the second second second second view assessment of construction viewasses are unconstruction					D	1.50	1.60	1.70
Image: Strike is the strike	L	 │			E	1.10	1.20	1.30
TOP VIEW SIDE VIEW TOP VIEW SIDE VIEW TOP VIEW SIDE VIEW Image: Side View Image: Side View Style 1 PIN 1: Cattor 1 2: BASE 1 2: ENTITE 2 3: COLLECTOR 2 3: BASE 2 4: EMITTER 1 PIN 1: Cattor 2 5: BASE 2 3: BASE 2 6: COLLECTOR 2 3: ANDE 4 7) 1: Cattor 2 3: ANDE 4 9) 1: Cattor 2 3: Cattor 2 6: COLLECTOR 2 3: Cattor 2 7: Cattor 2 3: Cattor 2 8: Cattor 2 3: Cattor 2 8: Cattor 2 3: Cattor 2 9: C					e		0.50 BSC	
STYLE 1/2 STYLE 2/2 STYLE 3/2 PIN 1. EMITTER 1 PIN 1. EMITTER 1 PIN 1. CATHODE 1 2. BASE 1 2. CATHODE 1 2. CATHODE 2 3. COLLECTOR 2 4. CATHODE 2 4. CATHODE 2 4. EMITTER 2 4. CATHODE 2 4. CATHODE 2 5. BASE 1 2. CATHODE 2 4. CATHODE 2 6. COLLECTOR 1 6. COLLECTOR 1 6. ANDE/ANDOE 1 STYLE 4 STYLE 5/2 STYLE 6/2 PIN 1. CATHODE 2 2. CATHODE 2 2. COLLECTOR 1 6. COLLECTOR 1 3. ANDDE 3. CATHODE 2 2. COLLECTOR 3 3. ANDDE 4 3. COLLECTOR 4 STYLE 5/2 3. COLLECTOR 5 CATHODE 2 4. CATHODE 5 STYLE 6/2 7. TYLE 4 STYLE 5/2 8. STYLE 7/2 STYLE 9/2 9. N. 1. CATHODE 5 CATHODE 6 3. CATHODE 6 CATHODE 6 4. CATHODE 7/2 SCHLECTOR 7 3. CATHODE 7/2 SCHLECTOR 7 3. CATHODE 7/2 SCHLECTOR 7 3. CATHODE 8 SCHLECTOR 7 3. CATHODE 9/1 SCHLECTOR 7 3. CATHODE 1 S				SIDE VIEW	н	1.50	1.60	1.70
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MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



SOT-963 1.00x1.00x0.37, 0.35P CASE 527AD				
ISSUE F			DATE	20 FEB 2024
NDTES:		MILLIMETERS		
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2. CONTROLLING DIMENSION: MILLIMETERS.	2018. DIM	MIN.	NDM.	MAX.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM		0,34	0.37	0,40
THICKNESS OF BASE MATERIAL.	h	0.10	0.15	0.20
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.		0.07	0.12	0.17
	D	0.95	1.00	1.05
	E	0,75	0.80	0.85
	e		0.35 BS0	2
+-+-+ Ė ⊢ Ĥ	Н	0.95	1.00	1.05
			0.19 REF	-
$T \Pi P V I F W$	L2	0.05	0.10	0.15
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L2→ → ← 6X b (\$\$\0,08 A B]	RECOMME	NDED	MOUNT	ING
	*For addition Free strateg			
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3. COLLECTOR 2 3. BASE 2 3. ANODE/ANODE 2	Reference			
4. EMITTER 2 4. COLLECTOR 2 4. CATHODE 2 5. BASE 2 5. BASE 1 5. CATHODE 2 6. COLLECTOR 1 6. COLLECTOR 1 6. ANODE/ANODE 1				
STYLE 4: STYLE 5: STYLE 6:				
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4. EMITTER4. ANODE4. CATHODE5. COLLECTOR5. CATHODE5. CATHODE	MARKING DIAGRAM*			
6. COLLECTOR 6. CATHODE 6. CATHODE STVLE 7: STVLE 9: STVLE 0:]			
STYLE 7: STYLE 8: STYLE 9: PIN 1. CATHODE PIN 1. DRAIN PIN 1. SOURCE 1 2. ANODE 2. DRAIN 2. GATE 1	1	°XXW		
3. CATHODE 3. GATE 3. DRAIN 2 4. CATHODE 4. SOURCE 4. SOURCE 2	XX - Sn	сific Devic	e Code	
5. ANODE5. DRAIN5. GATE 26. CATHODE6. DRAIN6. DRAIN 1		nth Code		
STYLE 10: *This information is generic. Please refer to PIN 1. CATHODE 1 device data sheet for actual part marking. 2. N/C Pb-Free indicator, "G" or microdot "=", may 4. ANODE 2 or may not be present. Some products may 5. N/C not follow the Generic Marking.				
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