

 ENCLUSTRA <small>FPGA SOLUTIONS</small>	Enclustra - Product Change Notification (PCN)	No.: PCN_SP_2021-10
		Date: 07.04.2021

Affected Products	
Product Series	Mercury(+) XU* and Mars XU* modules
Affected Product Model	All product models
Affected Parts	All parts
Replacement Parts	N/A

Type of PCN, Description and expected impact for customers	
Type of PCN	<p>A – Post on Enclustra Website</p> <p><input type="checkbox"/> Removal of Product Model (if no customer is affected)</p> <p><input type="checkbox"/> Minor Revision without impact on customer</p> <p><input type="checkbox"/> Other, specify:</p> <p>B – Inform current customers</p> <p><input type="checkbox"/> Increase of minimum order quantity (MOQ)</p> <p><input type="checkbox"/> Status Changes to end-of-life (EOL)</p> <p><input type="checkbox"/> Removal of Product Model (if customer is affected)</p> <p><input type="checkbox"/> Major Revision roll-out</p> <p><input type="checkbox"/> Minor Revision with impact for customer</p> <p><input checked="" type="checkbox"/> Other, specify: release of a new reference design version</p> <p>C – Inform all affected customers</p> <p><input type="checkbox"/> Security related bug / Major non-conformance to specification</p> <p><input type="checkbox"/> Other, specify:</p>
Description of change	<p>For the specified product series, a new reference design version (Vivado, Vitis) has been released to keep up with the Xilinx tool releases.</p> <p>As the last release took place approximately 2-3 years ago, various processing system (PS) configuration settings, such as memory parameters, have been changed. It has been observed that in certain conditions the older PS configuration may cause inconsistencies. It is therefore strongly recommended to perform an update of the SoC design to match the newest PS configuration settings from the reference designs.</p>
Reason for change	Update to newest Xilinx tools and consolidation of best practices.
Expected impact on customer	It has been observed that in certain conditions the older PS configuration may cause inconsistencies. It is therefore strongly recommended to perform an update of the SoC design to match the newest PS configuration settings from the reference designs.
Forecasted key milestones	None

Details of change	
Details	All new reference designs are available under: https://github.com/enclustra .

Contact	
Details	<p>If you have any questions or require additional information, please contact info@enclustra.com.</p> <p>Web: www.enclustra.com</p>